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Primary Textbook:

Teacher:

# 1 Chapter 1 - Introduction

1. Computer systems are viewed from a bottom up approach of how transistors run basic programs and a top down approach of how complicated programs are converted into simple logic commands
  - (a) This is used to understand commands a computer does well and does badly to program more efficiently, and to understand how changes in technology change the speed of computing
  - (b) C is used due to being high level enough to write large programs, but low level enough to allow direct modification of bits
2. The concept of abstraction is central to computer science, focusing on a higher level, rather than the component ideas to save time and mental effort, assuming the details work
  - (a) On the other hand, it must go in turn with deconstruction, breaking down an abstract idea into more concert sub-ideas, the opposite of abstraction, in case there is a problem
3. The concept of viewing hardware and software as joint components of a single system, which must both be taken into account to design either, is another central idea, to design the most effective components
4. Processors/CPU's are the primary unit of a computer, used to direct the processing of information and perform the calculations required to process it, though there are other components to make use easier
  - (a) Originally, they were made of large boards covered in integrated circuit packages, but now are just a single silicon microprocessor chip with millions of transistors
5. The only limitations of computers are time and amount of memory, but otherwise all computers can do the same tasks, though not at the same pace
  - (a) This is due to computers being universal computing devices, as digital machines which could be increased in precision unlike analog/physical machines, but which were not made for individual tasks
  - (b) Turing in 1937 proposed the Turing machine, which would be able to carry out all computations of some type, and later began to define what computation is, abstracting tasks by a black box model, showing the task, input, and output, with no specification about how it is performed
    - i. Turing's thesis states that a Turing machine can do all computations, such that improvements to it do not change the amount of computations, making a universal Turing machine able to simulate all different Turing machines
  - (c) Computers/universal Turing machines are able to do any computations, due to being programmable
6. Computer problems must be converted into voltages to influence the flow of electrons which the computer is made of, made of a series of methods to allow carrying out of complex tasks
  - (a) The levels of transformation are the levels of choice to convert the problem into an electron flow for the computer, starting with the statement in a natural/human language, which has too much ambiguity to give directly to the computer
  - (b) The first transformation is to an algorithm, or a finite step by step procedure, with definiteness, or precisely stated, and effective compatibility, or able to be carried out by a computer,
    - i. There are many possible algorithms, depending on the number of steps allowed concurrently by the computer, with many different speeds and lengths

- (c) After, it is converted to a mechanical/programming language, specifically created to avoid ambiguity, often designed for specific purposes
  - i. High level languages are those far from the computer itself, often machine independent, such that they don't rely on the computer specifics, such as C
  - ii. Low level languages are specific to the computer, such that there is one for each computer generally, called assembly
- (d) The third level is conversion into the computers instruction set architecture (ISA), or the specification of interface between programs and the hardware, generally x86 on Intel processors
  - i. The ISA specifies the instructions and operations the computer can do, what inputs (operands) it requires, and the operand formats (data types) it is able to accept
  - ii. It also contains mechanisms for the computer to find operands, called addressing modes, the number of unique memory locations, and the number of bits in each location
  - iii. High level languages are converted to ISA format by a compiler, while low level languages are converted by an assembler
- (e) After, the ISA is transformed into an implementation, based on the microprocessor-specific microarchitecture
  - i. Unlike the ISA, which specifies what the computer can do, the microarchitecture specifies how the computer actually performs those tasks
- (f) The microarchitecture is made out of logic circuits, determining the trade-off of cost and efficiency during manufacturing, each of which is further made out of a specific type of circuit materials, with its own specifications

## 2 Chapter 2 - Bits, Data Types, and Operations

1. The movement of electrons is controlled by devices reacting to the presence of voltage, rather than the amount for simplicity
  - (a) These voltages are signified by binary digits/bits, 1 for voltage near the maximum, and 0 for voltage near zero (rounding due to device variation)
  - (b) Bits are then combined to get a large number of distinct values
2. Data types are representations of values in which operations are encoded within the data types, mainly using 2's complement integers, ASCII codes, and floating points
  - (a) Unsigned integers are used to identify locations and counts, represented by positional binary representation, ranging from 0 to  $2^k - 1$  for k binary digits
  - (b) Signed integers are used for arithmetic, with a leading 0 to signify positive, from 0 to  $2^{k-1} - 1$  for k binary digits, using different systems for negative
    - i. The signed magnitude system uses a leading 1 for a negative value, with a leading 1 on 0 signifying -0
    - ii. The 1's complement system uses a leading 1 for negative values, signifying it is equivalent to switching every other binary digit in the value (complement) to get the magnitude
    - iii. The 2's complement system is the standard data type, equivalent to the 1's complement - 1 for each negative value, found to be easiest to design hardware to do arithmetic on

3. Computers generally use an arithmetic and logic unit (ALU) for addition, converting two inputs to one output of the sum, performing it in the same column method with carrying as standard addition, without actually determining the values, leading to the 2's complement system
  - (a) Thus, it is necessary for the sum of a number and its inverse to equal 0, ignoring all extra digits, such that only the correct number of digits is kept, and it is necessary for the sequence to be equal to one added, such that it works for results in the range, providing the advantage of the complements
  - (b) The advantage of 2's complement over 1's complement is making full use of the maximum amount of digits, rather than having two versions of 0