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Subject: System Programming and Operating Systems

Topic: Assignment 1 (Theory)



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Questions:

- Write algorithm for Pass 1 of two pass assembler.
- Explain advanced assembler directives with examples.

Answers:

- The algorithm for pass -I of two pass assembler is as follows:

1. $LC = 0$ (default value)

$littab_ptr = 1;$

$pooltab_ptr = 1;$

$POOLTAB[1] : first := 1 : POOLTAB[1] \# literals := 0$

2. While the next statement is not an ENP statement:

- i. If a symbol is present in label field then
 $th3_label = \text{symbol in label field}$

Make an entry ($th3_label < LC >, -$) in SYMTAB.

- ii. If an LORG statement then

- a. If $POOLTAB[pooltab_ptr] \# literals > 0$ then
process the entire LITAB [$POOLTAB[pooltab_ptr]$ first].

LITAB [$littab_ptr - 1$] to allocate memory to the literal, put address of allocated memory area in the address field of the LITAB entry and update the address contained in location counter accordingly.

- b. $pooltab_ptr = pooltab_ptr + 1;$

- c. $POOLTAB[pooltab_ptr], first := littab_ptr;$



- POOLTAB [pooltab_ptr] # literals := 0;
- iii. IF a START or ORIGIN statement then
LC := value specified in operand field;
- iv. IF an EQU statement then,
a. this := value of <address specification>:
b. correct SYMTAB entry for this label to (this.label1, this.addr1)
- v. IF a declarative stmt then
a. Invoke routine whose id is mentioned in the mnemonic info field. This routine returns code & size.
b. IF a symbol is present in label field, correct the symbol entry for this.label1 to (this.label1, <LC>size).
c. LC := LC + size;
d. Generate IC for declaration statement.
- vi. IF an imperative statement then
a. code := machine opcode from mnemonic info of OPTAB.
b. LC := LC + instruction length from the mnemonic info field of OPTAB.
c. IF operand is a literal then
this_literal := literal in operand field:
if POOLTAB: [pooltab_ptr] # literals = 0 or
this_literal doesn't match any literal in
the range LITTAB [POOLTAB] [pooltab_ptr]
first --- LITTAB [littab_ptr] then:
LITTAB [littab_ptr] --, value := this_literal:
POOLTAB [pooltab_ptr] : # literals + 1:

$\text{litlab_ptr} := \text{litlab_ptr} + 1$
 else (i.e. operand is a symbol)
 $\text{this_entry} := \text{SYMTAB entry number of operand};$
 Generate IC for imperative statement

3. Processing of END statement:

- Perform actions (i) - (iii) of step 2(b)
- Generate IC for the END statement.

b.

1. ORIGIN (ORG):

- Used to indirectly assign value to symbols.
- When this statement is encountered during assembly of program, the assembler resets its location counter to the specified value.
- Normally, when an ORG without specified value is encountered, the previously saved location counter value is restored.
- syntax : $\text{ORIGIN } \langle \text{add spec} \rangle$
 where $\langle \text{add spec} \rangle$ is an $\langle \text{operand spec} \rangle$
 or $\langle \text{constant} \rangle$.
- eg: $\text{SYMBOL} : 6 \text{ byte}$
 $\text{VALUE} : 1 \text{ word}$
 $\text{FLAGS} : 2 \text{ bytes}$

2. EQU:

- One common use of EQU is to establish symbol name that can be used for improved readability in place of numeric values.
- Another use of EQU is in defining mnemonic names for registers.



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- eg: A EQU 0
 X EQU 1

- These statements cause the symbols A, X --- to be entered into SYMBOL with their corresponding values 0, 1.

3. LORG:

- It allows placing literals into a pool at some other location in the object program.
- Directive LORG creates literal pool that contains all of the literal operands used since previous LORG or the beginning of the program.
- Literals placed in a pool by LORG will not be repeated in the pool at the end of program.
- Assembler allocates memory to the literals of a literal pool. The pool contains all literals used in program since start of program or since the last LORG statement.

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