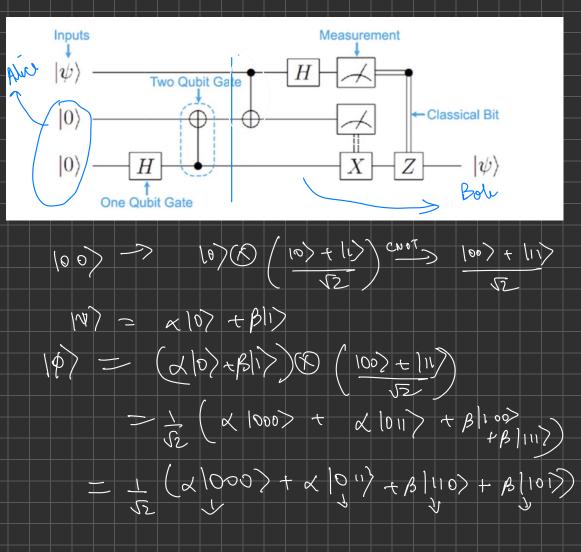
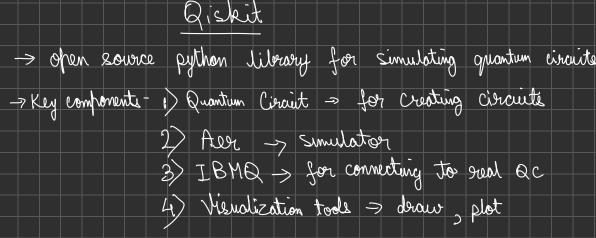


Quantum Teleportation





Week 1 Assignment: Quantum Teleportation

Objective

Design and implement a quantum circuit to simulate quantum teleportation of the state 0.6|0>+ 0.8| 1>. This assignment will introduce you to creating quantum circuits, running them on simulators and real quantum hardware, and analyzing the results.

Task Description

- 1. Circuit Design:
- Design a quantum circuit to implement the quantum teleportation protocol.
- Clearly annotate the circuit, showing all the steps: entanglement creation, Bell state measurement, and state reconstruction on the receiver's qubit.
 - 2. Implementation:
 - Use Qiskit to simulate the quantum teleportation process.
- Initialize the qubit in the state 0.6|0>+ 0.8|1> . Use Qiskit's state initialization methods to prepare this custom state.
 - 3. Execution:
 - Execute the circuit on both:
 - The Aer simulator (local simulation).
 - A real IBM Quantum Computer (e.g., IBMQ backend).
 - Ensure you configure the backend and job submission correctly.
 - 4. Analysis:
 - Measure the results after teleportation.
- Plot and compare the histograms of results from the simulator and the real quantum hardware.
- Analyze the results and provide an explanation for any observed discrepancies (e.g., noise in real quantum hardware).

Provide a clear and well-labeled quantum circuit diagram representing the quantum teleportation process. Submit the code used for the simulation and execution as a GitHub file. Include plots of the result histograms for both the Aer simulator and the IBMQ backend. Provide a brief explanation of the results, focusing on the correctness of the teleported state and the differences between simulated and hardware results.

General Guidelines

Make extensive use of the Qiskit documentation to thoroughly understand the functionality of each method. Avoid relying on AI tools such as ChatGPT for direct implementation, as the primary goal is to gain hands-on experience by independently exploring Qiskit's features and capabilities. Ensure that all code, diagrams, and analysis are original and created by you.

Submission deadline- 11:59 pm, 15th December

Good luck with the assignment!