LAB # 05
Parallel R, L, C Circuits



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REG NO: **21PWCSE1993** 

SECTION: "A"

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## **ASSESSMENT RUBRICS**

LAB REPORT ASSESSMENT									
	Criteria	Excellent	Average	Nil	Marks Obtained				
1.	Objectives of Lab	All objectives of lab are properly covered [Marks 1]	Objectives of lab are partially covered [Marks 0.5]	Objectives of lab are not shown [Marks 0]					
2.	Procedure	All experimental steps are shown. [Marks 2]	Some of the experimental steps are shown. [Marks 1]	Experimental steps not shown [Marks 0]					
3.	Demonstration of Concepts	The student demonstrated a clear understanding of the assignment concepts [Marks 2]	The student demonstrated a clear understanding of some of the assignment concepts [Marks 1]	The student failed to demonstrate a clear understanding of the assignment concepts [Marks 0]					
4.	Experimental Results	All experimental results are completely shown in form of table [Marks 3]	Experimental results are partially shown and some of the observations are missing [Marks 1.5]	No experimental results are shown [Marks 0]					
5.	conclusion	Conclusion of the lab is properly written [Marks 2]	Conclusion of the lab is partially written [Marks 1]	Conclusion of lab is not written [Marks 0]					
	Total Marks Obtained:								
	Instructor Signature:								

### Parallel R, L, C Circuits

## Objectives

This exercise examines the voltage and current relationships in parallel R, L, C networks. Of particular importance is the phase of the various components and how Kirchhoff's Current Law is extended for AC circuits. Both time domain and phasor plots of the currents are generated. A technique to measure current using a current sense resistor will also be explored.

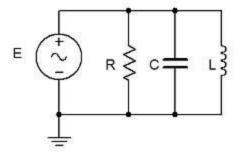
## **Theory Overview**

Recall that for resistors, the voltage is always in phase with the current, for capacitors the voltage always lags the current by 90 degrees, and for inductors the voltage always leads the current by 90 degrees. Because each element has a unique phase response between +90 and -90 degrees, a parallel combination of R, L and C components will yield a complex impedance with a phase angle between +90 and -90 degrees. Due to the phase response, Kirchhoff's Current law must be computed using vector (phasor) sums rather than simply relying on the magnitudes. Indeed, all computations of this nature, such as a current divider, must be computed using vectors.

### Equipment

- 1. AC Function Generator
- 2. Oscilloscope

#### Circuit Diagram



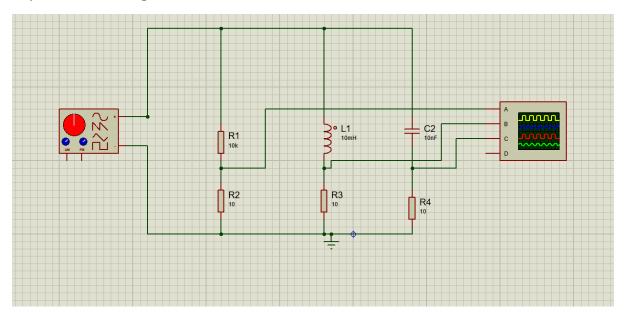
#### Components

- 10 nF actual:
- 1 kΩ actual: \_\_\_\_\_
- 10 Ω actual:

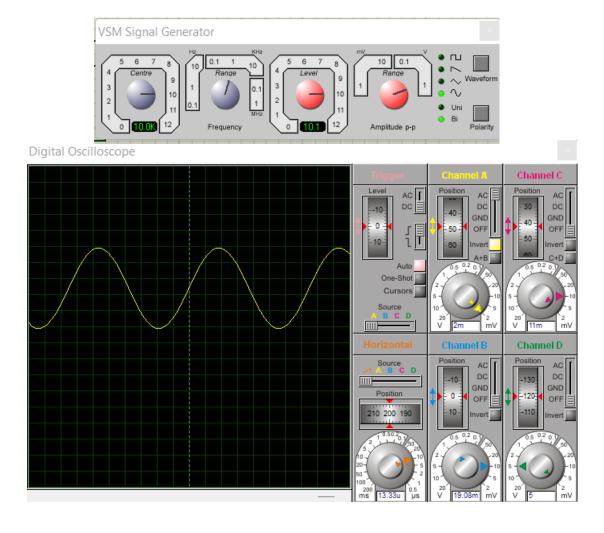
#### Procedure

- 1. Using Figure 1 with a 10 V p-p 10 kHz source, R=1k $\Omega$ , C=10nF and L=10mH, determine the theoretical capacitive reactance, inductive reactance and circuit impedance, and record the results in Table 1 (the experimental portion of this table will be filled out in step 5). Using the current divider rule, compute the currents in resistor(iR), inductor(iL) and capacitor(iC) and record them in Table 2.
- 2. Build the circuit of Figure 1 using R=1k $\Omega$ , L=10mH and C=10nF. A common method to measure current using the oscilloscope is to place a small current sense resistor in line with the current of interest. If the resistor is much smaller than the surrounding reactance it will have a minimal effect on the current. Because the voltage and current of the resistor are always in phase with each other, the relative phase of the current in question must be the same as that of the sensing resistor's voltage. Each of the three circuit currents will be measured separately and with respect to the source in order to determine relative phase. To measure the total current, place a  $10\Omega$  resistor between ground and the bottom connection of the parallel components. Set the generator to a 10 V p-p sine wave at 10 kHz.
- 3. Place probe1 across the generator and probe2 across the sense resistor. Measure the voltage across the sense resistor; calculate the corresponding total current via Ohm's Law and record in Table 2. Along with the magnitude, be sure to record the time deviation between the sense waveform and the input signal (from which the phase may be determined eventually).
- 4. Remove the sense resistor and place one  $10\Omega$  resistor between the capacitor and ground to serve as the capacitor current sense. Place a second  $10\Omega$  resistor between the resistor and ground to sense the resistor current, and a third  $10\Omega$  resistor between the inductor and ground for the inductor current. Leave probe one at the generator and move probe two across the sense resistor in the resistor branch. Repeat the process to obtain its current, recording the magnitude and phase angle in Table 2. In a similar way move probe2 so that it is first across the capacitor's sense resistor and then across the inductor sense resistor. Measure and record the appropriate values in Table 2.
- 5. Compute the deviations between the theoretical and experimental values of Table 2 and record the results in the final columns of Table 2. Based on the experimental values, determine the experimental Z, XL and XC values via Ohm's Law (XC =VC/IC, XL=VL/IL and XZ=Vin/iin) and record back in Table 1 along with the deviations.
- 6. Create a phasor plot showing iin, iC, iL and iR. Include both the time domain display and the phasor plot with the technical report.
- 7. Repeat the experiment for any values of C, L and R.

## Experimental Diagram:



## Outputs:



# Experimental results:

TABLE: 01

	Theoretical	Experimental	%Deviation
Хс	1592.3 Ω	1515.1 Ω	4.84833%
XL	628 Ω	657.89 Ω	4.75955%
Z	9953.4 Ω	9999.1 Ω	0.457041%
Θ	-5.523 Ω	-5.71 Ω	3.38584%

TABLE: 02

	Theoretical	Experimental	%Deviation
Ic	0.00628 A	0.0066 A	5.09554%
$I_{L}$	0.015 A	0.015 A	0.000%
$I_R$	0.001 A	0.001 A	0.000%
Is	0.0224 A	0.0228 A	1.78571%