

Homework 2 Solution

6. The 'ADD 3' block takes a 3-bit I/P & gives (14)
 a 3-bit O/P. The value at the O/P is greater than
 I/P by 3. If O/P is too big to be represented, then
 all the O/P bits are set to 1.



- Construct the truth table
- Simplify b_0, b_1, b_2 using K-Map.
- Draw the ckt diagram using AND, OR, NOT gates

Ans:

a) TRUTH TABLE :

a2	a1	a0	b2	b1	b0
0	0	0	0	1	1 ✓
0	0	1	1	0	0 ✓
0	1	0	1	0	1 ✓
0	1	1	1	1	0 ✓
1	0	0	1	1	1 ✓
1	0	1	1	1	1 ✓
1	1	0	1	1	1 ✓
1	1	1	1	1	1 ✓

b) K-Maps :

b₀ :

a2 \ a1 a0	00	01	10	11
0	1	0	0	1 ✓
1	1	1	1	1 ✓

$$\Rightarrow b_0 = a_0' + a_2$$

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$b_1 :$

	$a_1 a_0$	$a_1' a_0'$	$a_1' a_0$	$a_1 a_0'$
a_2'	1	0	1	0
a_2	1	1	1	1

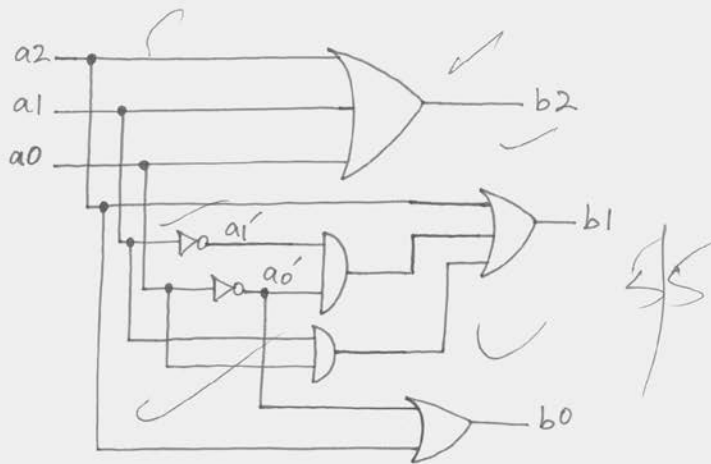
$\Rightarrow b_1 = a_2 + a_1' a_0' + a_1 a_0$

$b_2 :$

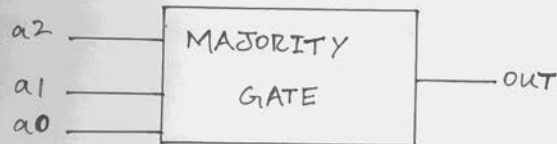
	$a_1 a_0$	$a_1' a_0'$	$a_1' a_0$	$a_1 a_0'$
a_2'	0	1	1	1
a_2	1	1	1	1

$\Rightarrow b_2 = a_2 + a_0 + a_1$

c) CIRCUIT DIAGRAM :



7. The 'MAJORITY GATE' block takes a 3-bit I/P & produces a 1-bit O/P. O/P is 1 if majority of the I/P's are 1's. The O/P is 0 otherwise. (15)



- Build the truth table
- Simplify out using K-Map
- Draw the ckt diagram using NAND Gates only.

Ans:

a) TRUTH TABLE:

a2	a1	a0	OUT
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1

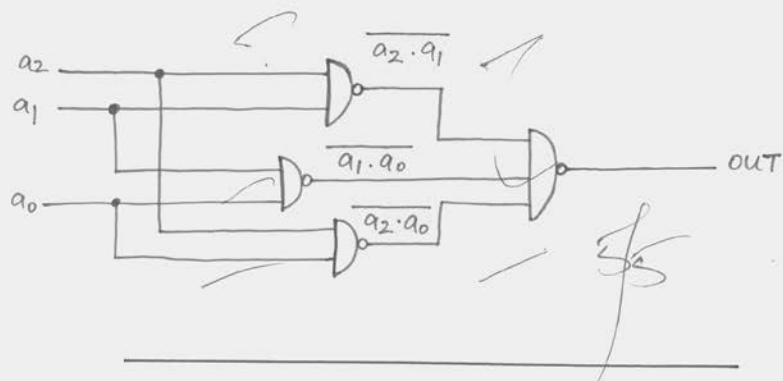
b) K-Map:

a2 \ a1a0	a1a0	a1a0	a1a0	a1a0
a2	0	1	1	0
a2	0	1	1	1

$$\Rightarrow \text{OUT} = a_2a_0 + a_1a_0 + a_2a_1$$

(29)

c) CIRCUIT DIAGRAM:
(USING NAND GATES)



8. There is a committee with 3 people. Each person votes either YES or No for a proposal. If at least 2 people vote YES, the proposal is passed. Design a ckt \rightarrow whether a proposal passes. (16)

ANS:

Let In Voting, YES $\rightarrow 1$
No $\rightarrow 0$

for proposal, PASSED $\rightarrow 1$
NOT PASSED $\rightarrow 0$

TRUTH TABLE:

People			Proposal
A	B	C	$F(A,B,C)$
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	1

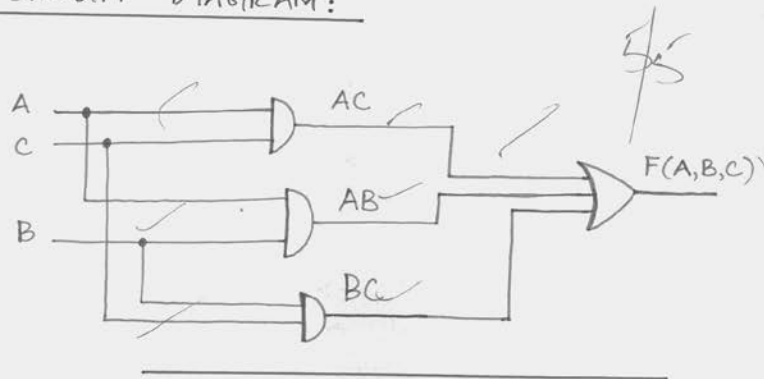
K-MAP:

A	BC			
	$B'C'$	$B'C$	BC	BC'
A'	0	0	1	0
A	0	1	1	1

$$\Rightarrow F(A,B,C) = AC + AB + BC$$

(31)

CIRCUIT DIAGRAM:



9. Sometimes lights are controlled by more than one switch, like in stairs. The downstairs switch is 'x' & upstairs switch is 'y'. The lights should get ON when both 'x' and 'y' are ON or both are OFF. Draw the logic circuit of $L(x,y)$.

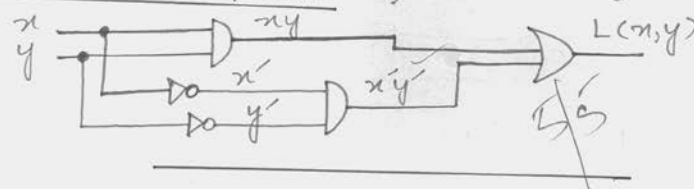
ANS: Let for Switches, ON $\rightarrow 1$ ✓
OFF $\rightarrow 0$ ✓
for Light, ON $\rightarrow 1$ ✓
OFF $\rightarrow 0$ ✓

TRUTH TABLE:

SWITCHES		LIGHT $L(x,y)$
x	y	
0	0 ✓	1 ✓
0	1 ✓	0 ✓
1	0 ✓	0 ✓
1	1 ✓	1 ✓

$$\Rightarrow L(x,y) = x'y' + xy$$

CIRCUIT DIAGRAM:



(17)

10. Design a logic ckt that accepts a 3-bit No. & generates an 8-bit binary No. equal to the square of I/P No.

ANS:

TRUTH TABLE:

S. No.	a_0	a_1	a_2	b_0	b_1	b_2	b_3	b_4	b_5
0	0	0	0	0	0	0	0	0	0
1	0	0	1	0	0	0	0	0	1
2	0	1	0	0	0	0	1	0	0
3	0	1	1	0	0	1	0	0	1
4	1	0	0	0	1	0	0	0	0
5	1	0	1	0	1	1	0	0	1
6	1	1	0	1	0	0	1	0	0
7	1	1	1	1	1	0	0	0	1

$$b_0 : \Rightarrow b_0 = a_0 a_1 a_2' + a_0 a_1 a_2 \Rightarrow b_0 = a_0 a_1$$

$$\begin{aligned} b_1 : \Rightarrow b_1 &= a_0 a_1' a_2' + a_0 a_1' a_2 + a_0 a_1 a_2 \\ &= a_0 (a_1' a_2' + a_1' a_2 + a_1 a_2) \\ &= a_0 (a_1' (a_2' + a_2) + a_2 (a_1' + a_1)) \\ &= a_0 (a_1' + a_2) \Rightarrow b_1 = a_0 a_1' + a_0 a_2 \end{aligned}$$

$$b_2 : \Rightarrow b_2 = a_0' a_1 a_2 + a_0 a_1' a_2$$

$$b_3 : \Rightarrow b_3 = a_0' a_1 a_2' + a_0 a_1 a_2' \Rightarrow b_3 = a_1 a_2'$$

$$b_4 : \Rightarrow b_4 = 0$$

$$b_5 : \begin{array}{c|cccc} & a_1' a_2' & a_1' a_2 & a_1 a_2 & a_1 a_2' \\ \hline a_0' & 0 & 1 & 1 & 0 \\ a_0 & 0 & 1 & 1 & 0 \end{array}$$

$$\Rightarrow b_5 = a_2$$

(34)

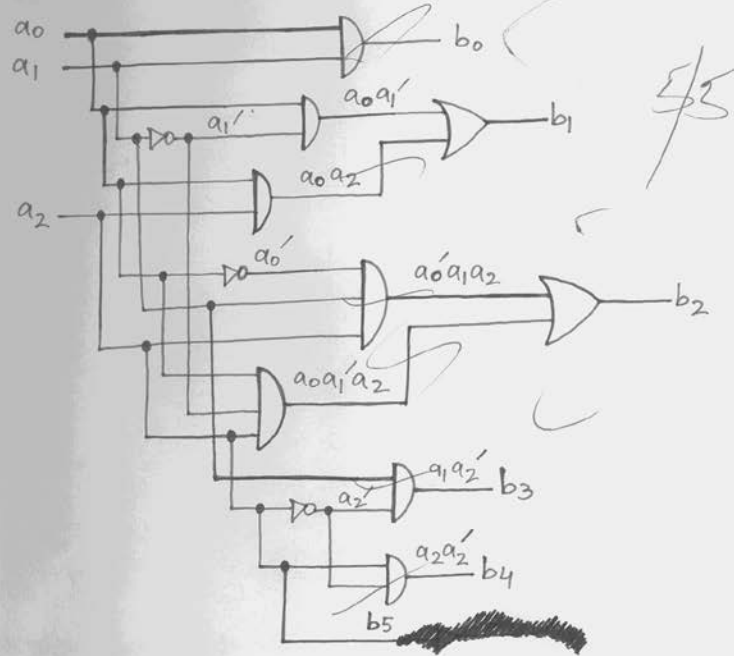
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(18)

CIRCUIT DIAGRAM:



QUESTION - 11

(36)

12. Design a combinational circuit whose I/p is a 4-bit No. and whose o/p is the 2's complement of I/P No.

ANS:

TRUTH TABLE :

a	b	c	d	F ₁	F ₂	F ₃	F ₄
0	0	0	0	0	0	0	0 ✓
0	0	0	1	1	1	1	1 ✓
0	0	1	0	1	1	1	0 ✓
0	0	1	1	1	1	0	1 ✓
0	1	0	0	1	1	0	0 ✓
0	1	0	1	1	0	1	1 ✓
0	1	1	0	1	0	1	0 ✓
0	1	1	1	1	0	0	1 ✓
1	0	0	0	1	0	0	0 ✓
1	0	0	1	0	1	1	1 ✓
1	0	1	0	0	1	1	0 ✓
1	0	1	1	0	1	0	1 ✓
1	1	0	0	0	1	0	0 ✓
1	1	0	1	0	0	1	1 ✓
1	1	1	0	0	0	1	0 ✓
1	1	1	1	0	0	0	1 ✓

(19)

 $F_1 \Rightarrow$

$ab \backslash cd$					
		$c'd'$	$c'd$	cd	cd'
$a'b'$		0	1	1	1
$a'b$		1	1	1	1
ab		0	0	0	0
ab'		1	0	0	0

$$\Rightarrow F_1 = a'd + a'c + a'b + ab'c'd'$$

 $F_2 \Rightarrow$

$ab \backslash cd$					
		$c'd'$	$c'd$	cd	cd'
$a'b'$		0	1	1	1
$a'b$		1	0	0	0
ab'		1	0	0	0
ab		0	1	1	1

$$\Rightarrow F_2 = b'd + b'c + bc'd'$$

 $F_3 \Rightarrow$

$ab \backslash cd$					
		$c'd'$	$c'd$	cd	cd'
$a'b'$		0	1	0	1
$a'b$		0	1	0	1
ab		0	1	0	1
ab'		0	1	0	1

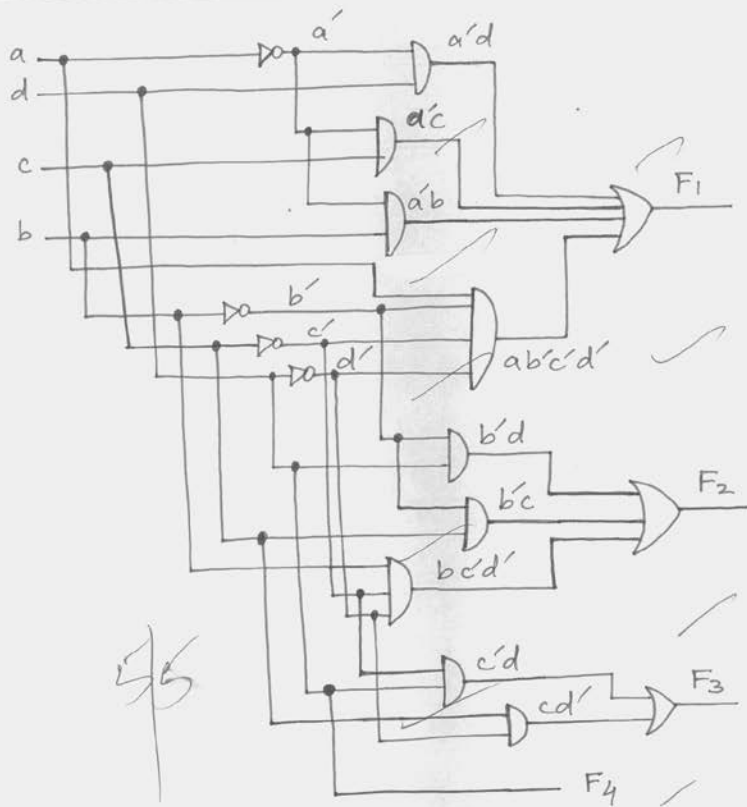
$$\Rightarrow F_3 = c'd + cd'$$

 $F_4 \Rightarrow$

$$F_4 = d \quad \checkmark \quad (\text{from the truth table})$$

(20)

CIRCUIT DIAGRAM :



13. You as a System Engineer is given a project to construct a security system for a sensitive military zone. The conditions are as follows:

(20)

a) If it is night and door is opened or laser light is disturbed, alarm should go on.

b) In day time military is on duty, so there is no risk.

Develop the logic circuit to meet the above requirements.

ANS:

Let Time (T) — Night → 0 ✓
Day → 1 ✓

Door (D) — Closed → 1 ✓
Open → 0 ✓

Laser (L) — Disturbed → 0 ✓
Not Disturbed → 1 ✓

Alarm (A) — ON → 1 ✓
OFF → 0 ✓

TRUTH TABLE:

T	D	L	A
0	0	0	1 ✓
0	0	1	1 ✓
0	1	0	1 ✓
0	1	1	0 ✓
1	0	0	0 ✓
1	0	1	0 ✓
1	1	0	0 ✓
1	1	1	0 ✓

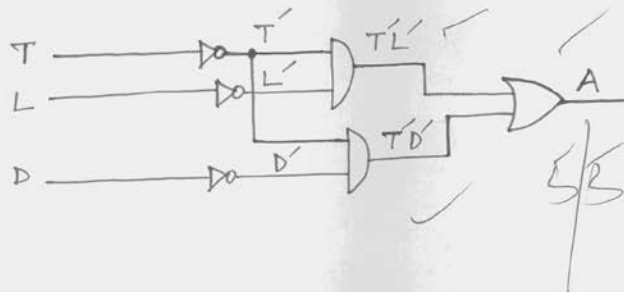
(10)

$$\Rightarrow A = T'D'L' + T'D'L + TDL'$$

$\backslash D$	$D'L'$	$D'L$	DL	DL'
T'	1	1	0	1
T	0	0	0	0

$$\Rightarrow A = T'L' + T'D'$$

CIRCUIT DIAGRAM:



(21)

14. Design a logic circuit to produce a HIGH o/p, only if the I/p, represented by a 4-bit binary No. is greater than 12 or less than 3. First develop the truth table & draw the logic diagram.

ANS:

Let High o/p $\rightarrow 1$
 Low o/p $\rightarrow 0$ } $F(a, b, c, d)$

TRUTH TABLE:

S.No.	a	b	c	d	$F(a, b, c, d)$
0	0	0	0	0	1✓
1	0	0	0	1	1✓
2	0	0	1	0	1✓
3	0	0	1	1	0
4	0	1	0	0	0✓
5	0	1	0	1	0
6	0	1	1	0	0
7	0	1	1	1	0✓
8	1	0	0	0	0
9	1	0	0	1	0✓
10	1	0	1	0	0
11	1	0	1	1	0✓
12	1	1	0	0	0
13	1	1	0	1	1✓
14	1	1	1	0	1✓
15	1	1	1	1	1✓

(12)

K-Map:

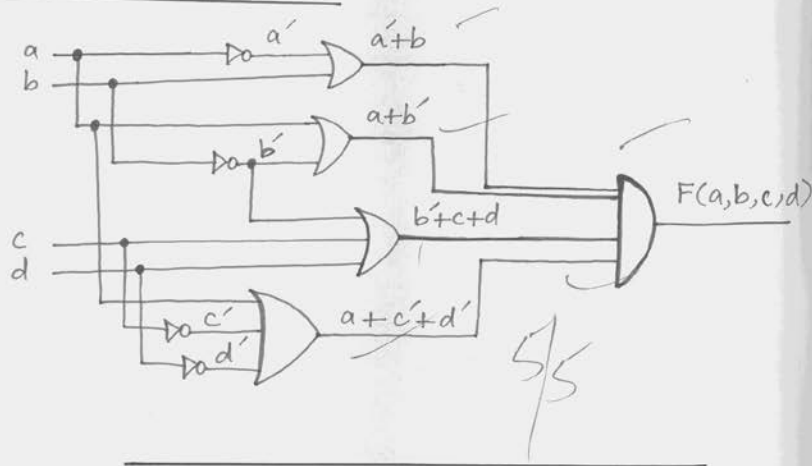
		cd			
		c'd'	c'd	cd	cd'
ab	a'b'	1	1	0	1
	a'b	0	0	0	0
	ab	0	1	1	1
	ab'	0	0	0	0

5/5

$$\Rightarrow F' = ab' + a'b + bc'd' + a'cd$$

$$\Rightarrow F = (a'+b)(a+b')(b'+c+d)(a+c'+d')$$

CIRCUIT DIAGRAM:



15. Construct a logic ckt to meet the following requirements:

(22)

A battery-powered lamp in a room is to be operated from 2 switches, one at the back of one at the front door. The lamp is ON if front switch is ON & back switch is OFF, or if the front is OFF and the back is ON. The lamp is OFF if both switches are OFF or both are ON. Let a high o/p represents the ON & low represents the OFF condition.

ANS:

Let for

Back door S/W $\Rightarrow B$ $\begin{cases} \text{ON} \rightarrow 1 \\ \text{OFF} \rightarrow 0 \end{cases}$

Front door S/W $\Rightarrow F$ $\begin{cases} \text{ON} \rightarrow 1 \\ \text{OFF} \rightarrow 0 \end{cases}$

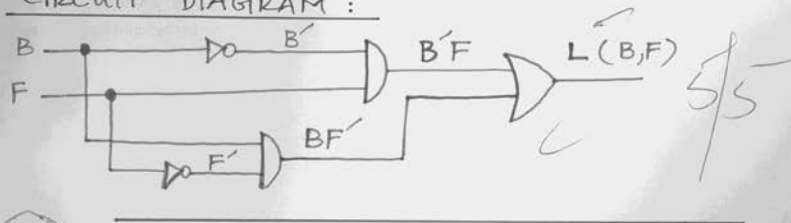
Lamp $\Rightarrow L$ $\begin{cases} \text{ON} \rightarrow 1 \\ \text{OFF} \rightarrow 0 \end{cases}$

TRUTH TABLE:

B	F	L(B,F)
0	0	0
0	1	1
1	0	1
1	1	0

$$\Rightarrow L(B,F) = B'F + BF'$$

CIRCUIT DIAGRAM:



(44)

16. Design a logic circuit that produces '1' only when the No. of 1's in a set of 3 I/p variables A, B, C is even. (Even Parity checker)

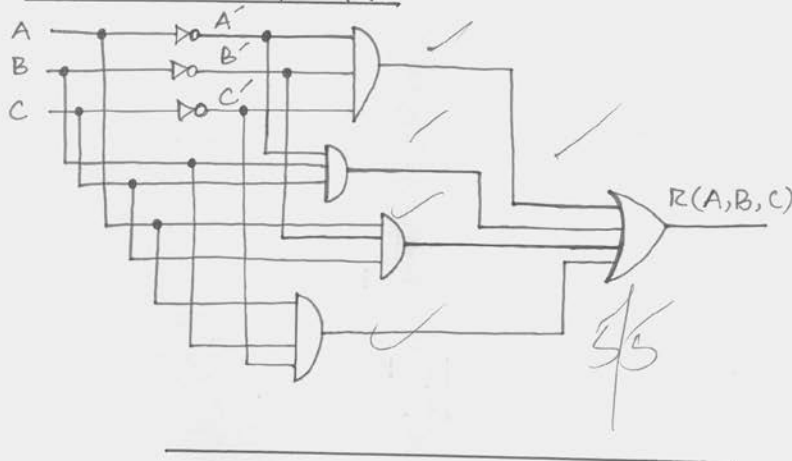
ANS:

TRUTH TABLE:

A	B	C	R(A,B,C)
0	0	0 ✓	1 ✓
0	0	1	0
0	1	0 ✓	1 ✓
0	1	1	0
1	0	0 ✓	1 ✓
1	0	1	0
1	1	0 ✓	1 ✓
1	1	1	0

$$\Rightarrow R(A,B,C) = A'B'C' + \bar{A}BC + AB\bar{C} + ABC'$$

CIRCUIT DIAGRAM:



17. obtain the simplified expressions in SOP: (23)

(a). $F(x, y, z) = \sum m(2, 3, 6, 7)$

Let $F = m_2 + m_3 + m_6 + m_7$ ✓

$\Rightarrow F = x'y'z' + x'yz' + xyz' + xyz$

using K-Map:

$\begin{matrix} yz \\ x \end{matrix}$	$y'z'$	$y'z$	yz	yz'
x'	0	0	1	1
x	0	0	1	1

$\Rightarrow F(x, y, z) = y$ ✓

(b). $F(A, B, C, D) = \sum m(7, 13, 14, 15)$

Let $F = m_7 + m_{13} + m_{14} + m_{15}$

$\Rightarrow F = A'BCD + ABC'D + ABCD' + ABCD$

using K-Map:

$\begin{matrix} CD \\ AB \end{matrix}$	$C'D'$	$C'D$	CD	CD'
$A'B'$	0	0	0	0
$A'B$	0	0	1	0
AB	0	1	1	1
AB'	0	0	0	0

$\Rightarrow F(A, B, C, D) = ABD + ABC + BCD$

(c). $F(A, B, C, D) = \sum m(4, 6, 7, 15)$

Let $F = m_4 + m_6 + m_7 + m_{15}$

$\Rightarrow F = A'BC'D + A'BCD' + A'BCD + ABCD$

using K-Map:

(46)

AB \ CD	C'D'	C'D	CD	CD'
A'B'	0 ✓	0	0 ✓	0 ✓
A'B	1	0	1	1
AB	0	0	1	0
AB'	0	0	0	0

$$\Rightarrow F(A, B, C, D) = A'B'D' + BCD$$

(d). $F(w, x, y, z) = \sum m(2, 3, 12, 13, 14, 15)$

Let $F = m_2 + m_3 + m_{12} + m_{13} + m_{14} + m_{15}$

$$\Rightarrow F = w'x'y'z' + w'x'yz' + wxy'z' + wxy'z + wxyz' + wxyz$$

using K-Map:

wx \ yz	y'z'	y'z	yz	yz'
w'x'	0	0	1	1
w'x	0	0	0	0
wx	1	1	1	1
wx'	0	0	0	0

$$\Rightarrow F(w, x, y, z) = wx + w'x'y$$

(e). $F(x, y, z) = xy + x'y'z' + x'yz'$

Let $F = xyz + xyz' + x'y'z' + x'yz'$

using K-Map:

x \ yz	y'z'	y'z	yz	yz'
x'	1	0	0	1
x	0	0	1	1

$$\Rightarrow F(x, y, z) = x'z' + xy$$

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(f) $F(A, B, C) = A'B + BC + B'C'$

(24)

Let $F = A'BC + A'BC' + ABC' + A'BC' + AB'C' + A'BC'$

Using K-Map:

$\backslash BC$	$B'C'$	$B'C$	BC	BC'
A'	1	0	1	1
A	1	0	0	1

$\Rightarrow F(A, B, C) = C' + A'B$

(g). $F(a, b, c) = a'b' + bc + a'bc'$

Let $F = a'b'c + a'b'c' + abc + a'bc + a'bc'$

Using K-Map:

$\backslash bc$	$b'c'$	$b'c$	bc	bc'
a'	1	1	1	1
a	0	0	1	0

$\Rightarrow F(a, b, c) = a' + bc$

(h). $F(x, y, z) = xy'z + xyz' + x'y'z + xyz$

Let $F = xy'z + xyz' + x'y'z + xyz$

using K-Map:

$\backslash yz$	$y'z'$	$y'z$	yz	yz'
x'	0	0	1	0
x	0	1	1	1

$\Rightarrow F(x, y, z) = yz + xy + xz$

18. obtain the simplified expressions in POS:

a). $F(x, y, z) = \pi M(0, 1, 4, 5)$

Let $F = M_0 M_1 M_4 M_5$

Using K-Map:

$\begin{matrix} yz \\ x \end{matrix}$	$y'z'$	$y'z$	yz	yz'
x'	0	0	1	1
x	0	0	1	1

$\Rightarrow F'(x, y, z) = y' \Rightarrow F(x, y, z) = y$

b). $F(A, B, C, D) = \pi M(0, 1, 2, 3, 4, 10, 11)$

Let $F = M_0 M_1 M_2 M_3 M_4 M_{10} M_{11}$

Using K-Map:

$\begin{matrix} CD \\ AB \end{matrix}$	$C'D'$	$C'D$	CD	CD'
$A'B'$	0	0	0	0
$A'B$	0	1	1	1
AB	1	1	1	1
AB'	1	1	0	0

$\Rightarrow F'(A, B, C, D) = A'C'D' + A'B' + B'C$

So, $\Rightarrow F(A, B, C, D) = (A+C+D)(A+B)(B+C')$

c). $F(w, x, y, z) = \pi M(1, 3, 5, 7, 13, 15)$

Let $F = M_1 M_3 M_5 M_7 M_{13} M_{15}$

Now, using K-Map:

(25)

$w \backslash yz$	$y'z'$	$y'z$	yz	yz'
$w'x'$	1	0	0	1
$w'x$	1	0	0	1
wx	1	0	0	1
wx'	1	1	1	1

$$\Rightarrow F(w, x, y, z) = w'z + xz$$

$$\text{So, } F(w, x, y, z) = (w+z')(x'+z')$$

19.

Obtain the simplified expressions in (1) SOP,
(2) POS

$$a). F(x, y, z) = x'z' + y'z' + yz' + xyz$$

$$\text{Let } F = x'y'z' + x'y'z + xy'z' + x'y'z' + xyz' + x'y'z' + xyz$$

Using K-Map:

$x \backslash yz$	$y'z'$	$y'z$	yz	yz'
x'	1	0	0	1
x	1	0	1	1

$$(1) \Rightarrow F = z' + xy$$

$$(2) \Rightarrow F' = y'z + x'z'$$

$$\Rightarrow F = (y+z')(x+z')$$

(50)

b). $F(A, B, C, D) = (A+B+D)(A'+B+D)(C+D)(C'D')$

Let $F = (A+B'+C+D)(A+B'+C'+D)(A'+B+C+D)$
 $(A'+B+C'+D)(A+B+C+D)(A'+B+C+D)$
 $(A+B'+C+D)(A'+B'+C+D)(A+B+C'+D)$
 $(A'+B+C'+D')(A+B'+C'+D)(A'+B+C'+D)$

Using K-Map:

AB \ CD	C'D	C'D	CD	CD'
A'B'	0	1	0	1
A'B	0	1	0	0
AB	0	1	1	1
AB'	0	1	0	0

(1) $\Rightarrow F = C'D + A'B'CD + ABCD$ --- SOP

(2) $\Rightarrow F' = C'D + CD + A'BD' + ABD'$

$\Rightarrow F = (C+D)(C'+D')(A+B+D)(A'+B+D)$ --- POS

c). $F(A, B, C, D) = (A'+B'+D')(A+B'+C')$
 $(A'+B+D')(B+C'+D')$

Let $F = (A'+B'+C+D')(A'+B'+C'+D')$
 $(A+B'+C'+D)(A+B'+C'+D')$
 $(A'+B+C+D')(A'+B+C'+D')$
 $(A+B+C'+D')(A'+B+C'+D')$

Using K-Map:

AB \ CD	C'D	C'D	CD	CD'
A'B'	1	1	0	1
A'B	1	1	0	0
AB	1	0	0	1
AB'	1	0	0	1

(25)

(1) $\Rightarrow F = B'D' + A'C' + AD'$ --- SOP (26)

(2) $\Rightarrow F' = CD + AD + A'BC$ ✓
 $\Rightarrow F = (C'D')(A'D')(A+B+C')$ --- POS

d). $F(A,B,C,D) = (A'+B'+D)(A'+D')(A+B+D')$
 $(A+B'+C+D)$

Let $F = (A'+B'+C+D)(A'+B'+C'+D)$
 $(A'+B+C+D')(A'+B+C'+D')$
 $(A'+B'+C+D')(A'+B'+C'+D)$
 $(A+B+C+D')(A+B+C'+D')$
 $(A+B'+C+D)$

using K-Map:

AB \ CD	C'D'	C'D	CD	CD'
A'B'	1	0	0	1
A'B	0	1	1	1
AB	0	0	0	0
AB'	1	0	0	1

(1) $\Rightarrow F = B'D' + A'BC + A'BD$ --- SOP

(2) $\Rightarrow F' = BC'D' + AB + B'D$ ✓
 $\Rightarrow F = (B'+C+D)(A'+B')(B+D')$ --- POS

e). $F(v,w,x,y) = wy + vw + vw'x + v'w + v'wy'$

Let $F = vw'y + v'wy + vw'x + vw'x'$
 $+ v'wx + v'wx' + v'wx'y + v'wx'y'$
 $+ v'wx'y + v'wx'y'$

$\Rightarrow F = vw'xy + vw'x'y + v'wx'y + v'wx'y'$
 $+ v'wx'y + v'wx'y' + v'wx'y + v'wx'y'$
 $+ v'wx'y + v'wx'y' + v'wx'y + v'wx'y'$
 $+ v'wx'y + v'wx'y' + v'wx'y + v'wx'y'$

(52)

using K-Map:

vw	xy				
		$x'y'$	$x'y$	xy	xy'
$v'w'$		1	1	1	1
$v'w$		1	1	1	1
vw		0	0	0	0
vw'		1	1	1	1

(1) $\Rightarrow F = w' + v'$ --- SOP

(2) $\Rightarrow F' = vw$

So, $\Rightarrow F = v' + w'$ --- POS

20. Simplify the following functions using the don't care conditions 'd' in (1) SOP (2) POS

a). $F(A,B,C,D) = A'B'D' + A'CD + A'BC$

$d = A'BC'D + ACD + AB'D'$

Let $F = A'B'CD' + A'B'C'D' + A'BCD + A'BC'D$
 $+ A'BCD + A'BCD'$

$\& d = A'BC'D + ABCD + AB'CD + AB'CD' + ABC'D'$

AB	CD				
		$C'D'$	$C'D$	CD	CD'
$A'B'$		1	0	1	1
$A'B$		0	X	1	1
AB		0	0	X	0
AB'		X	0	X	X

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$$(1) \Rightarrow F = A'C + B'D \quad \text{--- SOP} \quad \checkmark$$

$$(2) \Rightarrow F' = C'D + BC' + A \quad \checkmark$$

$$\text{So, } \Rightarrow F = (C+D')(B'+C)A' \quad \text{--- POS} \quad \checkmark$$

$$b). F(w, x, y, z) = w'(xy + x'y' + xyz) + xz'(y+w)$$

$$d = w'x(y'z + yz') + wyz$$

$$\text{Let } F = w'x'y + w'x'y' + w'xyz + x'yz' + x'wz'$$

$$\Rightarrow F = w'x'yz + w'x'yz' + w'x'y'z + w'x'y'z' + w'xyz + w'xyz' + w'x'y'z + w'x'y'z' + w'xyz + w'xyz'$$

$$\text{eg } d = w'x'y'z + w'x'yz' + w'xyz + w'x'yz$$

using K-Map:

$w \backslash x$	$y'z'$	$y'z$	yz	yz'
$w'x'$	1	1	1	1
$w'x$	0	X	1	X
wx	0	0	X	0
wx'	1	0	X	1

$$(1) \Rightarrow F = x'z' + w'z \quad \checkmark \quad \text{--- SOP}$$

$$(2) \Rightarrow F' = xz' + wz \quad \checkmark$$

$$\text{So, } \Rightarrow F = (x' + z)(w' + z') \quad \text{--- POS} \quad \checkmark$$

$$c). F(A, B, C, D) = B'C'D' + BCD' + ABCD'$$

$$d = B'C'D' + A'BC'D$$

$$\text{Let } F = AB'C'D' + A'BC'D' + ABCD' + A'BCD' + ABCD'$$

$$\text{eg } d = AB'C'D' + A'BC'D' + A'BC'D$$

(54)

Using K-Map:

AB \ CD	C'D'	C'D	CD	CD'
A'B'	1	0	0	X
A'B	0	X	0	1
AB	0	0	0	1
AB'	1	0	0	X

(1) $\Rightarrow F = B'D' + CD'$ --- SOP

(2) $\Rightarrow F' = BC' + D$

So, $\Rightarrow F = D'(B'+C)$ --- POS

d). $F(x, y, z) = y' + x'z'$, $d = yz + xy$

Let $F = xy' + x'zy + x'z'y' + x'y'$

$\Rightarrow F = xy'z + xy'z' + x'y'z' + x'y'z'$
 $+ x'yz + x'yz'$

q $d = xyz + x'yz + xyz + xyz'$

using K-Map:

x \ yz	y'z'	y'z	yz	yz'
x'	1	1	X	1
x	1	1	X	X

(1) $\Rightarrow F = 1$ --- SOP

(2) $\Rightarrow F' = 0$

$\Rightarrow F = 1$ --- POS

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