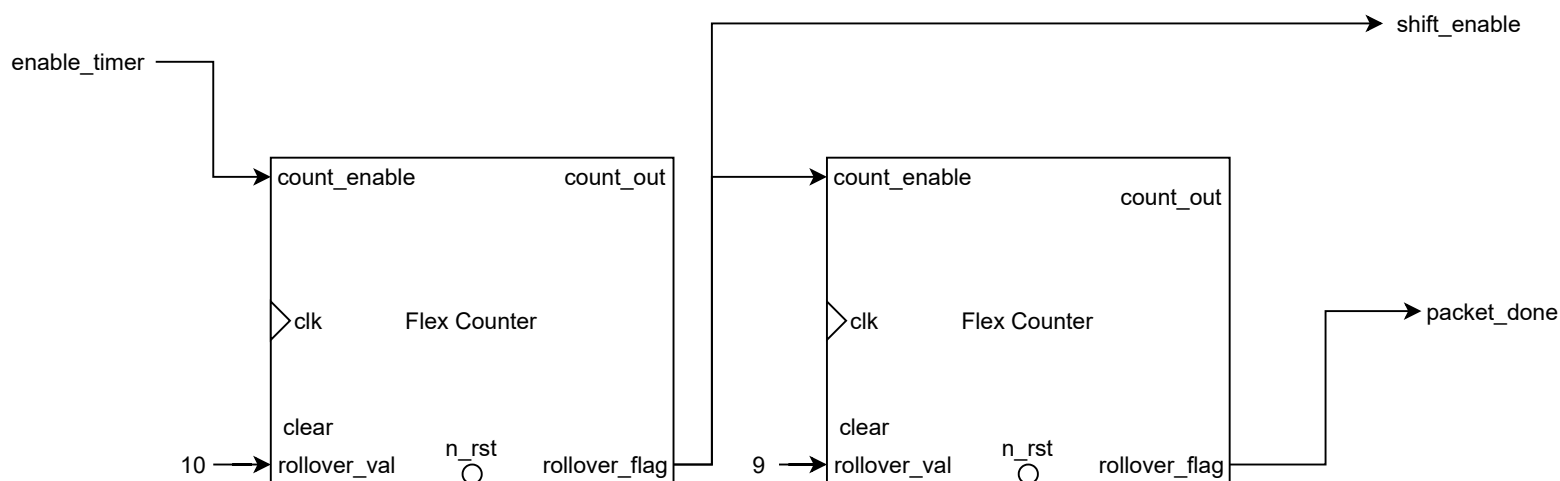enable_timer: connected to count_enable of first timer
shift_enable: connected to rollover flag of first timer
packet_done: connected to rollover flag of second timer

shift_enable

enable_timer

count_enable	count_out
clk	Flex Counter
clear	
10 → rollover_val	n_rst	rollover_flag

count_enable	count_out
clk	Flex Counter
clear	
9 → rollover_val	n_rst	rollover_flag

packet_done

First counter counts to 10 starting when enable_timer is
asserted. When it reaches 10, it outputs a strobe for shifting in a
new value into the 9-bit register and to count another bit shifted in
the second counter.

Second counter counts the bits based on the rollover flag from
the first counter, then outputs packet_done using its rollover flag
when it reaches 9 bits counted.