



AHB "Slave" Interface Address Mapping				
Address	Size (bytes)	Read/Write	Description	
0x0	2	R	Register: R0 Converter in idle 1 for bit 0. Converter is busy 1 for bit 1. Converter is warming up	
0x2	2	R	FIFO Result Register: Reading from the FIFO Result Register causes stall, so that need read the next value and so on.	
0x4	2	R/W	New Sample Columns Register: Write 12-40 sample column here (3-4 bit coeffs)	
0x6	2	R/W	R0 Coefficient Columns Register: Write 1st 12-40 coeff column here (3-4 bit coeffs)	
0x8	2	R/W	R1 Coefficient Columns Register: Write 2nd 12-40 coeff column here (3-4 bit coeffs)	
0xA	2	R/W	R2 Coefficient Columns Register: Write 3rd 12-40 coeff column here (3-4 bit coeffs)	
0xC	1	R/W	Command/Control Register: 0: Idle 1: Load Coefficients (3-4 bit) to indicate start of new sample 2: Load Coefficients Only (indicates starting column steering) 4: Start Load (indicates start of 1st load, three column and 3-4 bit coeffs) 6: Sample Done (indicates 1st load is done and end of sample)	

