

root
: Root

system
: System

cpu
: O3_ARM_v7a_3

itb
: ArmTLB

dtb
: ArmTLB

walker
: ArmTableWalker

walker
: ArmTableWalker

port

port

dcache_port

icache_port

dcache
: O3_ARM_v7a_DCache

icache
: O3_ARM_v7a_ICache

itb_walker_cache
: O3_ARM_v7aWalkCache

dtb_walker_cache
: O3_ARM_v7aWalkCache

cpu_side

mem_side

cpu_side

mem_side

cpu_side

mem_side

cpu_side

mem_side

system_port

membus
: SystemXBar

slave

master

mem_ctrls
: LPDDR3_1600_1x32

port