

## Limiting Gain Stage

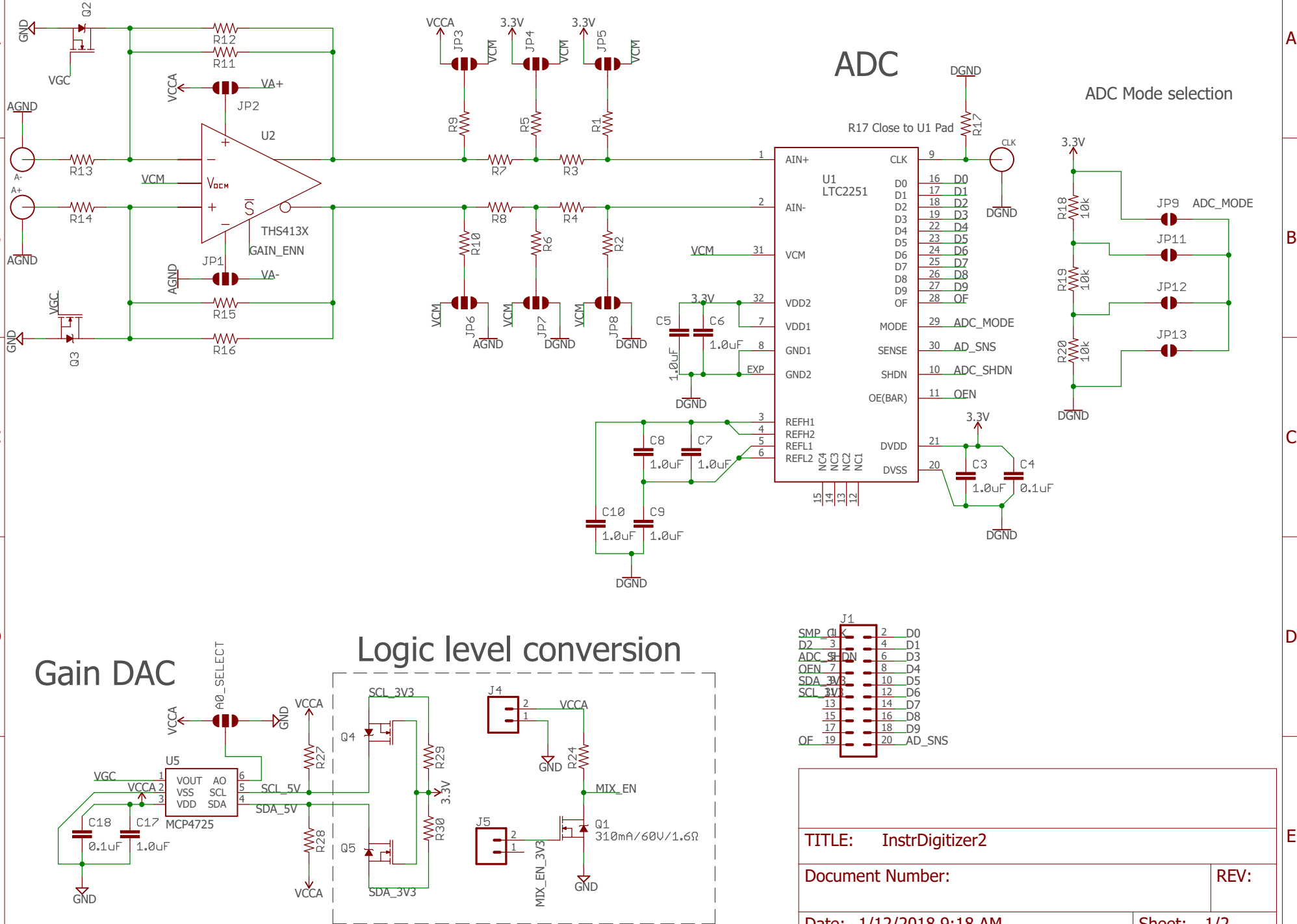
## Reconfigurable Filter Pads

## ADC

## ADC Mode selection

## Gain DAC

## Logic level conversion



SMP_CLK	2	D0
D2	3	D1
ADC_SHDN	4	D2
OEN	5	D3
SDA_3V3	6	D4
SCL_3V3	7	D5
	8	D6
	9	D7
	10	D8
	11	D9
	12	D10
	13	D11
	14	D12
	15	D13
	16	D14
	17	D15
	18	D16
	19	D17
	20	D18

TITLE: InstrDigitizer2

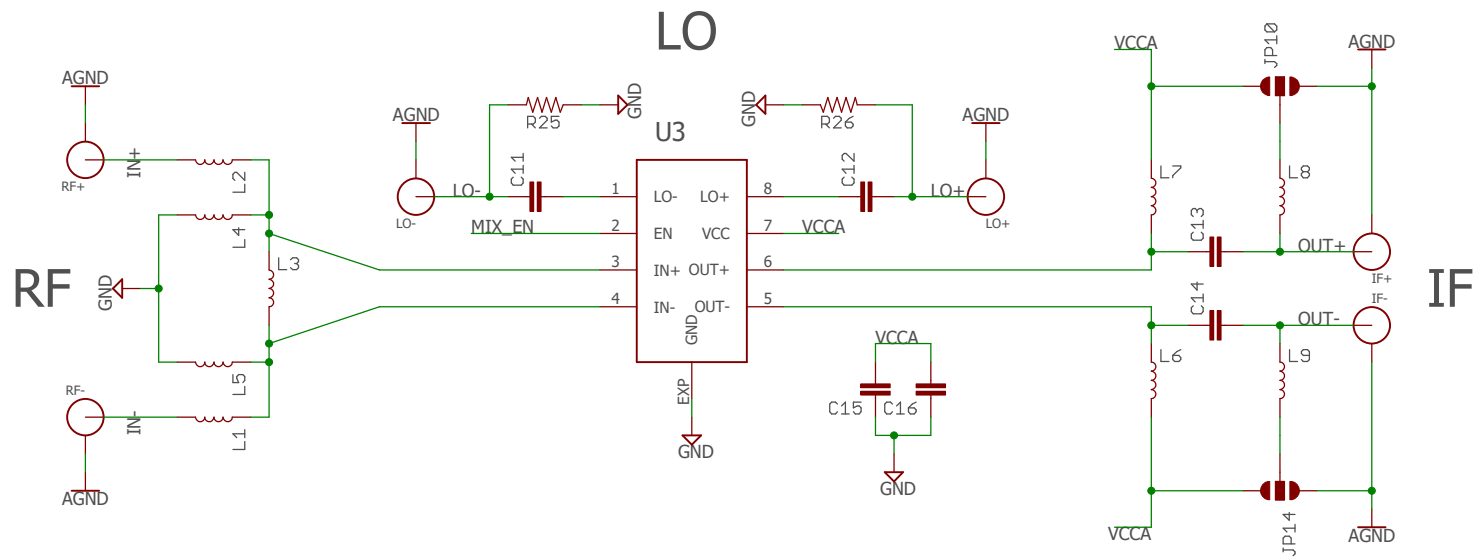
Document Number:

REV:

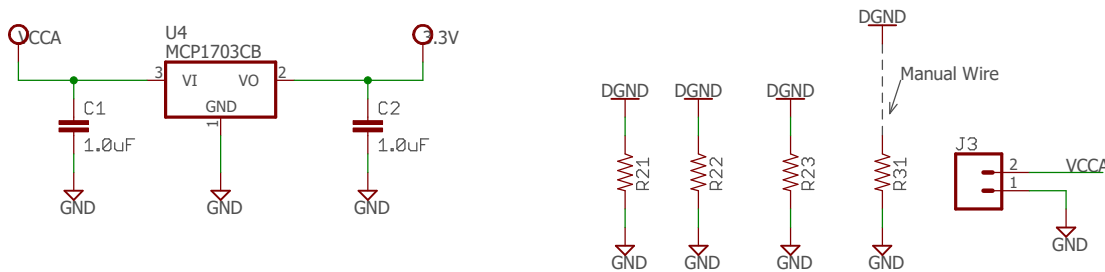
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## Down Conversion



## Power Delivery



Digital and Analog Ground Isolation

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