Computational Science on Many-Core Architectures Exercise 5

Example 1 Inclusive and Exclusive Scan (4 Points)

a)

For the first kernel "kernel_1" and simulate it with 4 blocks and 6 threads per block. Begin with line 24: the for loop iterates over the values X which belong to the block. At the end of the for loop \rightarrow write the temperaryy result of the scan into a vector Ya nd the offset stored in block_offset. At the end of the kernel every block containes ist scannes value and the vector for the next step.

```
Shared memory: 123456...

Shared memory 2: 11 1+2 2+3 3+4 4+5 5+6 ...

Shared memory 2: 11 1+2 1+2+3 1+2+3+1/4+5+2+3 ...

Shared memory 4

11 1+2 1+2+3 1+2+3+1/4+2+3+1/4

Weca weat veca black-offset weas

grid: 

Weca weat veca black-offset

Weca weat veca black-offset
```

b)

Listing 1: kernel for inclusive_scan)

```
__global__ void makeInclusive(double *Y, int N, const double *X)
2
       for (int i = blockDim.x * blockIdx.x + threadIdx.x; i < N-1; i += gridDim.x
3
4
           Y[i] = Y[i+1];
5
6
7
       if (blockDim.x * blockIdx.x + threadIdx.x == 0)
8
            // First step: Scan within each thread group and write carries
9
       scan_kernel_1 <<< num_blocks, threads_per_block >>> (input, output, N, carries);
10
       // Second step: Compute offset for each thread group (exclusive scan for each
11
12
       scan_kernel_2 <<<1, num_blocks>>>(carries);
13
14
       // Third step: Offset each thread group accordingly
15
       scan_kernel_3 <<< num_blocks, threads_per_block >>> (output, N, carries);
16
17
       // Make inclusive
       makeInclusive <<< num_blocks, threads_per_block >>> (output, N, input);
18
19
20
       cudaFree(carries);
21
       }{
           Y[N-1] += X[N-1];
22
23
   }
24
25
26
   void exclusive_scan(double const * input, double* output, int N)
27
   {
28
       int num_blocks = 256;
29
       int threads_per_block = 256;
30
       double *carries;
31
32
       cudaMalloc(&carries , sizeof(double) * num_blocks);
33
       // First step: Scan within each thread group and write carries
34
       scan_kernel_1 <<< num_blocks, threads_per_block >>> (input, output, N, carries);
35
36
```

```
// Second step: Compute offset for each thread group (exclusive scan for each
37
       scan_kernel_2 <<<1, num_blocks>>>(carries);
38
39
        // Third step: Offset each thread group accordingly
40
        scan_kernel_3 <<< num_blocks, threads_per_block >>> (output, N, carries);
41
42
43
        // Make inclusive
        makeInclusive <<< num_blocks, threads_per_block >>> (output, N, input);
44
45
46
        cudaFree(carries);
47
```

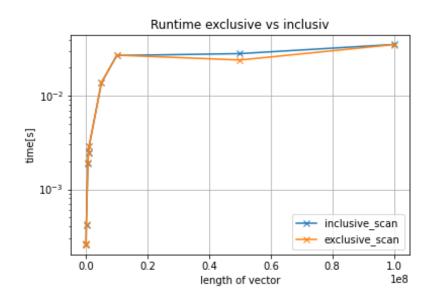
c)

Only nessesary to remove the folloing code snipped:

Listing 2: kernel for inclusive_scan)

```
// exclusive scan requires us to write a zero value at the beginning of each blow my_value = (threadIdx.x > 0)? shared_buffer[threadIdx.x - 1] : 0;
```

d)



There are bassicaly no differences.

2 Poisson equation (5 Points)

