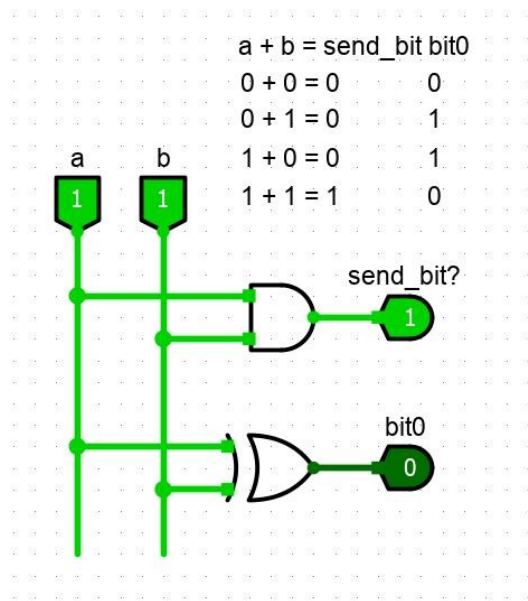
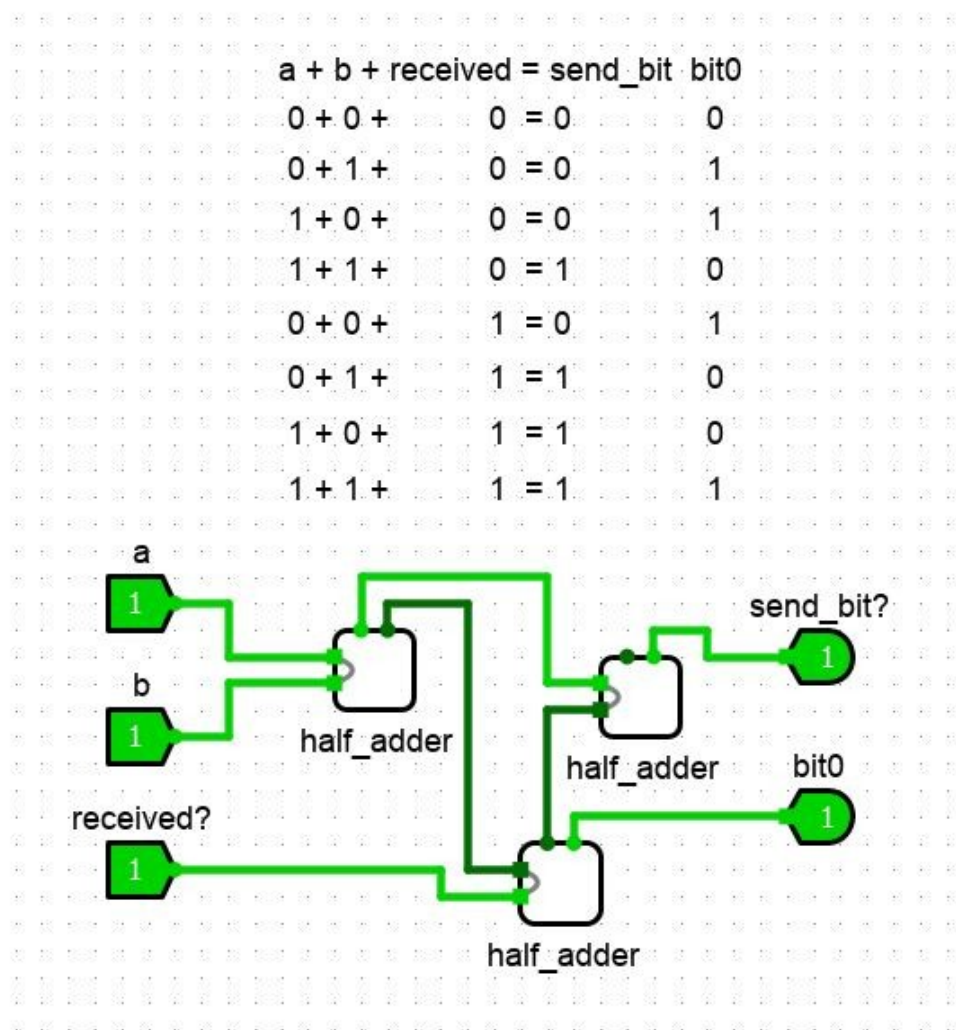


Meio_somador – Logisim:



Somador_completo_a_mais_b_mais_bit_recebido – Logisim:



ULA 1bit (and, or, not, sum, subtract) - Logisim:

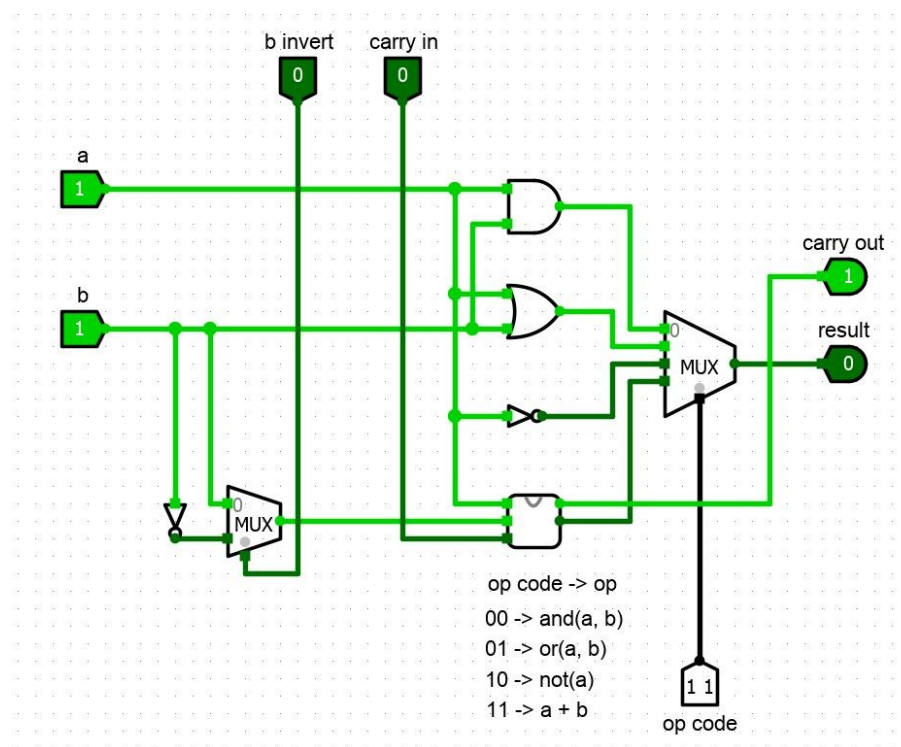
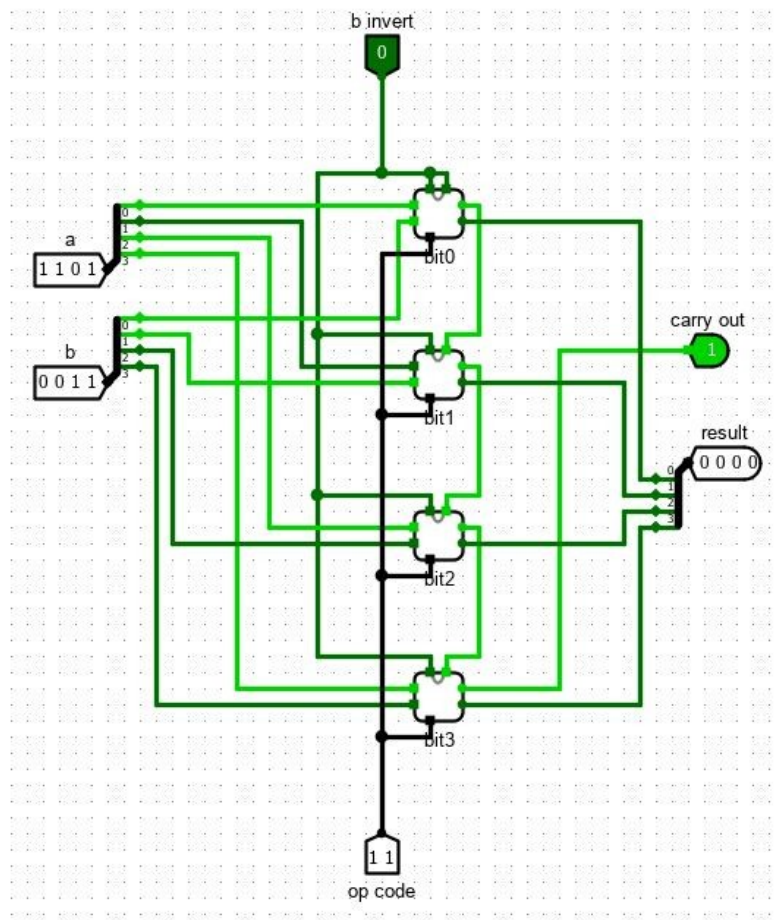


Tabela adotada no projeto da ULA tanto no Logisim como no Simulador 097:

1	op1	op0	operation		
2	0	0	and(a, b)		
3	0	1	or(a, b)		
4	1	0	not(a)		
5	1	1	sum(a, b)		
6					
7					
8	a	b	op1	op0	out
9	0	0	0	0	0
10	0	0	0	1	0
11	0	0	1	0	1
12	0	0	1	1	0
13	0	1	0	0	0
14	0	1	0	1	1
15	0	1	1	0	1
16	0	1	1	1	1
17	1	0	0	0	0
18	1	0	0	1	1
19	1	0	1	0	0
20	1	0	1	1	1
21	1	1	0	0	1
22	1	1	0	1	1
23	1	1	1	0	0
24	1	1	1	1	0
25					

ULA 4bits (and bit-bit, or bit-bit, not, sum, subtract) - Logisim:



ULA 1bit (and, or, not, sum) - Simulador097:

