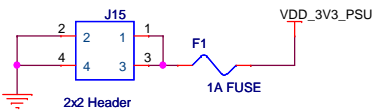
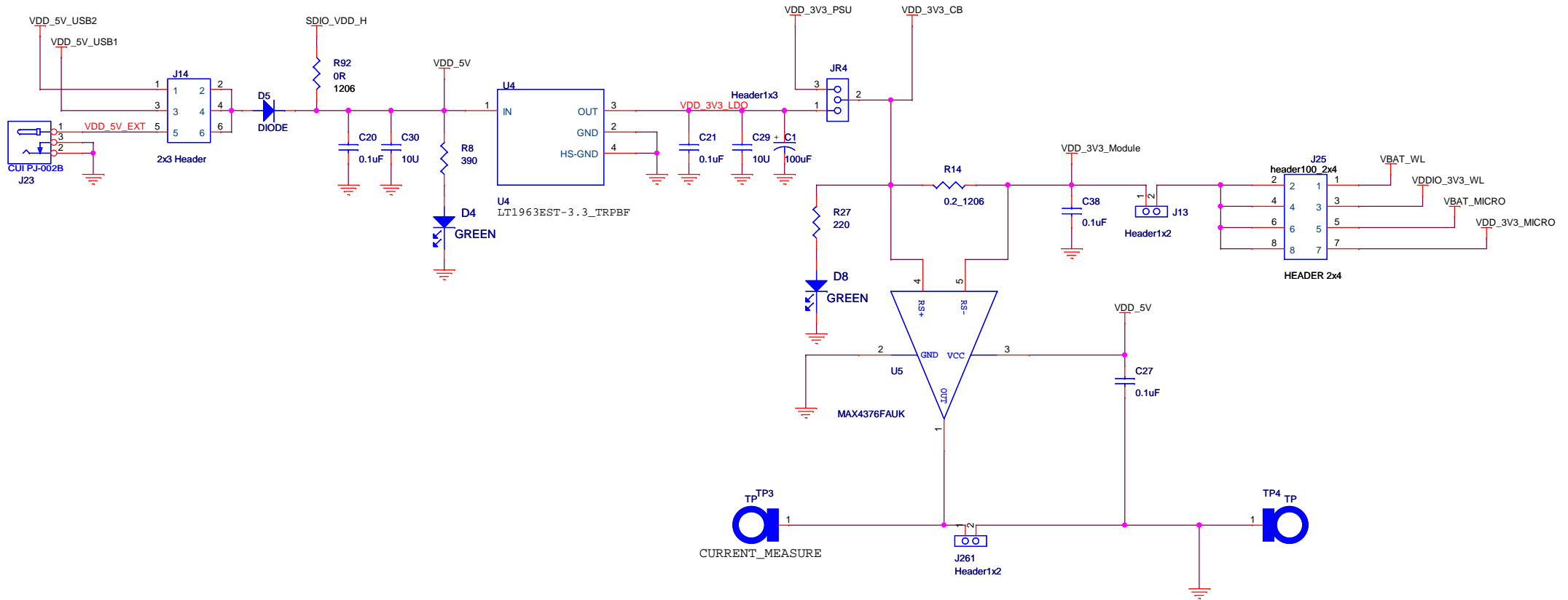
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JR4 jumper setting

Pin1-2: Power is provided from USB
or SDIO interface of PC

Pin2-3: Power source is PSU



Revision History

- 1) Rev. 01 - (2012/03/05)
- 2) Rev. 02 - (2012/03/06)

i) Modify U6 symbol, add pin2.
ii) Add jumper J7 between VBAT_WL and 60-pin header and add jumper J12 between VDDIO_3V3_WL and 60-pin header
iii) Change C45,C46,C47,C48,C49,C50,C51,C52 to NC.
iv) Modify the description for VDD_3V3_MICRO from "pin.F7" to "pin.A8,D9,E1,F7" on page1.
v) Connect WL_SLEEP_CLK net to 60-pin connector pin 59
vi) Change J16 and J19 to 2x4 header and remove J18
vii) Add LED circuit (R28,D6,Q3) for MICRO_JTAG_TCK signal
viii) Add 10K pullups on MICRO_JTAG_TRST/TCK/TDI/TMS
ix) Add jumper J13 between VDD_3V3_Module net and J25
- 3) Rev. 03 - (2012/03/06)

i) Replace J259,J260 to a 2x2 header,J15
ii) Remove JR9 and add J14,J23 for new VDD_5V_EXT rail
- 4) Rev. 04 - (2012/03/07)

i) Change R28 to 220R
ii) Mirror JTAG header circuit
iii) Populate R92
iv) Rename U10,U11 to J16,J17
- 5) Rev. 05 - (2012/03/09)

i) Add J262 for BCM43362 UART interface
- 6) Rev. 06 - (2012/03/12)

i) Change J16, J17 to header 2x5
ii) Re-define the pin definition of J24 & J9 for better routing
iii) Remove J262 (already have J3 & J4 for WLAN UART)
iv) Add R93 & R94 as option for reset circuit
v) Rename U8 to J1
- 7) Rev. 07 - (2012/03/16)

i) Change U6 to Microchip TC1270ATVRCTR and add R95
- 8) Rev. 08 - (2012/03/17)

i) Re-swap GPIO13 and GPIO14 with GPIO7 and GPIO8 so that all 4 I2S signals are back on J9
- 9) EVT. 1.0 - (2012/06/01)

i) change footprint of C1 to smd_1206
ii) swap VDD_5V_USB2 and VDD_5V_USB1 on J14

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Title <div>Revision History</div>			
Size <div>B</div>	Document Number <div>WM-N-BM-11 EVB</div>		Rev <div>08</div>
Date:	Monday, June 04, 2012	Sheet <div>5</div>	of <div>5</div>