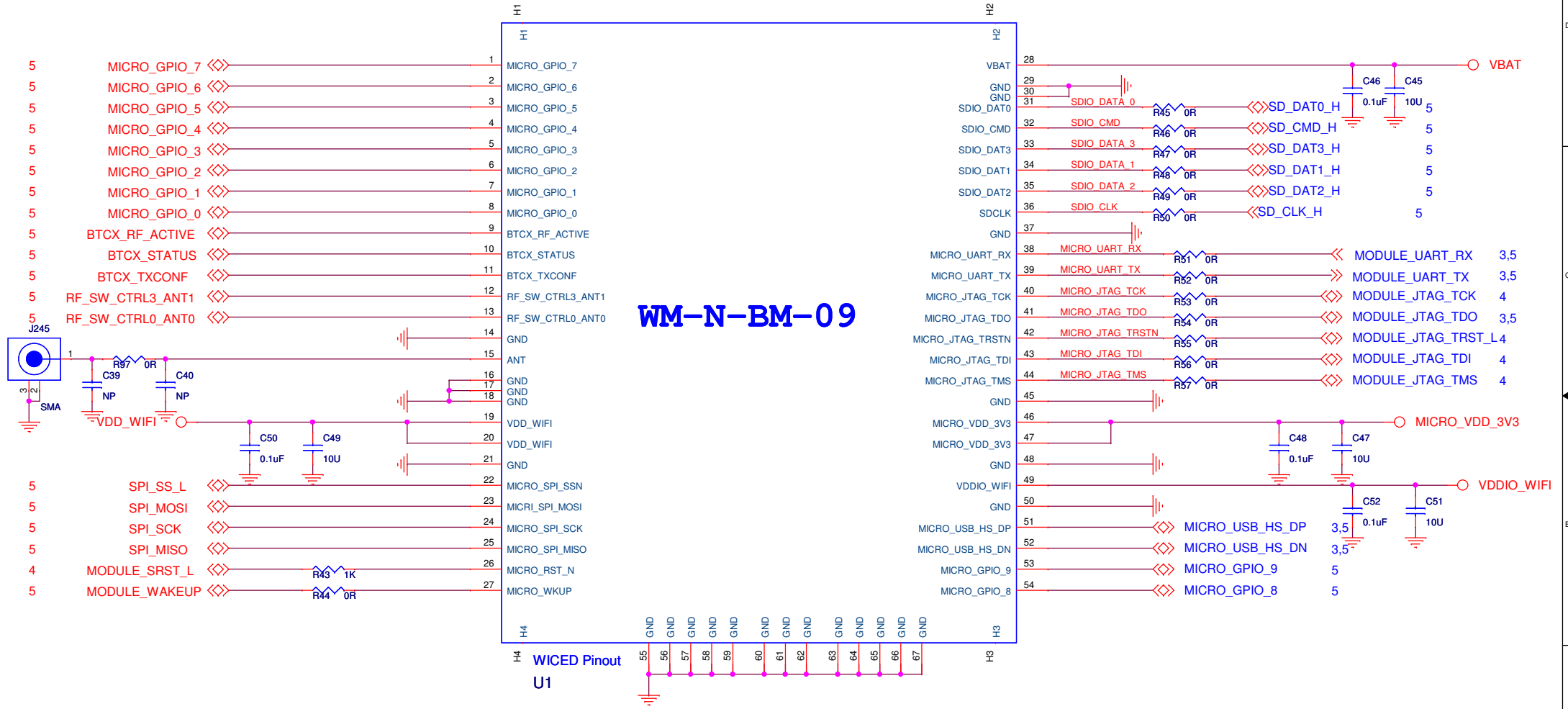
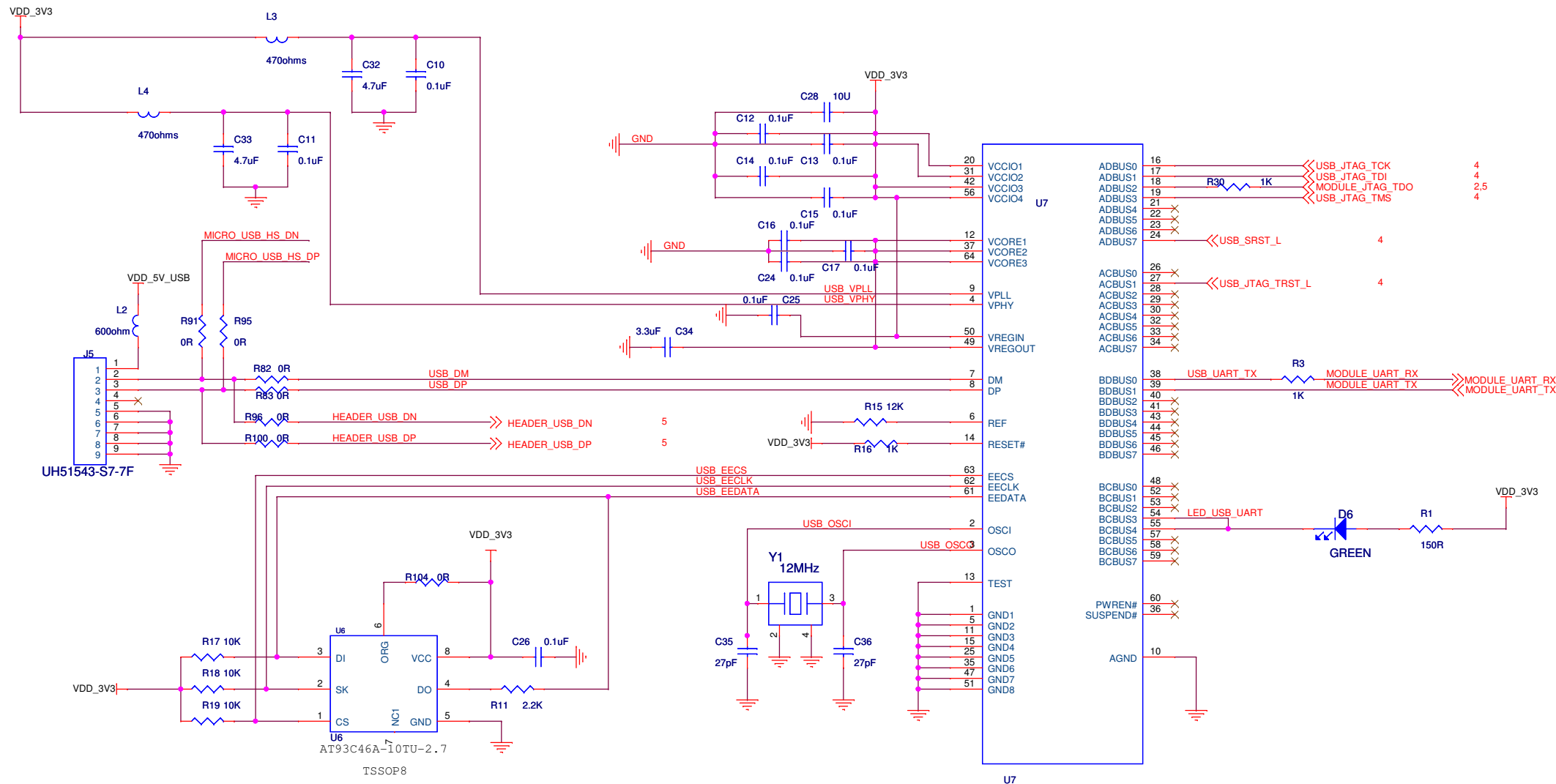


Title		
Block Diagram		
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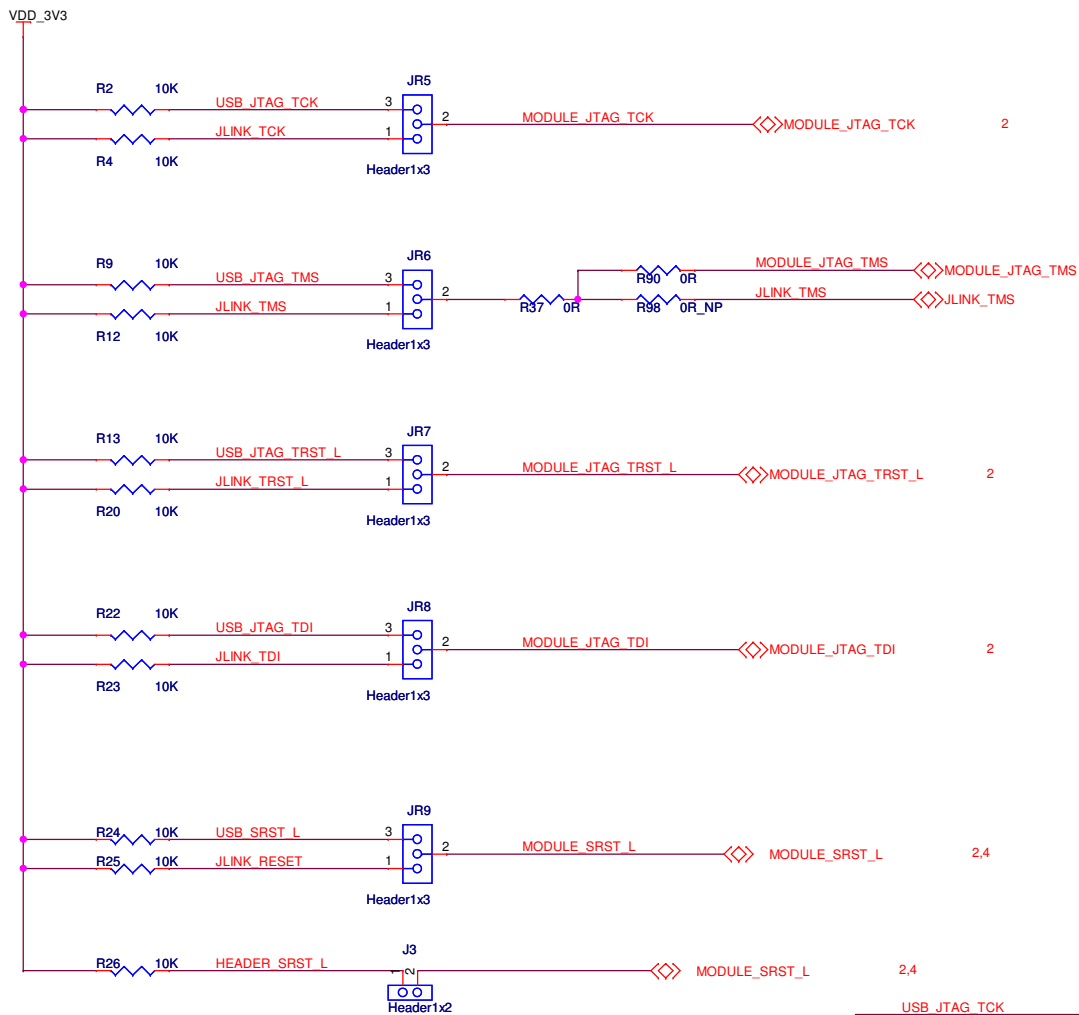


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WICED Module		
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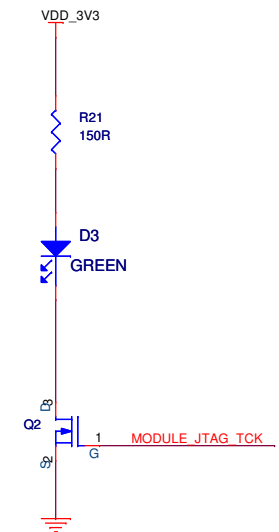
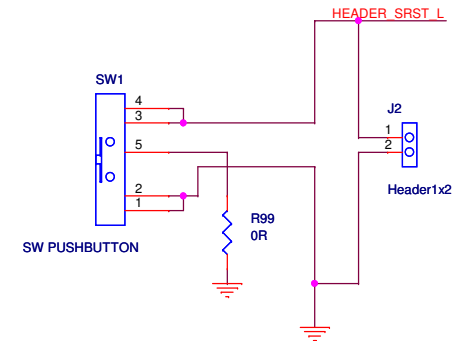


2,5 MICRO\_USB\_HS\_DN << MICRO\_USB\_HS\_DN  
2,5 MICRO\_USB\_HS\_DP << MICRO\_USB\_HS\_DP

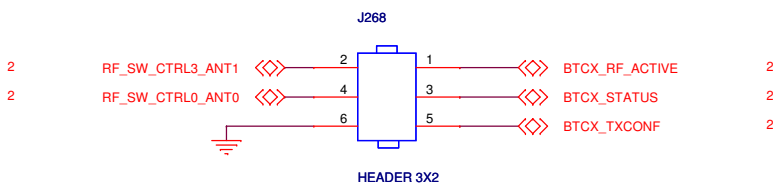
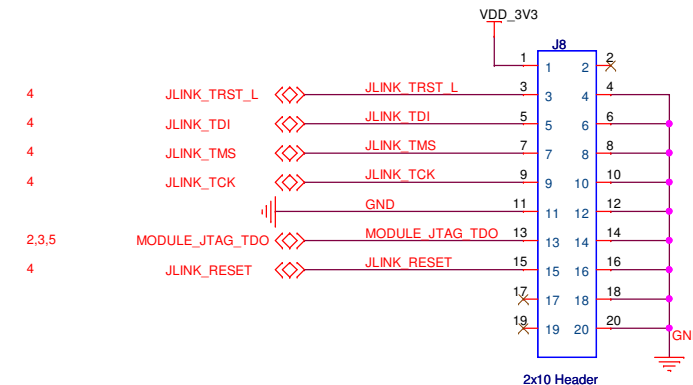
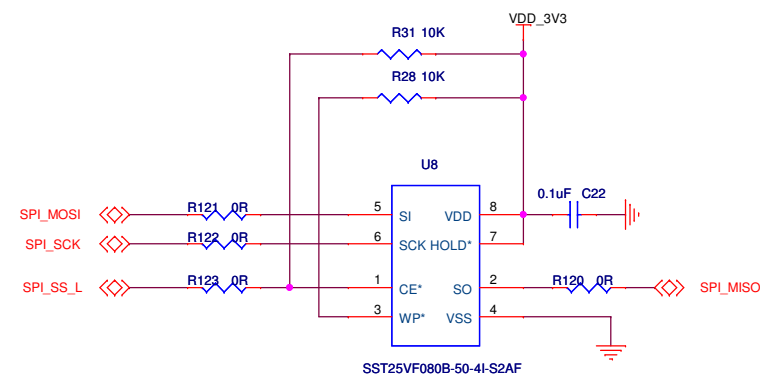
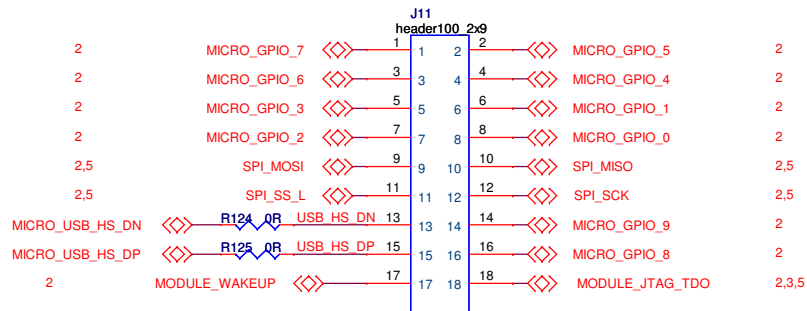
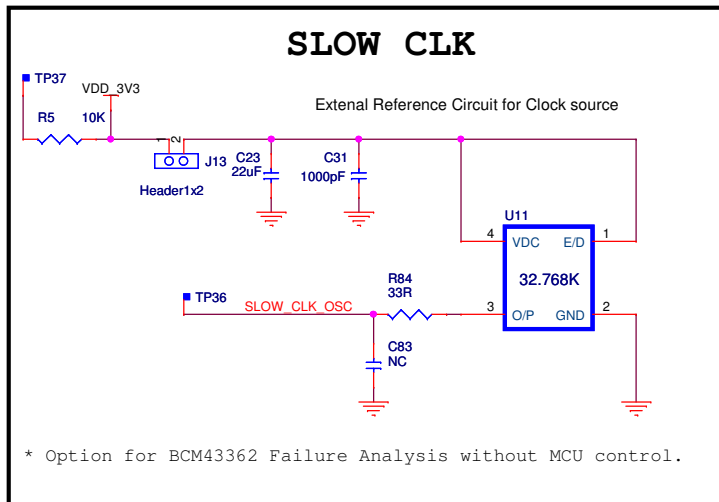
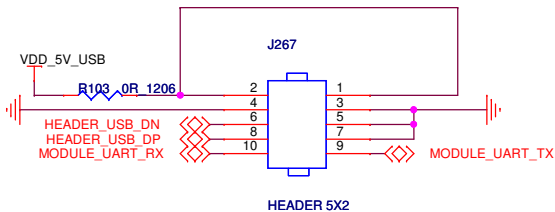
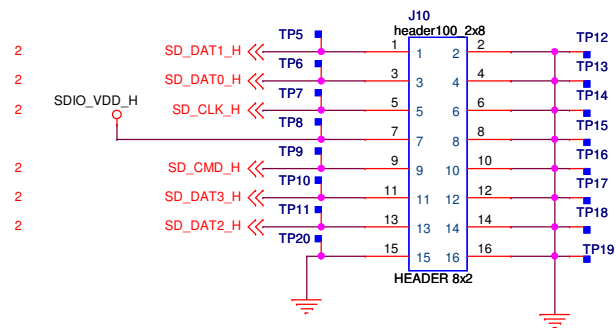
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Host Control Interface		
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USB_JTAG_TCK	⟷	USB_JTAG_TCK	3
JLINK_TCK	⟷	JLINK_TCK	5
USB_JTAG_TMS	⟷	USB_JTAG_TMS	3
JLINK_TMS	⟷	JLINK_TMS	4,5
USB_JTAG_TRST_L	⟷	USB_JTAG_TRST_L	3
JLINK_TRST_L	⟷	JLINK_TRST_L	5
USB_JTAG_TDI	⟷	USB_JTAG_TDI	3
JLINK_TDI	⟷	JLINK_TDI	5
USB_SRST_L	⟷	USB_SRST_L	3
JLINK_RESET	⟷	JLINK_RESET	5



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SDIO		
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## Revision History

A) Rev. 01 - (2012/03/08)

B) Rev. 11 - (2012/06/13)

- 1) Swap pin MICRO\_USB\_FS\_DN and pin MICRO\_USB\_HS\_DP.
- 2) Modify reset circuit for U6
- 3) Add J267. (For manufacturing requirement)
- 4) Add J11. ( 2x9 Header for MCU GPIO signal test)
- 5) Change D7 for VDD\_3V3 indication.
- 6) Change D8 for VDD\_3V3\_Module\_In indication.
- 7) Re-define pin definition for J10.
- 8) Remove C41,C42,R99,R84,R85,R86,R87.
- 9) Remove U2, U3.
- 10) Add JR5, JR6, JR7, JR8, JR9, J3, J25, J262, R96, R99, R100
- 11) Add U8 (SPI flash)
- 12) Add U11 (Slow CLK for BCM43362 failure analysis without MCU)
- 13) Change SW1 to another push button
- 14) Add J268 ( 2x3 Header )

Title			
Revision History			
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