

# Andrew Zhong

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Education	<b>University of California, Berkeley</b> – Berkeley, CA	
	Master of Engineering in Computer Science, Visual Computing and Computer Graphics	May 2014
	<b>University of Illinois at Urbana-Champaign</b> – Urbana, IL	
	Bachelor of Science in ECE with <i>Highest Honors</i> , Overall: 3.89/4.0, Technical: 3.92/4.0	May 2013
	<b>Related Coursework:</b> Algorithms, Data Structures, Computer Graphics, Computer Vision, Machine Learning, Parallel Computing, Operating Systems, Computer Architecture, Web Development, Database, Leadership	
Skills	<b>Languages:</b> Java, C/C++, Python, OpenGL, CUDA, HTML, CSS, Ruby, JavaScript, SQL, VHDL, Verilog, x86 <b>Applications:</b> Android, Google App Engine, OpenCV, Kinect, Eclipse, Git, MATLAB, ModelSim, Blender	
Experience	<b>Qualcomm Research – Augmented Reality (Prof. Björn Hartmann)</b> , Santa Clara, CA	Sep 2013 - Present
	<ul style="list-style-type: none"><li>• Create an Android AR application with OpenGL ES 2 and Qualcomm Indoor Navigation APIs</li><li>• Overlaid 3D graphical map objects on the camera view based on location and orientation</li></ul>	
	<b>Qualcomm Inc. – Graphics System Design Intern</b> , San Diego, CA	May – Aug 2013
	<ul style="list-style-type: none"><li>• Initiated and developed text processing and pattern matching tools for massive netlist and log files</li><li>• Performed MIPI DSI (Display Serial Interface) modeling and video stream simulations</li></ul>	
	<b>Coordinated Science Laboratory – Diagnosing Performance Violations at System Level Using Data Mining (Prof. Shobha Vasudevan)</b> , Urbana, IL	Oct 2012 – May 2013
	<ul style="list-style-type: none"><li>• Localized the latency and throughput violations using a concurrent pattern mining approach</li><li>• Applied domain knowledge to filter out up to 92.8% of transaction traces, increasing mining efficiency</li></ul>	
	<b>Beckman Institute – Brain-controlled Programmable Embedded System</b> , Urbana, IL	Jan – May 2013
	<ul style="list-style-type: none"><li>• Designed an EEG-signal-controlled tablet prototype with ~2.5 s response time and 95% reliability</li><li>• Earned Research Award in Senior Design Hall of Fame</li></ul>	
Projects	<b>Qualcomm Inc. – Algorithm and System Design Intern</b> , San Diego, CA	Jun – Aug 2012
	<ul style="list-style-type: none"><li>• Implemented the color processing algorithm based on 3D look-up table gamut mapping for Mirasol</li><li>• Optimized memory placement algorithm and reduced look-up table size by a factor of 4</li></ul>	
	<b>Augmented Object Detector - Android App</b> , CS Berkeley	Sep 2013 – Present
	<ul style="list-style-type: none"><li>• Detected objects from Android camera video stream with Haar training</li><li>• Rendered the detected objects in a 3D virtual scene on top of camera view with OpenGL ES 2</li></ul>	
	<b>Operating Systems</b> , CS Berkeley, ECE UIUC	Jan 2013 – Present
	<ul style="list-style-type: none"><li>• Developed a multi-threaded HTTP server that supports asynchronous IO and thread-safe caching</li><li>• Built a Linux-based OS in C and x86: paging, interrupts, system calls, program loader, page allocator, multiple terminals, scheduling, signals, sound and mouse support, shell extensions and GUI</li><li>• Achieved 4<sup>th</sup> place out of 30 teams in the Microsoft Operating System Design Competition</li></ul>	
	<b>Computer Graphics and Computer Vision</b> , CS Berkeley	Sep – Nov 2013
	<ul style="list-style-type: none"><li>• Coded in C++ from scratch: a ray tracer that implements Phong shading, refraction and .obj file inputs</li><li>• Developed in OpenGL: uniform subdivision, adaptive tessellation, obj &amp; mtl inputs, vertex shading</li><li>• Explored homography rectification, 3D reconstruction, edge detection, texture and digit recognition</li></ul>	
Publications	<b>Pipelined Processor Design</b> , ECE UIUC	Sep – Dec 2012
	<ul style="list-style-type: none"><li>• Designed and verified datapath, control and cache of a 5-stage pipelined processor based on LC3b</li><li>• Achieved 2<sup>nd</sup> place out of 22 teams in the AMD Processor Design Competition</li></ul>	
	<ul style="list-style-type: none"><li>• “Diagnosing Root Causes of System Level Performance Violations”, ACM/IEEE ICCAD 2013</li><li>• “Troubleshooting Performance Violations at System Level Using Data Mining”, Poster at DAC 2013</li></ul>	
Honors	Eta Kappa Nu, Tau Beta Pi, National Society of Collegiate Scholars, IEEE, SIAM Highest Honors at Graduation, O. Thomas and Martha S. Purl Scholarship, Dean’s List First Prize in National Physics Contest in Jiangsu, China, 2007	