BIRZEIT UNIVERSITY

Faculty of Engineering and Technology
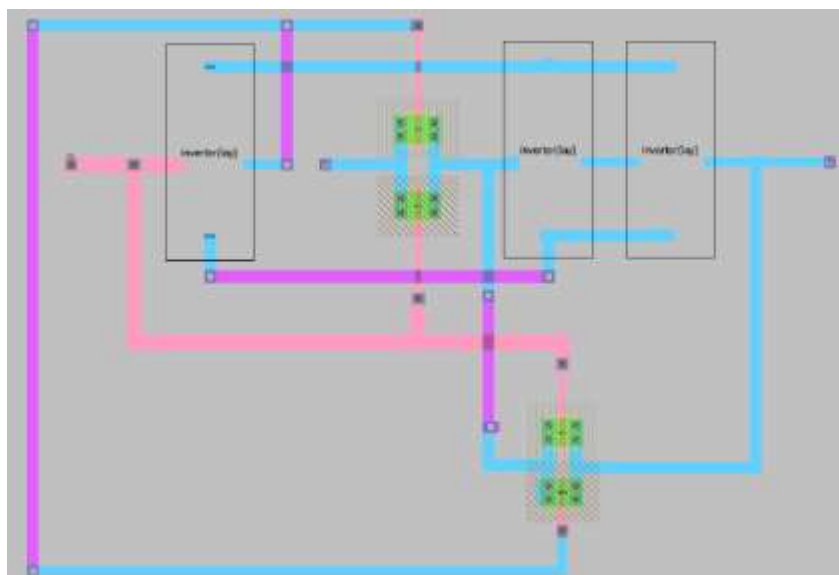
Electrical and Computer Engineering Department
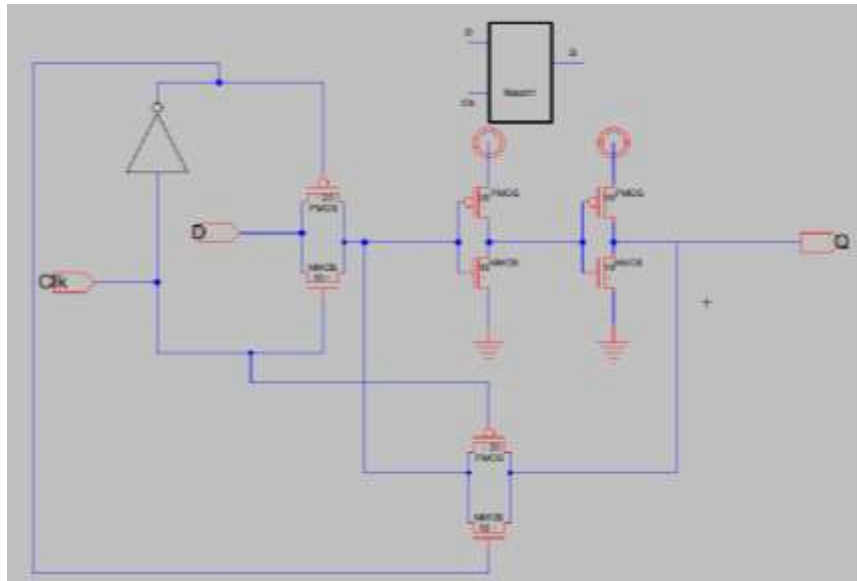
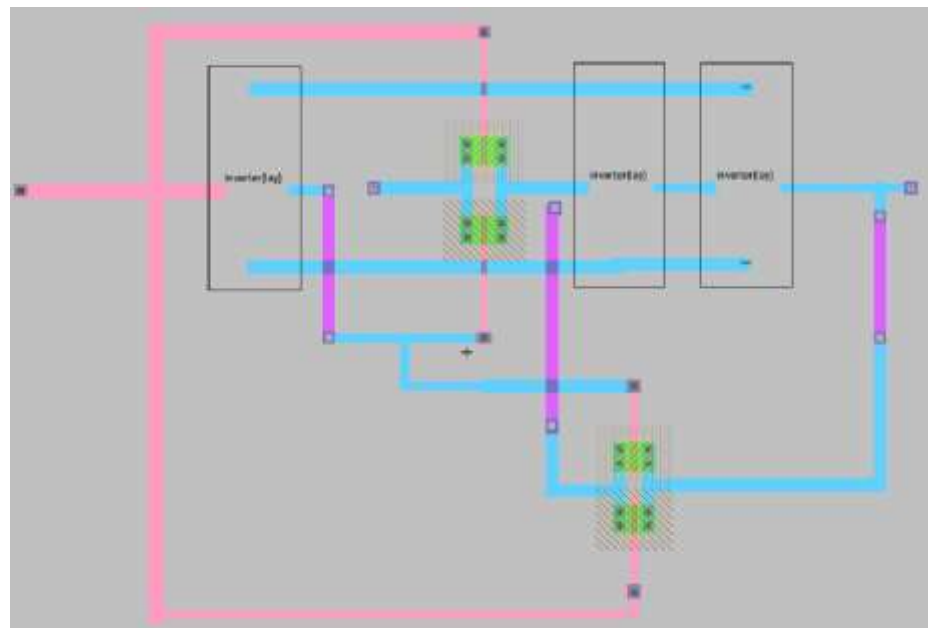DIGITAL INTEGRATED CIRCUITS
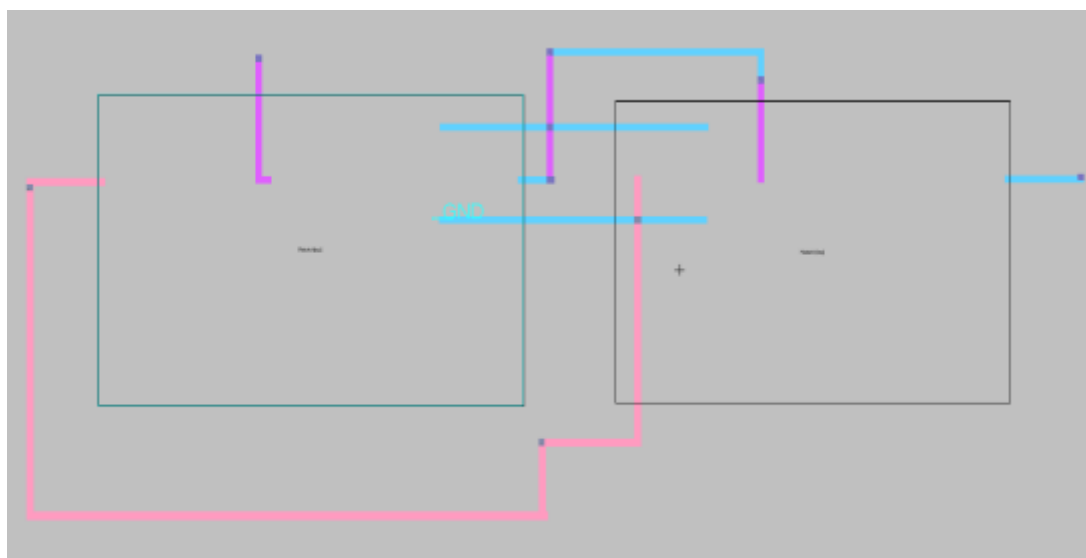
Assignment 2

Name: Aya Dahbour

ID: 1201738

Instructor: Khader Mohammad

1- Build N-latch schematic and layout.

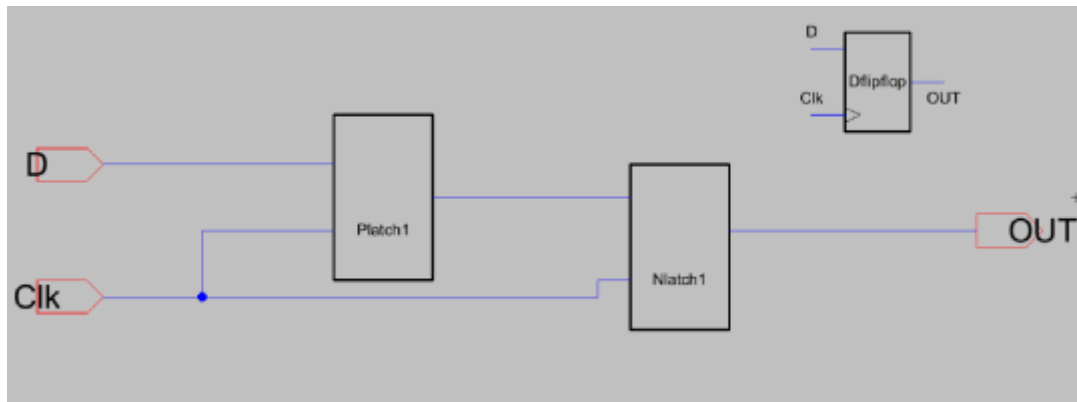## 2- Build P-latch schematic and layout.

3- Build a rising edge Flip-Flop schematic and layout.

4- Build a falling edge Flip-Flop schematic and layout.