



**Faculty of Engineering and Technology**  
**Electrical and Computer Engineering Department**

**DIGITAL INTEGRATED CIRCUITS**

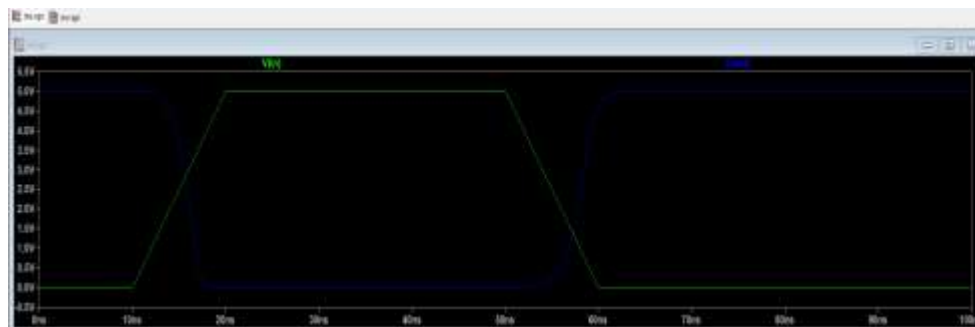
**Assignment 1**

Name: Aya Dahbour

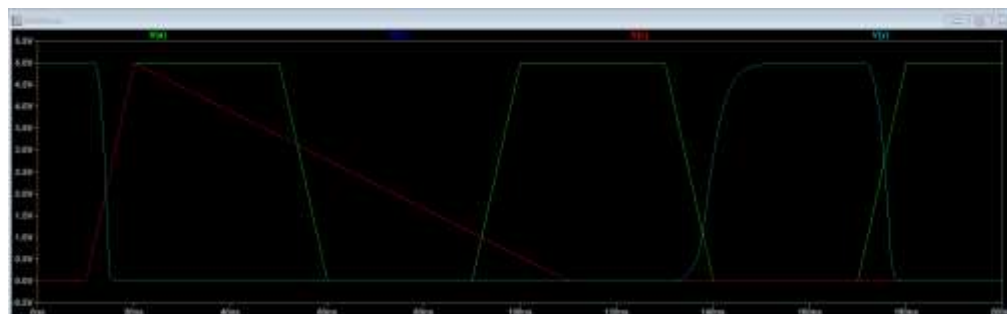
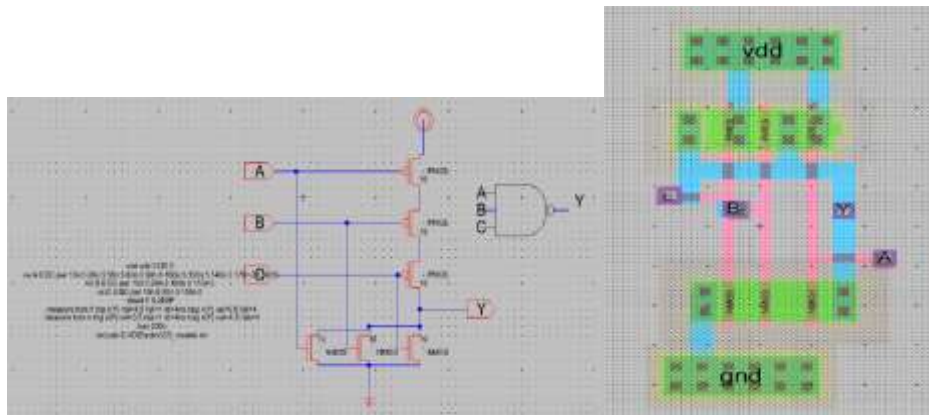
ID: 1201738

Instructor: Khader Mohammad

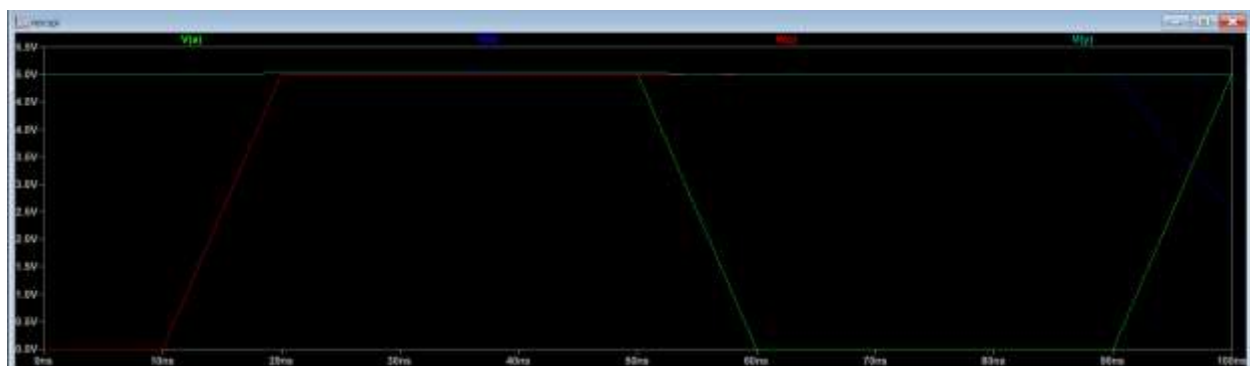
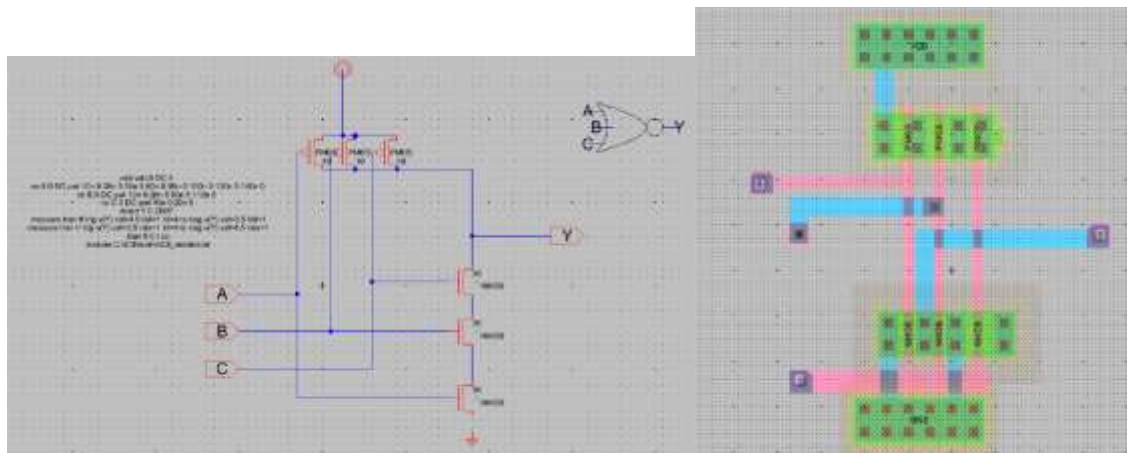
## 1. Design, layout, and simulation of inverter:



## 2.Design, layout, and simulation of 3input NAND gate:



### 3.Design, layout, and simulation of 3input NOR gate:



### 3.Design, layout, and simulation of one bit PASS gate:

