

VEERMATA JIJABAI TECHNOLOGICAL INSTITUTE,

MATUNGA, MUMBAI – 400019.

End Semester Examination December 2020 LAB courses Time Table (Odd Term-Semester III)

Electrical Engineering Department

S. Y. B. Tech. (Electrical, Electronics and Electronics & Telecommunication)

Date & Day	Time	S.Y.B. Tech Electrical	S.Y.B. Tech Electronics	S.Y.B. Tech EXTC
07/12/2020 Monday	9 am to 5 pm	--	--	--
08/12/2020 Tuesday	9 am to 5 pm	Electronic devices & Circuits LAB (R4EE2004P) Dr. Soumya Gupta	Electronic Circuit analysis & Design I LAB (R4EC2002P) Dr. Prashant Bhopale	Analog Communication LAB (R4ET2004P) Dr. Shweta Ghodeshwar
09/12/2020 Wednesday	9 am to 5 pm	--	Digital Combinational Circuits LAB (R4EC2003P) Dr. Chinmay Rajhans	Digital Logic Design LAB (R4ET2005P) Prof. Vivek Kachhatiya
10/12/2020 Thursday	9 am to 5 pm	Electrical machines –I LAB (R4EE2003P) Dr. S R Wagh	Python Programming LAB (R4EC2005P) Dr. Tanushree Sharma	Electronic Circuit Analysis & Design LAB (R4ET2003P) Dr. Prashant Bhopale
11/12/2020 Friday	9 am to 5 pm	--	--	--

Approved By

Dean (AP)

Head of Electrical Engineering Department

Examination coordinator

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End Semester Examination December 2020 LAB courses Time Table (Odd Term-Semester V)

Electrical Engineering Department

T. Y. B. Tech. (Electrical, Electronics and Electronics & Telecommunication)

Day & Date	Time	T.Y.B. Tech Electrical	T.Y.B. Tech Electronics	T.Y.B. Tech EXTC
07/12/2020 Monday	9 am to 5 pm	--	--	--
08/12/2020 Tuesday	9 am to 5 pm	Electromagnetic Fields & Waves Lab (R4EE3002P) Prof. Krishna Kanakgiri	Analog Integrated Circuits Lab (R4EC3004P) Dr. Chinmay Rajhans	Digital Signal Processing (R4ET3003P) Rizwan Ahmed
09/12/2020 Wednesday	9 am to 5 pm	Control systems –I Lab (R4EE3003P) Prof. J O Chandle	Digital Signal Processing Lab (R4EC3001P) Prof. R A Patil	Communication Circuit Design R4ET3006L Dr. Shweta Ghodeshwar
10/12/2020 Thursday	9 am to 5 pm	Microprocessor & Microcontroller Lab (R4EE3004P) Prof. H B Chaudhari	Microprocessor Systems Lab (R4EC3005P) Dr. R D Daruwala	Digital Communication Systems (R4ET3004P) Dr. D P Rathod
11/12/2020 Friday	9 am to 5 pm	Simulation software-I R4EE3006L Prof. H B Chaudhari	Electronic Engineering Practice R4EC3007L Dr. R D Daruwala	Computer Communication Networks (R4ET3005P) Prof. Amutha Jeyakumar

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End Semester Examination December 2020 LAB courses Time Table (Odd Term-Semester VII)

Electrical Engineering Department

B. Tech. (Electrical, Electronics and Electronics & Telecommunication)

Day & Date	Time	B. Tech Electrical	B. Tech Electronics	B. Tech EXTC
07/12/2020 Monday	9 am to 5 pm	--	--	--
08/12/2020 Tuesday	9 am to 5 pm	Drives & Control Lab (EE4003P) Dr. S R Wagh	Embedded Systems Lab (EC4003P) Dr. Javed Alam	--
09/12/2020 Wednesday	9 am to 5 pm	Biomedical Electronics Lab (EE4101P) Dr. M S Panse FACTS Lab (EE4102P) Prof. P P Gupta	Data Communication & Networking (EC4002P) Prof. Amutha Jeyakumar	Antenna Theory (ET4003P) Rizwan Ahmed
10/12/2020 Thursday	9 am to 5 pm	--	--	--
11/12/2020 Friday	9 am to 5 pm	--	Error Correcting Codes Lab (EC4102P) Dr. D P Rathod	Radio Frequency Circuit Design (ET4101P) Dr. Shweta Ghodeshwar
12/12/2020 Saturday	9 am to 5 pm	Project-I EE4901D	Project-I EC4901D	Project-I ET4901D

Approved By

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Head of Electrical Engineering Department

Examination coordinator