## VEERMATA JIJABAI TECHNOLOGICAL INSTITUTE,

MATUNGA, MUMBAI – 400019.

#### Mid Semester Examination Time Table (Odd Term-Semester III) Electrical Engineering Department

S. Y. B. Tech. (Electrical, Electronics and Electronics & Telecommunication)

Date & Day	Time	S.Y.B. Tech Electrical	S.Y.B. Tech Electronics	S.Y.B. Tech EXTC
28/09/2020	12 to 1 mm	Mathematics for Electrical	Mathematics for Electrical	Mathematics for Electrical
Monday	12 to 1 pm	Engineers-I (MA2003S)	Engineers-I (MA2003S)	Engineers-I (MA2003S)
29/09/2020	12 to 1	Environmental Studies	Environmental Studies	Signals & Systems
Tuesday	12 to 1 pm		(R4CE2012S)	(R4ET2001S)
30/09/2020	12 to 1 pm	Electrical Networks-I	Electrical networks	Network Analysis & Synthesis
Wednesday	12 to 1 pill	(R4EE2001S)	(R4EC2001S)	(R4ET2002S)
01/10/2020	12 to 1 mm	Electronic devices & Circuits	Electronic Circuit analysis &	Electronic Circuit Analysis &
Thursday	12 to 1 pm	(R4EE2004T)	Design I (R4EC2002T)	Design (R4ET2003T)
03/10/2020	12 to 1 mm	Electrical machines –I	Digital Combinational Circuits	Digital Logic Design
Saturday	12 to 1 pm	(R4EE2003T)	(R4EC2003T)	(R4ET2005T)
05/10/2020	12 to 1 nm	Introduction to power systems	Numerical Techniques	Analog Communication
Monday	12 to 1 pm	(R4EE2002S)	(R4EC2004T)	(R4ET2004T)
06/10/2020	12 to 1 nm		Python Programming	
Tuesday	12 to 1 pm		(R4EC2005T)	

Approved By

Dean (AP)

Head of Electrical Engineering Department

Examination coordinator

## VEERMATA JIJABAI TECHNOLOGICAL INSTITUTE,

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#### Mid Semester Examination Time Table (Odd Term-Semester V) Electrical Engineering Department

T. Y. B. Tech. (Electrical, Electronics and Electronics & Telecommunication)

Day & Date	Time	T.Y.B. Tech Electronics  T.Y.B. Tech Electronics		T.Y.B. Tech EXTC
28/09/2020	12 to 1	Math V	Principles of Communication	Computer Communication
Monday	12 to 1 pm		Systems (R4EC3003S)	Networks (R4ET3005T)
29/09/2020	12 to 1	Electromagnetic Fields & Waves	Electromagnetic Fields & Waves	Electromagnetic Wave Engg.
Tuesday	12 to 1 pm	(R4EE3002T)	(R4EC3002S)	(R4ET3001S)
30/09/2020	12 to 1 mm	Signals & Systems	Digital Signal Processing	Digital Signal Processing
Wednesday	12 to 1 pm	(R4EE3001S)	(R4EC3001T)	(R4ET3003T)
01/10/2020	12 to 1 nm	Control systems –I	Analog Integrated Circuits	Control Systems
Thursday	12 to 1 pm	(R4EE3003T)	(R4EC3004T)	(R4ET3002S)
03/10/2020	12 to 1	Microprocessor & Microcontroller	Microprocessor Systems	Digital Communication Systems
Saturday	12 to 1 pm	(R4EE3004T)	(R4EC3005T)	(R4ET3004T)
05/10/2020	_			
Monday				

Approved By

Dean (AP)

Head of Electrical Engineering Department

Examination coordinator

# VEERMATA JIJABAI TECHNOLOGICAL INSTITUTE,

MATUNGA, MUMBAI – 400019.

### Mid Semester Examination Time Table (Odd Term-Semester VII) Electrical Engineering Department

B. Tech. (Electrical, Electronics and Electronics & Telecommunication)

Day & Date	Time	B. Tech Electrical	B. Tech Electronics	B. Tech EXTC
28/09/2020	12 to 1 pm	OPEN ELECTIVE	OPEN ELECTIVE	OPEN ELECTIVE
Monday	12 to 1 pili			
29/09/2020	12 to 1 pm	Power Quality	Microwave & Optical	Fiber Optic Communication
Tuesday	12 to 1 pm	(EE4001S)	Communication (EC4001S)	(ET4001S)
30/09/2020	12 to 1 pm	Digital signal Processing	Data Communication &	Digital Image Processing
Wednesday	12 to 1 pili	(EE4002S)	Networking (EC4002T)	(ET4002S)
01/10/2020	12 to 1 pm	Drives & Control	Embedded Systems	Antenna Theory
Thursday	12 to 1 pm	(EE4003T)	(EC4003T)	(ET4003T)
03/10/2020 Saturday	12 to 1 pm	Biomedical Electronics	Error Correcting Codes	Radio Frequency Circuit Design
		(EE4101T)/	(EC4102T)/ Block Chain	(ET4101T)/ Block Chain
		FACTS (EE4102T)	Technology (CO4118T)	Technology (CO4118T)
05/10/2020	12 to 1 mm	-		
Monday	12 to 1 pm		_	-

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Dean (AP)

Head of Electrical Engineering Department

Examination coordinator