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PhD Comprehensive Examination
Department of Computer Science and Engineering

Date: 14-March-2013

Time: 4 Hours

Maximum Marks: 100

[Answer any five questions from Group A, and any five questions from Groups B and C combined.]

Group A

A.1 Write only the answers of the following questions. No need to write any explanation.

(a) Write the asymptotic upper bound of $T(n) = T(3n/10) + T(7n/10) + 10n$. (2)

(b) The array representing a max-heap is

| | | | | | | | | | |
|----|----|----|---|---|---|---|---|---|---|
| 20 | 12 | 10 | 8 | 4 | 7 | 6 | 5 | 2 | 3 |
|----|----|----|---|---|---|---|---|---|---|

Write the value of the element stored in the parent node of the node containing the smallest element, i.e., '2'. (2)

(c) Given an unordered list L of n integers, what is a tight lower bound on the worst case time complexity to find a/the pair of elements from L , which have the minimum difference among all possible pairs in L ? (2)

(d) What is a tight lower bound on the average-case time complexity for successful search in a complete binary tree (which is not necessarily a binary search tree). (2)

(e) An undirected graph G containing n nodes and $O(n \log n)$ edges is represented by an adjacency matrix. Write the time complexity that will be required by an optimal algorithm to prepare the adjacency list of G from its adjacency matrix. (2)

A.2 $G(V, E)$ is an undirected, unweighted graph, represented by an adjacency list. It is known that G is a forest comprising of some trees. Suggest an efficient algorithm to find a set of edges $F \subset (V \times V) - E$ so that $G'(V, E \cup F)$ is a single tree (i.e., find a set of edges from $(V \times V) - E$ to connect all the trees into a single tree). Explain its time complexity. (7 + 3 = 10)

A.3 T is a height-balanced binary search tree containing n distinct integers. Suggest an algorithm to report the elements of T lying in the interval $[a, b]$. Assume that $n \gg b - a$. Explain its time complexity as a function of n, a, b . (7 + 3 = 10)

A.4 Write a C function `int SubPal(char *S, int *start, int *len)` that takes a null-terminated character string S as a parameter, and checks if any substring of S is a palindrome (a substring is any set of consecutive characters in the string). The function returns 0 if no such substrings are found. It returns 1 if at least one such substring exists, in which case the starting index of the substring is returned in $start$ and the length of the substring is returned in len . If more than one such palindrome substrings exists, the starting index and length of any one such substring is returned.

For example, if $S = \text{"character"}$, then the function returns 1 and sets $*start = 2$ and $*len = 3$ for the palindrome substring "ara". However, if $S = \text{"palindrome"}$, the function returns 0, and the values in $*start$ and $*len$ are irrelevant (can be anything).

(10)

A.5 You are to implement a hash table to store at most 1000 integers. The hash function used for any key k is $k \% 1000$. The collision resolution scheme used is open addressing.

(a) Define a datatype `HASHTABLE` to represent a hash table to store at most 1000 integers.

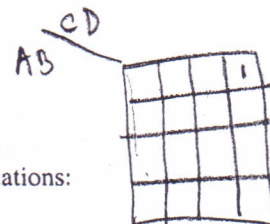
(b) Write a C function `int Insert(HASHTABLE H, int k)` that inserts a key k into a hashtable H . The function returns 1 if the insert is successful, 0 if the table is full.

- C.3 (a) Can a non-preemptive CPU scheduler be called when some I/O that a process is waiting for completes? Justify your answer clearly.
- (b) "The page table of a process is kept in main memory. To access anything in main memory in a system with logical addressing, the translation of a logical address to a physical address is needed. Therefore, to access the page table itself, a logical address must be converted to a physical address, which cannot be done without accessing the page table. Hence, paging cannot be implemented" - where is the flaw in this argument? Justify your answer clearly.
- (c) Does a context switch have any effect on the TLB? Justify your answer.

(3 + 4 + 4)

- C.4 (a) Implement the four Boolean functions listed using three half-adder circuits:

$$\begin{aligned} D &= A \oplus B \oplus C \\ E &= A'BC + AB'C \\ F &= ABC' + (A' + B')C \\ G &= ABC \end{aligned}$$



- (b) Design a sequential circuit with JK flip-flops to satisfy the following state equations:

$$\begin{aligned} A(t+1) &= A'B'CD + A'B'C + ACD + AC'D' \\ B(t+1) &= A'C + CD' + A'BC' \end{aligned}$$

(5 + 5 = 10)

- C.5 (a) You are an engineer at Outtel, a start-up company aspiring to compete with Intel via its new processor technology that outperforms the latest Intel processor by a factor of 2.5 on floating point instructions. To achieve this level of floating point performance, the design team had to make compromises that led to a 20% average increase in execution times of all other instructions. You are in charge of choosing benchmarks that would showcase Outtel's performance edge.

- (i) What is the minimum required fraction f of time spent on floating point instructions in a program on the Intel processor to show a speed up of 2 for Outtel?
- (ii) If on the Intel processor, execution time of a floating point instruction is on an average 3 times as long as other instructions, what does the fraction in your answer to part (i) mean in terms of the instruction mix of the benchmark?

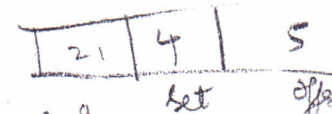
- (b) Suppose a 2KB cache has set-associative address mapping. There are 16 sets, each containing four cache blocks or lines. The memory address size is 32 bits, and the smallest addressable unit is the byte. Label the components of the 32-bit address for accessing the cache memory.

(5 + 5 = 10)

- C.6 Cache memories refer to a faster intermediate form of memory within a larger memory hierarchy. The following sequence of numbers represent memory addresses in a 64-word main memory: 0, 1, 2, 3, 4, 15, 14, 13, 12, 11, 10, 9, 0, 1, 2, 3, 4, 56, 28, 32, 15, 14, 13, 12, 0, 1, 2, 3. Classify each of the accesses as a cache hit, compulsory miss, capacity miss, or conflict miss, given the following cache parameters:

- (i) Direct mapped, 4-word lines, 4-line capacity.

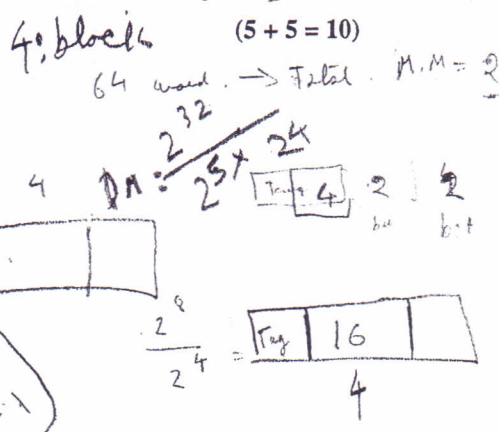
- (ii) Two-way set associative, 4-word lines, 2-set capacity, LRU replacement.



(5 + 5 = 10)

Handwritten notes on the left side of the page:

- 64 / 16 = 4
- 16 * 4 = 64
- the 64
- 4
- 2 * 2 = 4
- 2 * 2 = 4
- 0000
- 40 block = 2^4 * 2^2 = 2^6
- 1111 P
- A B C D
- 10
- 11



Group B

- (5 + 5 = 10)**

- $(4 + 6 = 10)$

- Prove that L_2 is regular.

$$L_3 = \{0^n 1^n | n \geq 0\}$$

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- $$(5 + 5 = 10)$$

Group C

- C.1 (a) List clearly how a running process can be preempted and a new process run on a system using round-robin scheduling.
- (b) An OS gives two system calls *sleep()* and *wakeup(p)*. The *sleep()* call just puts the calling process (say *x*) to sleep until it is woken up by a *wakeup(x)* call made by some other process. The *wakeup(x)* call wakes up the specified process *x* if it is sleeping; if *x* is not sleeping when the *wakeup(x)* call is made, the call has no further effect. The following C-like pseudo-code is proposed for a naïve solution to the *Producer-Consumer Problem* on this OS:

```
#define N 100 // number of slots in the buffer
int count = 0; // number of items currently in the buffer

void producer (void)
{
    int item;
    while (TRUE) {
        item = produce_item();
        if (count == N) sleep(); // if buffer is full, sleep until woken up
        insert_item(item);
        count++; // increment number of items in buffer
        if (count == 1) wakeup(consumer); // was buffer empty?
    }
}

void consumer (void)
{
    int item;
    while (TRUE) {
        if (!count) sleep(); // if buffer is empty, sleep
        item = remove_item();
        count--;
        if (count == N - 1) wakeup(producer); // was buffer full?
        consume_item(item);
    }
}
```

Unfortunately, this solution runs into trouble sometimes, with both the producer and the consumer going to sleep forever. Explain how this might happen in brief, clearly stating the possible sequence of operations that can cause this. Assume that no atomic operations are available.

(5 + 5)

- Q.2 (a) Suppose we have a program that consists of eight pages (numbered 1 through 8), and we have four page frames of physical memory for it. Find the minimum number of page faults possible for the following page reference string:

1 2 3 2 5 6 3 4 6 3 7 3 1 5 3 6 3 4 2 4 3 4 5 1

- (b) Estimate the number of page-faults for the following page reference string when the FIFO page-replacement scheme is applied to it, for two different number of available frames – 3 frames and 4 frames:

1 2 3 4 1 2 5 1 2 3 4 5

(5 + 5)