

# Abdurrahman Yaşar



CONTACT	E-Mail: <a href="mailto:ayasar@gatech.edu">ayasar@gatech.edu</a> Phone: +1 (404) 528-0697		
EDUCATION	<b>Georgia Institute of Technology, USA</b> , PhD. Computer Science 2015 - 2021 (Expected) Advisor; Ümit V. Çatalyürek <i>Dissertation</i> : Towards Performance Portable Graph Algorithms <b>Bilkent University, Turkey</b> , MSc. Computer Engineering 2013 - 2015 Advisor; Buğra Gedik <i>Dissertation</i> : Scalable Layout of Large Graphs on Disk <b>Galatasaray University, Turkey</b> , B.Sc. Computer Engineering 2007 - 2012		
SUMMARY	Ph.D. Candidate in Computer Science. Research interest in large-scale graph mining and processing. Seeking full time employment starting May'21.		
HONORS & AWARDS	<ul style="list-style-type: none"><li>• Travel Award: SIAM Conference on Parallel Processing for Scientific Computing 2020</li><li>• Two of the MIT/Amazon/IEEE HPEC 2018 Graph Challenge Innovation Awards 2019</li><li>• Travel Award: SIAM Conference on Computational Science and Engineering 2019</li><li>• One of the four invited students to Chesapeake Large-Scale Analytics Conference 2018</li><li>• One of the MIT/Amazon/IEEE HPEC 2018 Graph Challenge Champions 2018</li><li>• Excellence Study Grant Provided by the Embassy of France in Turkey 2013</li><li>• First Grade, Galatasaray University 2012</li><li>• Special Jury Award, Team ONGUN, IBM Software Academy, Turkey 2012</li><li>• Galatasaray Education Foundation (GEV) Bachelors Degree Fellowship 2008</li></ul>		
EXPERIENCE	<b>Georgia Institute of Technology</b> , College of Computing, Atlanta GA Aug. 2015 - <i>Graduate Research Associate</i> <b>Sandia National Laboratories</b> , Albuquerque NM May.-Aug'18 and May.-Aug'19 <i>Graduate Summer Intern</i> <b>IBM Almaden Research Center</b> , San Jose CA May. 2016 - Aug. 2016 <i>Graduate Summer Intern</i> <b>Inria - Lille Nord Europe</b> , Equipe DART, Lille France May. 2011 - Sep. 2011 <i>Summer Intern</i>		
RESEARCH	<b>DATA/COMPUTATION PARTITIONING</b> : Balanced distribution of the computation and the data to the processors. I studied different partitioning strategies. <ul style="list-style-type: none"><li>• Efficient spatial partitioning techniques to speed up irregular problems.</li><li>• Computation space partitioning strategies to reduce the algorithmic complexity.</li><li>• Layout techniques to increase memory utilization.</li><li>• C++ (OpenMP, TBB), Java (Hadoop)</li></ul> <b>A Novel Subgradient-based Method for d-Dimensional Rectilinear Partitioning</b> 2020 <i>submitted to IEEE International Parallel &amp; Distributed Processing Symposium (IPDPS)</i> M. F. Balin, X. An, <b>A. Yaşar</b> , L. Song and Ü. V. Çatalyürek <b>On Symmetric Rectilinear Matrix Partitioning</b> 2020 <i>submitted to SIAM Journal on Scientific Computing (SISC)</i> <b>A. Yaşar</b> , M. F. Balin, X. An, K. Sancak and Ü. V. Çatalyürek <b>Distributed block formation and layout for disk-based management of large-scale graphs</b> 2017 <i>Distributed and Parallel Databases (DPDS)</i> <b>A. Yaşar</b> , B. Gedik, H. Ferhatosmanolu		

**GRAPH MINING & BLOCK-BASED ALGORITHM DESIGN:** High-performance processing of large scale sparse graphs is crucial and pervasive. I worked on several graph mining problems.

- Proposing a novel, fast graph merging algorithm.
- Providing coarse-grained and medium-grained triangle counting formulations.
- Implementing architecture (resource)-aware parallelization techniques.
- C++ (OpenMP, Cilk, TBB)

Scalable Triangle Counting on Distributed-Memory Systems (**Graph Challenge Innovation Award**)  
2019

*IEEE High Performance Extreme Computing Conference (HPEC)*

S. Acer, **A. Yaşar**, S. Rajamanickam, M. M. Wolf and Ü. V. Çatalyürek

Fast Triangle Counting Using Cilk (**Graph Challenge Champion**) 2018

*IEEE High Performance Extreme Computing Conference (HPEC)*

**A. Yaşar**, S. Rajamanickam, M. M. Wolf, J. W. Berry, Ü. V. Çatalyürek

An Iterative Global Structure-Assisted Network Aligner 2018

*ACM International Conference on Knowledge Discovery & Data Mining (KDD)*

**A. Yaşar** and Ü. V. Çatalyürek

SiNA: A Scalable Iterative Network Aligner 2018

*IEEE/ACM International Conference on Advances in Social Networks Analysis and Mining (ASONAM)*

**A. Yaşar**, B. Uçar and Ü. V. Çatalyürek

**PERFORMANCE PORTABILITY:** Heterogeneous computing environments requires designing flexible algorithms that can run well on various platforms. I worked on several arising issues.

- Porting graph merging algorithm to an emerging architecture (Emu Chick)
- Proposing a triangle counting formulation for heterogeneous systems
- C++ (OpenMP, Cilk, TBB, Kokkos), Cuda

BBTC: A Block-Based Triangle Counting Algorithm on Heterogeneous Environments 2020

*submitted to IEEE Transactions on Parallel and Distributed Systems (TPDS)*

**A. Yaşar**, S. Rajamanickam, J. W. Berry and Ü. V. Çatalyürek

Linear Algebra-Based Triangle Counting via Fine-Grained Tasking on Heterogeneous Environments  
(**Graph Challenge Innovation Award**) 2019

*IEEE High Performance Extreme Computing Conference (HPEC)*

**A. Yaşar**, S. Rajamanickam, M. M. Wolf, J. W. Berry, J. S. Young and Ü. V. Çatalyürek

Programming strategies for irregular algorithms on the Emu Chick 2019

*ACM Transactions on Parallel Computing (TOPC) - to appear*

E. Hein, S. Eswar, **A. Yaşar**, B. Ucar, Ü. V. Çatalyürek, T. Conte, J. Riedy, R. Vuduc, and J. S. Young

PATENT

Distributing Data by Successive Spatial Partitioning 2017

*Patent : US10430104B2*

A. Gupta, S. Seshadri, **A. Yaşar**

SKILLS

- C++ (OpenMP, Cilk, TBB, Kokkos), C, Cuda, Python, Java