# MENGM0056 - Product and Production Systems Scenario 1: Smartphone Sub-assembly Line

Hand-out for Group Coursework (2025/26)

UUID seed: 8db9437c-825c-11f0-8de9-0242ac120002 Checksum: 63c075b518da

### Purpose

This scenario simulates decision-making in a mid-volume consumer-electronics sub-assembly factory. Your group receives a fixed baseline design (resources, cycle times, defect and failure characteristics, and demand). You will identify improvement opportunities, select appropriate KPIs, choose and apply techniques from the unit, and justify your proposed changes to management.

#### Narrative

A contract manufacturer assembles a mid-range smartphone. Quality issues around camera alignment and intermittent congestion at functional test have been observed during promotional spikes. Demand is expected to grow. Capital expenditure is constrained; process and policy changes are preferred.

### Entities and flow (fixed structure)

PCB population (SMT)  $\rightarrow$  Camera module build & alignment  $\rightarrow$  In-circuit test (ICT)  $\rightarrow$  Final assembly & seal  $\rightarrow$  Functional test (FT)  $\rightarrow$  Pack.

# Baseline parameters (seeded)

### Global

| Shifts per day   | 2              |
|------------------|----------------|
| Shift length     | 7.5 h          |
| Demand (nominal) | 1014 units/day |
| Demand CV        | 0.122          |
| On-time target   | 95%            |
|                  |                |

#### **Stations**

| Resource               | Count | Time         | Quality         | Notes                        |
|------------------------|-------|--------------|-----------------|------------------------------|
| SMT lines              | 3     | 27.7 s/board | FPY 0.9977      | Parallel lines               |
| Camera alignment cells | 1     | 43.7  s/unit | Defect $0.0155$ | Rework permitted             |
| ICT bays               | 1     | 84.8 s/unit  | Detect 0.877    | Serial/parallel as per count |
| Final assembly cells   | 2     | 66.9  s/unit | FPY 0.9737      | Manual with jigs             |

| FT rack slots     | 5 | 116.2  s/unit | False fail 0.0052 | Parallel slots; queueing |
|-------------------|---|---------------|-------------------|--------------------------|
| Rework station(s) | 1 | 113.5  s/unit | Success $0.718$   | From alignment/ICT       |

#### Reliability and logistics

| Resource      | MTBF (min) | MTTR (min) | Arrival jitter CV |
|---------------|------------|------------|-------------------|
| SMT           | 253.1      | 22.3       | 0.08              |
| Alignment     | 185.6      | 10.7       | 0.083             |
| ICT           | 389.8      | 17.5       | 0.066             |
| FinalAssembly | 214.7      | 18.4       | 0.079             |
| FT            | 336.3      | 20.9       | 0.074             |

#### Costs

| Scrap cost per unit         | £21.98 |
|-----------------------------|--------|
| Rework labour cost per hour | £27.41 |

### Required KPIs

- First-pass yield (FPY) by station and rolled throughput yield (RTY).
- Throughput (units/day), on-time delivery probability, and average lead time.
- Work-in-progress (WIP) before FT and maximum queue length at FT.
- Rework rate and rework hours/day; scrap cost per unit.

### Techniques to apply (choose appropriately)

- Modelling & KPIs: KPI definitions, RTY ladder, capacity calculations.
- CAE: Camera alignment jig/tolerance stack-up if you propose design changes affecting quality or time.
- Mathematical programming: Staffing and test-bay/slot scheduling; buffer sizing under constraints.
- Uncertainty modelling: Demand, defect, test time variability, breakdowns; Monte Carlo assessment of service level.
- **Simulation**: Discrete-event simulation of the line (bottlenecks and rework loop). Agent-based modelling is optional if human-cobot interactions are relevant.
- Metaheuristic optimisation: Parameter tuning for conflicting objectives (e.g., reduce defect rate without increasing cycle time beyond takt).

# Improvement levers (examples, not exhaustive)

- Realignment of staffing across ICT and FT; time-of-day pooling of testers.
- Buffer policy revision to avoid blocking before FT.
- Tolerance/jig updates informed by CAE to cut alignment defects.
- Preventive maintenance intervals to reduce micro-stoppages at FT.
- Rework routing policies (thresholds for scrap vs. rework).

#### **Deliverables**

1. A report (max 20 sides of A4 including figures and references; appendices unmarked but admissible as evidence).

- 2. The report should include an executive summary for senior management.
- 3. Model files (e.g., simulation, optimisation) as appendices/evidence.

## Assessment emphasis

Clarity of problem framing and KPI choice; correctness and transparency of models; appropriateness of technique selection; quality of experimental design; depth of analysis; and persuasiveness of recommendations given operational constraints.

## Data ethics and reproducibility

Report your UUID seed and any random seeds used within tools to ensure reproducibility. State assumptions clearly.