Flowchart:

Start

Extend 21-bit fraction to a 32-bit significand

Is sign bit 1? —Yes→ 2's complement the significand

No

Compare exponents and shift significand of smaller number to match the exponent

Add significands

Is sign bit 1? —Yes→ 2's complement the significand

No

Normalize the resultant significand

Overflow or underflow? —Yes→ Signal overflow or underflow accordingly

No

Round the significand

Normalized? —No→

Yes

End