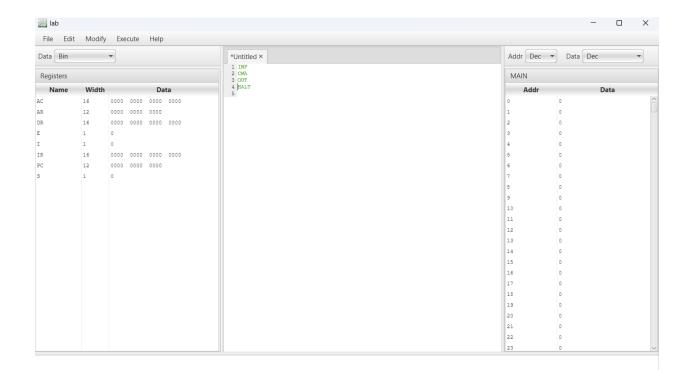
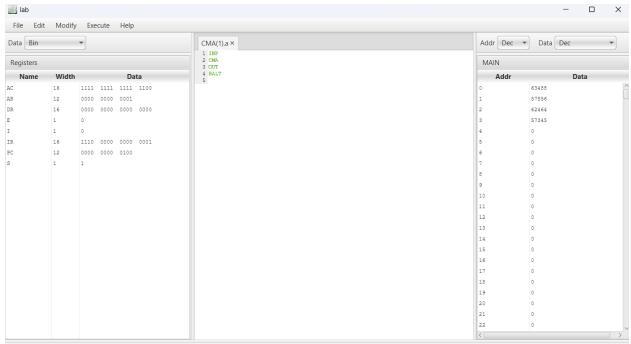
Ayesha Zubair
<u>52916</u>
<u>Lab 12</u>
<u>Tasks</u>
Task 1:
Verify that CLA sets the accumulator (AC) to zero.
INP
OUT
CLA
OUT
HALT
EXECUTING Enter Inputs, the first of which must be an Integer: 9 Output: 9 Output: 0 EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
Task 2:
Verify that CMA inverts all bits in the AC.
INP
CMA
OUT
HALT



Output:



EXECUTING...
Enter Inputs, the first of which must be an Integer: 3
Output: -4
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]

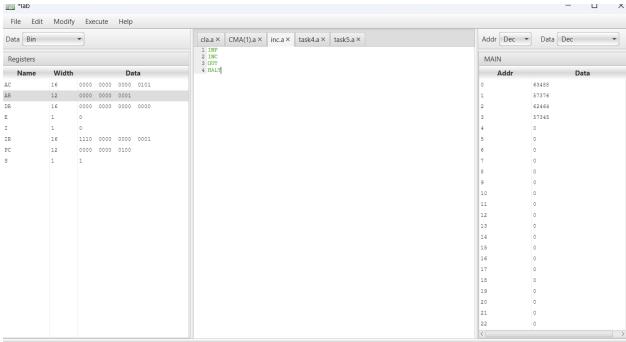
Task 3:

INP

INC

OUT

HALT



EXECUTING...
Enter Inputs, the first of which must be an Integer: 2

Output: 5
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]

Task 4:

Skip next instruction only if AC is positive.

INP

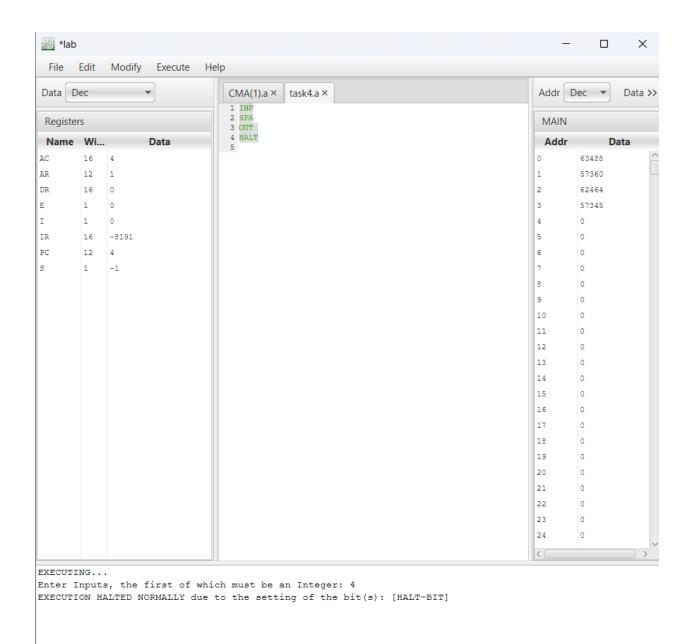
SPA

OUT

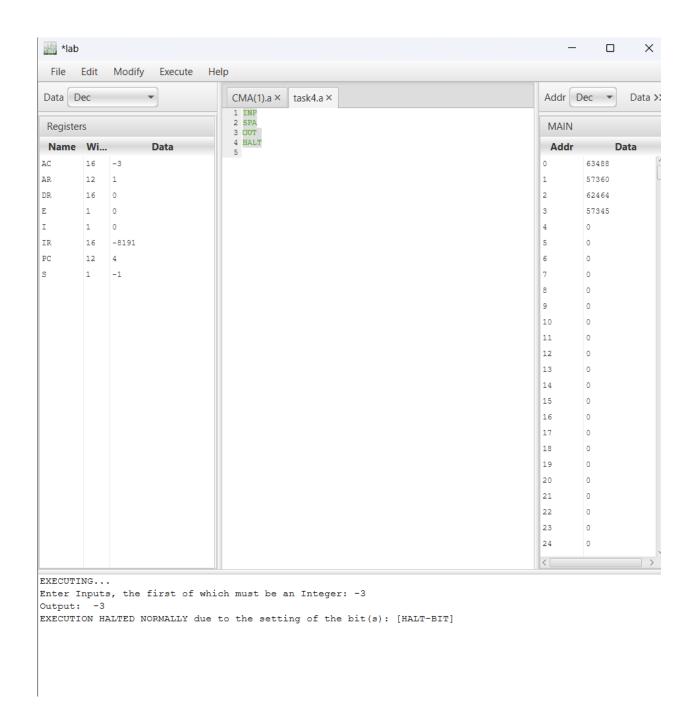
HALT

Output:

On entering positive number:



On entering negative number:



Task 5: Skip next instruction only if AC is negative.

INP

SNA

OUT

HALT

Output:

On entering negative number:

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: -2
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```

On entering positive number:

```
EXECUTING...
Enter Inputs, the first of which must be an Integer: 10
Dutput: 10
EXECUTION HALTED NORMALLY due to the setting of the bit(s): [HALT-BIT]
```