



Faculty of Information Technology

Electrical and Computer Engineering Department

OPERATING SYSTEMS (ENCS3390)

Project Report

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Abstract

Second Chance and Clock, the even ones in the algorithms given in the project description (2,4) depending on the max value of the least significant digit of the team ID numbers, were implemented in this project as a simulator for experimenting with page replacement methods with threading and Scheduling. We also created a user interface that is both straightforward and comfortable to use.

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Theory

A FIFO replacement algorithm is the foundation of the second-chance replacement algorithm. However, after a page has been identified, we examine its reference bit. If the value is 0, we will replace this page; however, if the value is 1, we will give the page a second opportunity before moving on to the next FIFO page. A page's reference bit is cleared and its arrival time is reset to the current time when it receives a second opportunity. As a result, a second-chance page will not be changed until all other pages have been replaced (or given second chances). Furthermore, a page will never be changed if it is utilized frequently enough to maintain its reference bit set.

A circular queue is one approach to implement the second-chance algorithm (also known as the clock algorithm). The next page to be replaced is indicated by a pointer (a hand on the clock). The pointer advances until it reaches a page with a 0-reference bit when a frame is required. It clears the reference bits as it progresses. When a victim page is located, it is replaced, and a fresh page is inserted in its place in the circular queue. When all bits are set in the worst scenario, the pointer cycles over the whole queue, giving each page a second opportunity. Before picking the next page for replacement, it clears all the reference bits. If all bits are set, second-chance replacement degenerates into FIFO replacement.

Within a process, a thread is a path of execution. Multiple threads can exist in a process. The lightweight process is also known as a thread. By splitting a process into many threads, concurrency may be achieved.

Process Scheduling is a job in the operating system that schedules processes in various stages such as ready, waiting, and running. Process scheduling allows the operating system to assign each process a time period for CPU execution. Another significant advantage of employing a process scheduling system is that it keeps the CPU active at all times. This enables you to get the quickest possible reaction time for applications.

Procedure

Implementation:

We utilized the Java programming language in our implementation. Main Class, Memory Class, TableData Class, Process Class, Memory Table class, and Result Table class are the java classes we created. Memory Class was like a single memory cell, consisting of a page and its bit reference, and so the memory was an array of Frames, Process Class was like a single memory cell, consisting of the process attributes (burst time, arrival time, start time, finish time, etc...), and so processes were java objects from this class, TableData class was like a single memory cell, consisting of the page number and the pid of the process that owned this page, and finally, Main class was the class that holds all the processing of the data and functions. Result Table class are the java classes were for the GUI.

Running Our Program

Our interface is very easy to use, as shown in the following screenshot, you just need to select a file from the computer then click the “Read“ button to input the data. After that you need to input memory size and time quantum, select an algorithm and click “Run” to run the program.

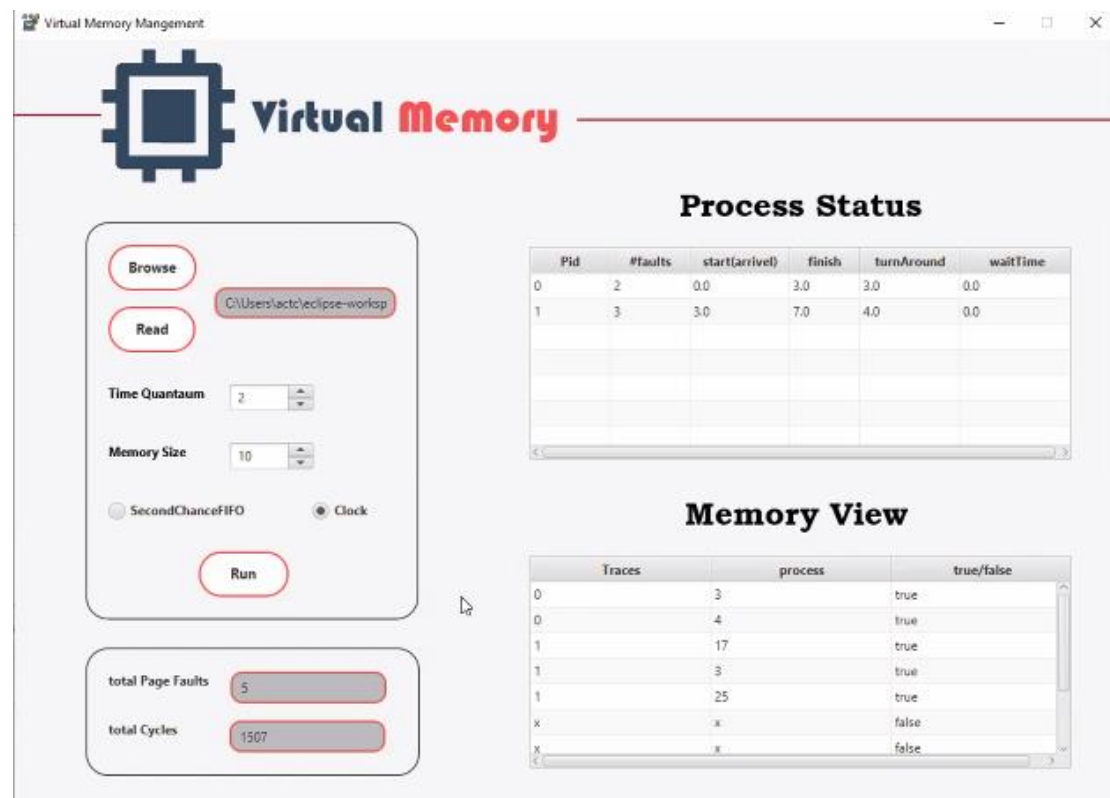


Figure 1: Program Interface

Input Files Structure

The structure of our program's input file should be similar to the following screenshot. The first three are used as the same way the input file structure format decided in the project file, the fourth and fifth lines will be skipped (but are required for our code), and are just included to make it easier to enter and read data from the file. The "|" separator is required between each line's numbers (but the spaces are not, you can enter data without spaces). And at the end of each line the traces in hexadecimal are separated by commas “,”, and in the program each trace first 12 lower bits will be deleted to produce a page number.

```
1 2
2 10
3 2
4 PID      Start      Duration      Size in pages      Memory traces
5 .....
6 0 |      0 |      3 |      3 |      00003fff,00004fff,00003fff
7 1 |      5 |      4 |      4 |      00011efa,00003fad,00011daa,00019aab
```

Figure 2: Input Files Structure

Test Case 1

```
1 2
2 10
3 2
4 PID      Start      Duration      Size in pages      Memory traces
5 .....
6 0 |      0 |      3 |      3 |      00003fff,00004fff,00003fff
7 1 |      5 |      4 |      4 |      00011efa,00003fad,00011daa,00019aab
```

Figure 3: Small Input Data File

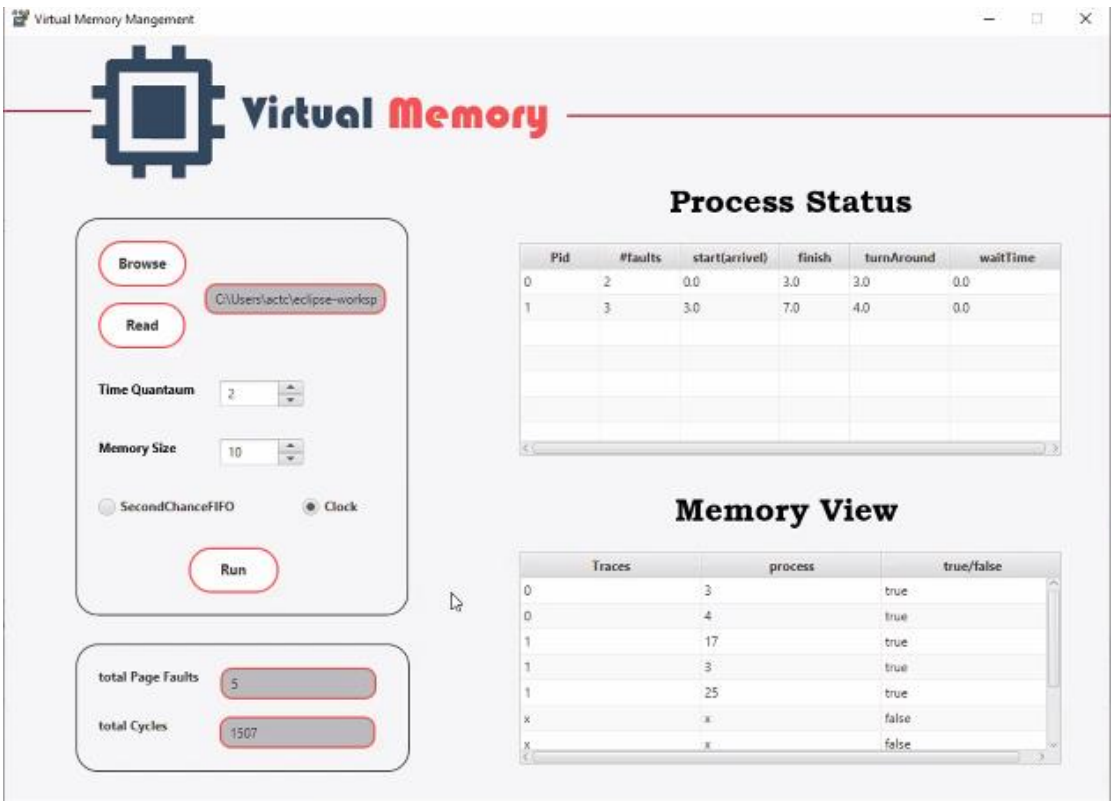


Figure 4: Small Input Clock Result

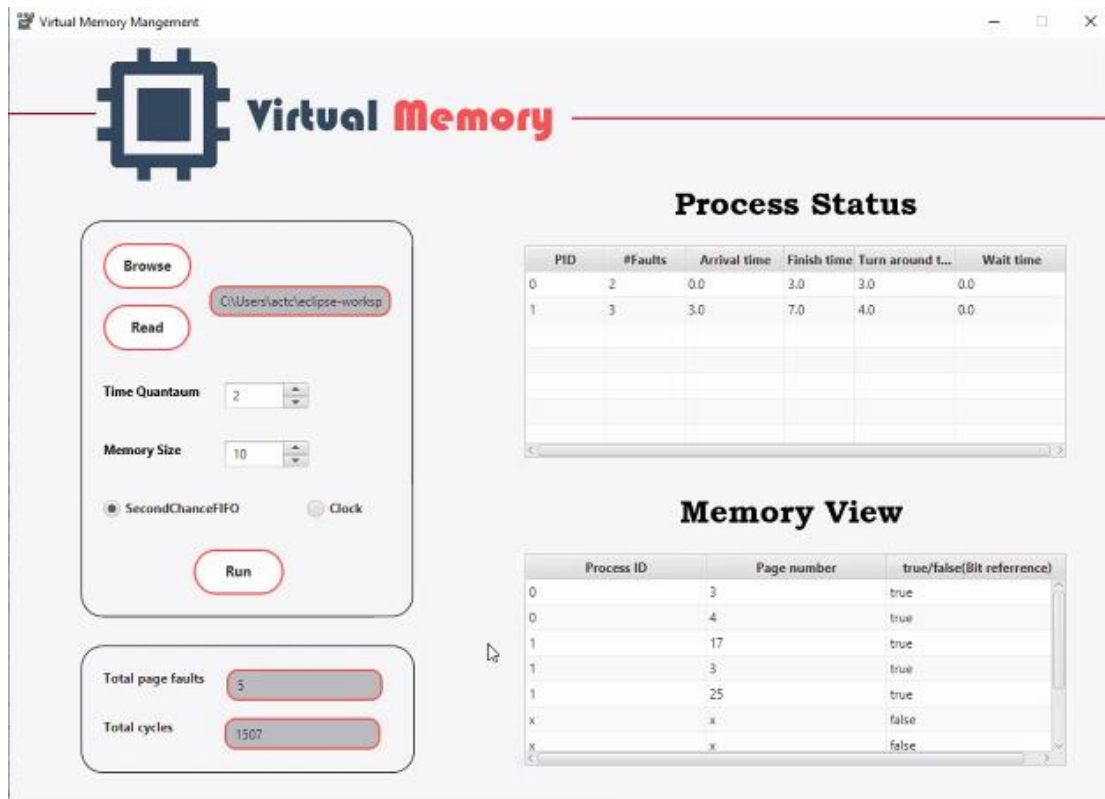


Figure 5: Small Input Second Chance FIFO Result

```

-----Our system thread synchronuization-----

Round Robin Thread
Process 0 own Thread
Clock Algorithm Thread
Clock Algorithm Thread
Process 0 own Thread
Clock Algorithm Thread
Clock Algorithm Thread
Process 1 own Thread
Clock Algorithm Thread
Clock Algorithm Thread
Process 1 own Thread
Clock Algorithm Thread
Clock Algorithm Thread

```

Figure 6: Small Input Threading Results

Test Case 2

| | | | | |
|------|----|-----|-----|--|
| 125 | 14 | 16 | 16 | 47dfcb,1388cd,78e083,36362a,8771cf,75c37e,b12025 |
| 100 | 0 | 7 | 7 | 0c1962,0f3ef5,730683,7145fd,ac1600,60066f,75c37e |
| 2 | 0 | 10 | 10 | 46d587,c3d09f,c0d097,42da00,69179b,46d587,c3d09f,c0d097,42da00,69179b |
| 2 | 1 | 8 | 8 | 941b29,a30a28,009522,a41029,a30a28,009522,a41029,a30a28 |
| 104 | 1 | 40 | 40 | 16cf19,bdf042,202587,901a08,c4fbc3,d1a06f,c78463,4667d7,6d2405,8e291c,e43dc1,4490de,24fca7,747e09,398bad,40545c,e18f0f,1e190a,f3b30b,ecb3ef,16cf19,bdf042,202587,901a08,c4fbc3 |
| 115 | 3 | 12 | 12 | a159f3,8a61bc,d10824,7044dd,f83800,9659fe,a159f3,8a61bc,d10824,7044dd,f83800,9659fe |
| 116 | 0 | 6 | 6 | 163a0b,32c375,a102ba,e04a28,163a0b,32c375 |
| 117 | 9 | 50 | 50 | 08aa00,25a5c7,492d2c,e6b38b,c3e383,fbd0e9,202587,3886ad,e7b72c,62ca79,7bb3b2,dec8ef,f7eaa1,b0451b,7176d3,65cf3f,ac2b7f,500a97,541313,d05ca0,41316a,5781fb,54b7d7,2e41bd,1da27c |
| 148 | 8 | 30 | 30 | 8bce9f,7ce415,a2918b,653e34,d1dc08,5278ab,40c3bd,e0a57f,b7044e,b00a1e,62cd05,acc707,f82258,8cd132,c473aa,8bce9f,7ce415,a2918b,653e34,d1dc08,5278ab,40c3bd,e0a57f,b7044e,b00a1e |
| 159 | 10 | 20 | 20 | 3c838b,f720ff,a0220a,c93631,c2539c,bdb075,20a034,51a226,f944ff,55f57e,3c838b,f720ff,a0220a,c93631,c2539c,bdb075,20a034,51a226,f944ff,55f57e |
| 1610 | 11 | 143 | 143 | fbc747,28a30b,2e5f87,12b413,73d484,1c1d83,c94807,bd50e1,c603d8,838e61,c13c08,f6f812,1cf28d,3f2096,154b02,50958a,f7bd25,1ec830,c78c65,a2d54b,30361e,0e774e,8b09fd,a0453d,220522 |
| 1711 | 13 | 100 | 100 | e94c4d,304e73,ac1830,8e40b0,905f73,50444a,75820b,70b8fd,5c4cfc,5050bf,f955de,1c4c3b,9cf538,56329c,b45a08,f08080,57d34f,bd027c,830a0e,4d7dc7,1c6114,6080ee,f4059a,e9809d,91d7e1 |
| 1812 | 14 | 13 | 13 | 347745,71dfe5,9f53fd,cab44b,155f09,504095,0aefee,347745,71dfe5,9f53fd,cab44b,155f09,504095 |
| 1913 | 15 | 9 | 9 | 384e73,ac1830,8e40b0,905f73,50444a,75820b,70b8fd,5c4cfc,5050bf,f955de,1c4c3b,9cf538,56329c,b45a08,f08080,57d34f,bd027c,830a0e,4d7dc7,1c6114,6080ee,f4059a,e9809d,91d7e1 |
| 2014 | 15 | 3 | 3 | 508a97,541313,d05ca0 |
| 2115 | 9 | 5 | 5 | f3b30b,ecb3ef,16cf19,bdf042,202587 |

Figure 7: Big Input Data File

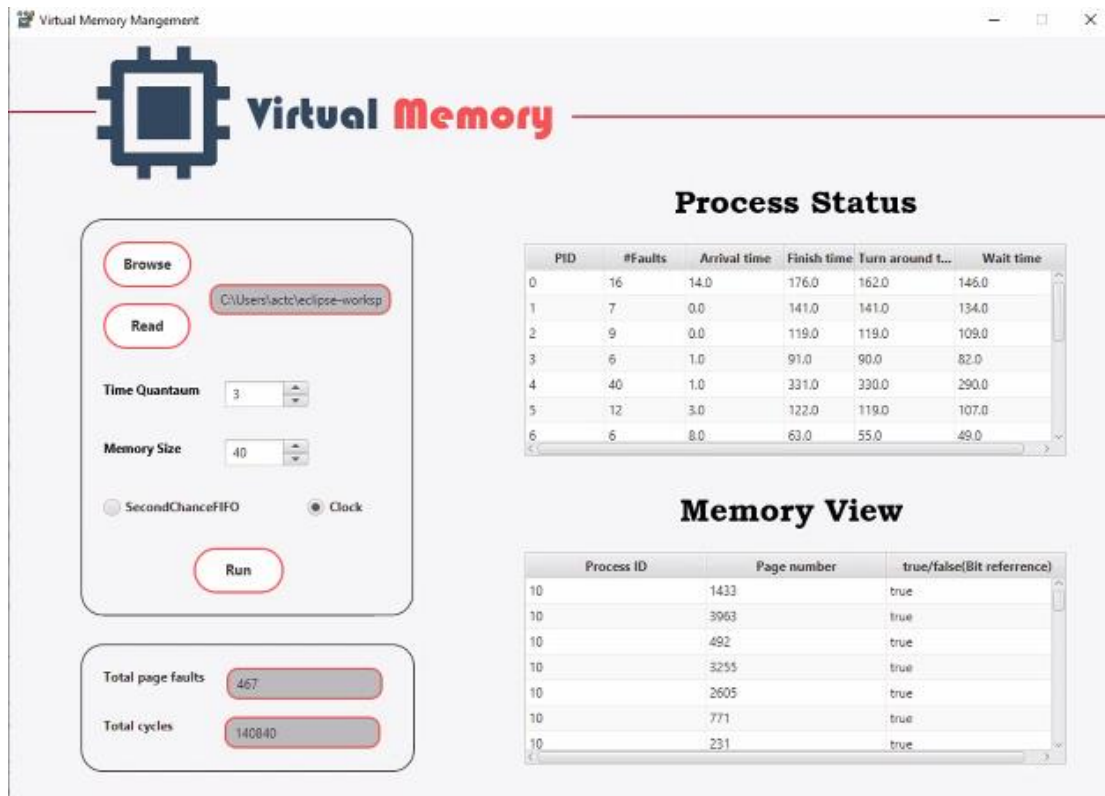


Figure 8: Big Input Result in Clock and 40 Memory Size

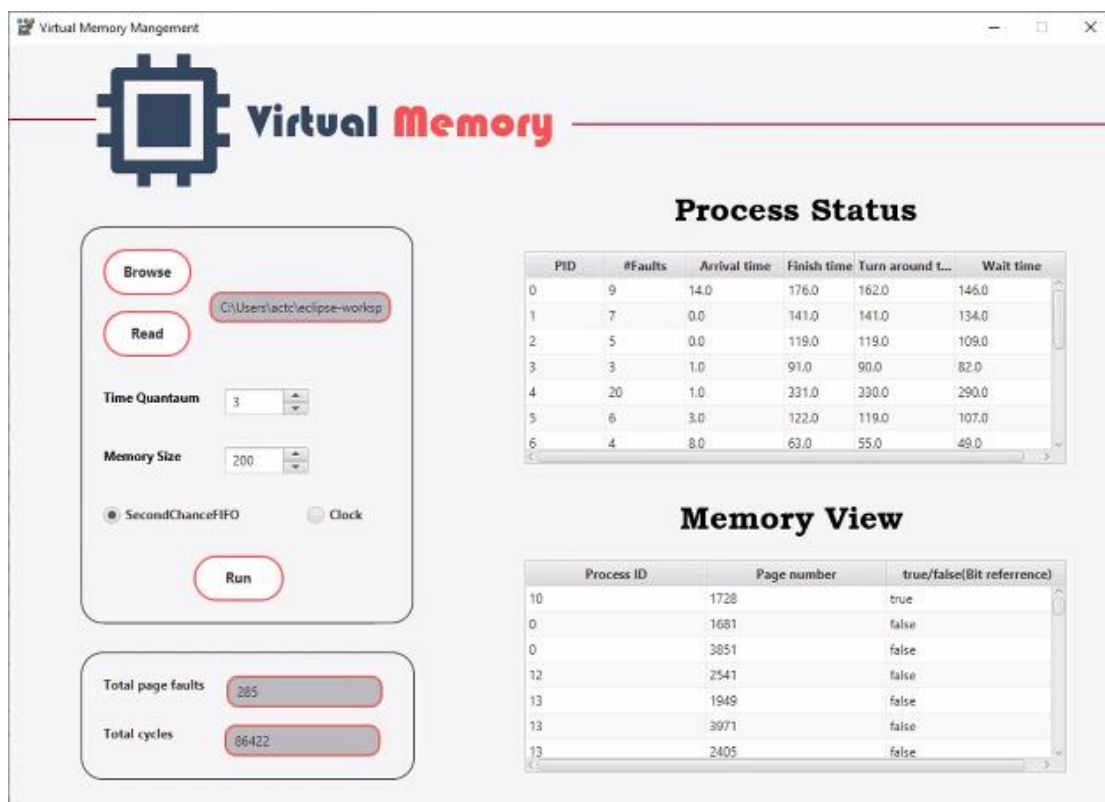


Figure 9: Big Input in Second Chance FIFO and 200 Memory Size

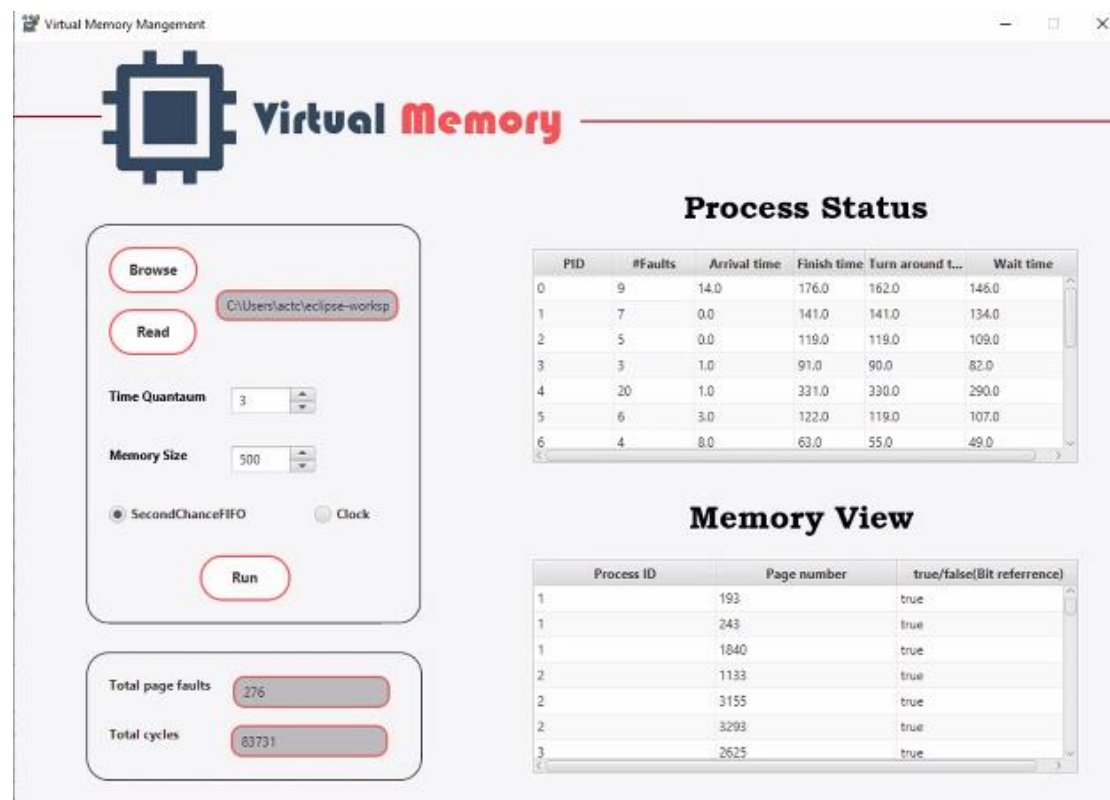


Figure 10: Big Input in Second Chance FIFO and 500 Memory Size

Conclusion

This project allows users to view and test several page replacement methods, compare them, and determine which is best for a certain purpose, as some algorithms may produce fewer page faults than others, while others may be better in other situations. Simulating algorithms is beneficial because it allows us to evaluate their efficiency before putting them into practice, and it was great to have a project to teach us how to do so. The threading part was useful to understand how threading work and how to implement it but a downside of the project is that threading doesn't change the output so we couldn't know if it was working or not without debugging.

References

- Operating System Concepts, 10th Edition.
- Geeks for Geeks, [\[Link\]](#).
- Baeldung, [\[Link\]](#).
- Stack Exchange, [\[Link\]](#).
- EXAMRADAR, [\[Link\]](#).
- JENKOV, [\[Link\]](#).
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