You will need to obtain the signature of your instructor or TA on the following items in order to receive credit for your lab assignment. This assignment is due by Friday, October 17, 2014 (Required Elements) and Wednesday, October 22, 2014 (Supplemental Elements). Labs completed after the due date will receive grade reductions.

Print your name below, sign the honor code pledge, circle your course number, and then demonstrate your working hardware & firmware in order to obtain the necessary signatures. All items must be completed to get a signature, but partial credit is given for incomplete labs. Receiving a signature on this signoff sheet does not mean that your work is eligible for any particular grade; it merely indicates that you have completed the work at an acceptable level.

is eligible for any particular grade; it men			L			
Student Name: Ali Toma	1	entre disputator i de comissi qui proclimato de son.				
Honor Code Pledge: "On my honor, as a unauthorized assistance on this work. I h					eceived	
	Student S	ignature: /	\mathcal{U}_{-}			
Signoff Checklist						
Required Elements BY Schematic of acceptable quality (all Pins and signals labeled, decoupling Very good knowledge of a terminal Demonstrates all 32KB of XRAM in Using PAULMON2, demonstrates h Knows how to use SDCC [Code::Black Knows how to analyze output files (C serial program and virtual debug p Hex display of buffer contents Supplemental Elements (Qualifies students) PWM controt works correctly Correctly enters Idle mode and exits Correctly enters Power Down mode All other PCA software menu items Good understanding of PCA modes	capacitors, and emulator in memory map highest baud rate ocks IDE or man. RST, .MEM, continuational interest for higher given external in	are functional te as: 576 ake optional] MAP) for cornand code commercade)	, including moni	tor block fill co	ommand 0/16/14	
Good user interface; program is easy to use		Mary 10/22/2014				
Instructor/TA Comments:			TA signature and	d date		
FOR INSTRUCTOR USE ONLY	a allege allegations of the control					
Required Elements Schematics, SPLD code Hardware physical implementation Required Elements functionality Sign-off done without excessive retries Student understanding and skills Overall Demo Quality	Not Applicable	Below Expectation	Meets Requirements	Exceeds Requirements	Outstanding	
Required Elements Schematics, SPLD code Hardware physical implementation Required Elements functionality Sign-off done without excessive retries Student understanding and skills Overall Demo Quality FOR INSTRUCTOR USE ONLY Supplemental Elements	Applicable			Requirements	Outstanding	
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Required Elements Schematics, SPLD code Hardware physical implementation Required Elements functionality Sign-off done without excessive retries Student understanding and skills Overall Demo Quality FOR INSTRUCTOR USE ONLY Supplemental Elements Supplemental Elements Sign-off done without excessive retries	Applicable	Expectation	Requirements	Requirements	Outstanding	

Optional Challenge: C and Assembly interfacing
Optional Challenge: Secial ISR
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Optional Challenge: Floating point and X2 mode

** Murify PLD Logic

- Schematics - WR not connected