

SO2 session 24 lecture 6 (from
segmentation to paging + PAE)

Linear mapping

Particular mapping from virtual
page to physical page

PAE (Physical address extension) :
from 32 bits (4Go to 36 bits 64Go
)

if we have 8 Go ram linux can't
mapping all the size only 4 Go so
that why we use PAE

1) Cpu 16 bit : work with
segmentation (segment),
segment register (CS,SS,DS,
offset,..),
segmentation unit (input :
segment adress + offset)
// in this we can only adress 1MB
SO we add 4 bits
// problem of security

we solve it by paging

size entries = $1024 * 1024 * 4 \sim 5\text{MB}$

to duplicate memory we just
duplicate table

