32th IFIP/IEEE International Conference on Very Large Scale Integration
Tangier, Morocco, October 6-9, 2024
Call for Papers

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VLSI-SoC 2024 is the 32th in a series of international conferences sponsored by IFIP WG 10.5, IEEE CEDA, and IEEE CASS, which explore the state-of-the-art in the areas of Very Large Scale Integration (VLSI) and System-on-Chip (SoC) design. The purpose of VLSI-SoC is to provide a forum to exchange ideas and showcase academic as well as industrial research in architectures, circuits, devices, design automation, verification, test, and security, within digital, analog, and mixed-signal systems. VLSI-SoC'24 will take place at hotel Barcelo, Tanger, Morocco, and it is organized by the Delft University of Technology.

VLSI-SoC 2024 will be held under the theme "SoCs for 5G Evolution and 6G" exploring the design and optimization challenges around 5G+6G communication systems and devices embedded systems, security, low power circuits, powered by CMOS and beyond CMOS-technologies.

The areas of interest include (but are not limited to) the following topics:
- Analog, mixed-signal, RF, and sensors
- Digital circuits, low-power design, SoC, NoC, and reconfigurable architectures
- Design for AI acceleration and machine learning for SoC design
- Circuits and systems for signal processing and communications
- Modelling, simulation, verification, and prototyping
- EDA tools and methodologies for IC design
- Design for testability, reliability, and fault tolerance
- Hardware security
- IoT, embedded and cyber-physical systems: Architecture, design, and Software
- Emerging computing paradigms: Quantum computing, cryogenic processors
- 5G / 6G emerging technologies

Key Dates:		
	Abstract Submission	1 April 2024
	Final Paper Submission	8 April 2024
	Author Notification	1 June 2024

Further Information:

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