```c
/* file: clocks.c */
/* Author - Onisokien Ayonoadu
            Priscilla Chua, Mar 10 2020 */

#include "stm32f10x.h"
#include "clocks.h"

void clockInit(void)
{
    RCC->CR =  0x01010081;      // Turn on PLL, HSE, HSI
    RCC->CFGR = 0x00050002;     // Turn off clock(NO clock),
                                // PLLMUL X3, PREDIV1 is PLL input

    // Wait for the PLL to stabilize
    uint32_t start = 0x00;
    do
    {
        start = RCC->CR & 0x02000000; //Check to see if the PLL lock bit is set
    }while (start != 0x02000000);

    RCC->APB2ENR = 0x001C; //turn on clocks for PORTS A, B, C

    //Enable peripheral clocks for various ports and subsystems
    //Bit 4: Port C Bit3: Port B Bit 2: Port A
    RCC->APB2ENR |=  RCC_APB2ENR_IOPAEN ;
    RCC->APB2ENR |=  RCC_APB2ENR_IOPBEN ;
    RCC->APB2ENR |=  RCC_APB2ENR_IOPCEN ;
}

//void delay(uint32_t count)
//{
//    int i=0;
//    for(i=0; i< count; ++i)
//    {
//    }
//}
```