Authorized documents: slides and personal notes from the lectures. December 14, 2022 - Duration: 2 hours.

- Cell phones should be turned off and stored in bags.
- This document contains 2 pages.
- Please avoid using a pencil.
- · The grading points are indicative, not definitive.

Exercice 1 - Warm-up (4 points)

 What is the difference between the synchronous mode and the eager mode when sending MPI messages? What are their respective advantages and inconvenients?

Which mode corresponds to the standard send (MPI_Send())?

2. For best GPU performance, should we use one thread per GPU core? or multiple threads per GPU core?

Exercice 2 - Heat diffusion on a grid (16 points)

given by the following equation: of the diffusion phenomenon. The computation of the temperature $u_{i,j}^{t+1}$ for the point of coordinates (i,j) at time t+1 is We study the phenomenon of heat diffusion on a grid over several time steps. The temperature at each grid point is implemented by a floating-point number (in single precision). In this subject, we limit ourselves to a simplified version

$$u_{i,j}^{t+1} = \frac{1}{5} \left(u_{i-1,j}^t + u_{i,j-1}^t + u_{i+1,j}^t + u_{i,j+1}^t + u_{i,j}^t \right) \tag{1}$$

neighbors (North, South, East, West). Each point therefore retains a fifth of its former value, to which are added the contributions received from its four direct

The algorithm ends when $\Delta < \epsilon$, where the error Δ is defined as:

$$\Delta = \max_{\forall (i,j)} \left| u_{i,j}^{t+1} - u_{i,j}^{t} \right|.$$

compute for the time t+1 the grid #1 using the grid #0. At each time step, the grids are switched. Each grid is stored as a 2-dimensional array of size $n \times n$. We will generally consider that n >> p, where p is the number of processes. A possible implementation of this algorithm relies on two grids: the grid #0 containing the values at time t, we

 $u_{i+1,j}$ will correspond to the values $u_{0,j}^{t}$. The same goes for the first and last columns. first line (i = 0), the values $u_{i-1,j}^t$ will correspond to the values $u_{n-1,j}^t$. And for the last line (i = n - 1), the values In order to simulate an infinite grid, the computations on the edges will be performed periodically. Namely, for the

the grid are initialized at 25 degrees, except for some columns initialized at 100 degrees. In practice, we have a function init (G, h, 1) which initializes a grid of size h x 1 starting at address G: all points of

The sequential algorithm is given in Algorithm 1.

We first consider a multi-process parallelization, using p MPI processes, p divisor of n.

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Algorithm 1 Sequential algorithm of heat diffusion on a grid Require: n and ϵ

1: memory allocation for grids G[0] and G[1] of size $n \times n$

- 2: init(G[0], n, n)
- 3: 9 + 0
- 4: repeat
- computation of G[1-g] from G[g], and of Δ between G[1-g] and G[g] $g \leftarrow 1 - g$
- 7: until $(\Delta < \epsilon)$
- 8: save G[g] (in a file)

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- 9: memory release of G[0] and G[1]
- 1. (1 point) How to update the termination condition in parallel? How can you implement this in MPI? (just give the principle)
- 2. (1 point) Which type of load balancing is relevant for this parallelization? Justify.
- 3. (6 points) Write the corresponding parallel algorithm in MPI using a 1D block distribution in the i dimension. You G_source. This function also returns the corresponding Δ value. of the grid of size $h \times l$ starting at address G_larger, from lines 0 to h-1 of the grid of size $h \times l$ starting at address will use the function compute (G_target, G_source, h, 1) which computes (according to equation (1)) lines 1 to h-2

its important parameters but you can omit the exact C programming syntax. All the local variables and their type indicating (for example) which lines are concerned. For the MPI functions, you will specify the function used and As far as MPI communications are concerned, you can avoid specifying the details regarding pointers by precisely

The complete grid will be collected by the process with rank ROOT in order to be written to disk

4. (1 point) Is it possible to overlap communications by computations?

If yes: present the principle of the corresponding algorithm; otherwise, justify your answer

We now consider a CUDA deployment of this application on one GPU, considering a CPU sequential execution (i.e.

- 5. (1 point) Is it possible to perform one single kernel call to run this application on GPU? Justify
- 6. (4 points) Write the corresponding CUDA program (with both CPU and GPU code). The memory allocations on thread blocks and of the block grid, as well as the kernel(s) code in pseudo-code or in CUDA (all details regarding GPU must be specified, as well as the required CPU-GPU memory transfers, the number of dimensions of the parallelism (indices, ...) must be clearly specified), and the kernel call(s).

The thread block size can be considered as constant here (with for example 256 threads per bloc)

- 7. (I point) How can we improve the GPU performance? (no detailed algorithms required here; only the overall ideas)
- 8. (I point) What is the compute intensity of this application? What can you (most likely) deduce about the hardware feature limiting the performance of this application on CPU and on GPU?