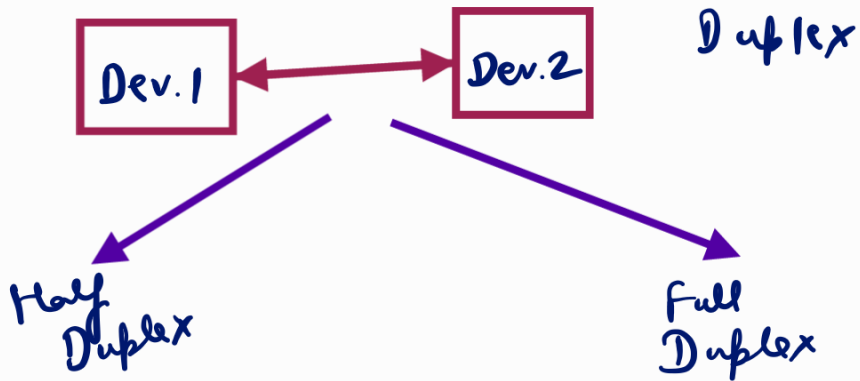
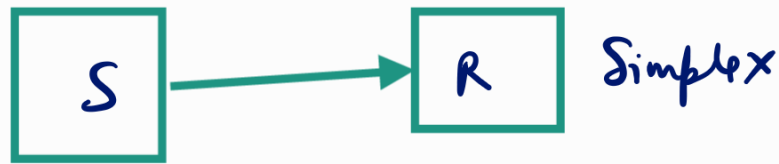


UART



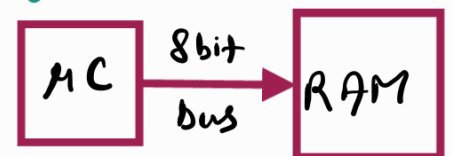
Communication

Serial
eg UART



Parallel

eg



Synchronous / Asynchronous

Both Sender & Receiver
Share Common Clock
(SPI, I2C)

Dev. [Sender Clock] → Master
[Receiver "] → Slave

Data is sent
w/o clock

eg UART

① Point-to-Point
Communication

② Multi-drop Comm \rightarrow Single Tx. Multiple Rx. SP1

③ Multi-point Comm \rightarrow Channel is shared b/w Devices

UART

① Baud rate

Speed of data transmission
Bits/Sec.

② Bit rate (bps)

e.g. 8250 \rightarrow 9600, 19200

AGENDA:

UART with only single Baud rate.

UART 16550 \rightarrow FIFO, Tx logic, Rx logic, UART Registers

