

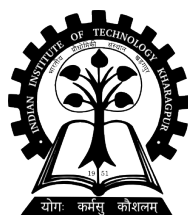
Robust Controller Design Using H_∞ -Loop Shaping Approach

*Report submitted in partial fulfillment to the requirements
for the degree of*

BACHELOR OF TECHNOLOGY

by

AYUSH PANDEY



Department of Electrical Engineering
Indian Institute of Technology, Kharagpur
West Bengal, INDIA 721302
November 2015



Department of Electrical Engineering
Indian Institute of Technology, Kharagpur
West Bengal, INDIA 721302
November 2015

Certificate

This is to certify that the report entitled **Robust Controller Design Using H_∞ Loop Shaping Approach**, submitted by **Ayush Pandey**, an undergraduate student, in the *Department of Electrical Engineering, Indian Institute of Technology, Kharagpur, India*, for the award of the degree of Bachelor of Technology, is a record of the work carried out by him under our supervision and guidance. Neither this report nor any part of it has been submitted for any degree or academic award elsewhere.

Dr. Sourav Patra
Assistant Professor,
Department of Electrical Engineering,
Indian Institute of Technology, Kharagpur,
INDIA 721302