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Title:	Stacking Engineered Room Temperature Ferroelectricity in Twisted Germanium Sulfide Nanowires
Authors:	Vasdev, Aastha (/jspui/browse?type=author&value=Vasdev%2C+Aastha) Sheet, Goutam (/jspui/browse?type=author&value=Sheet%2C+Goutam)
Keywords:	Stacking Ferroelectricity
Issue Date:	2022
Citation:	Advanced Electronic Materials, 8(5), 2101158.
Abstract:	Group-IV monochalcogenides have emerged with immense potential to be used as ferroelectric materials in recent times. However, in most of them, ferroelectricity is limited by the presence of inversion symmetry in their natural crystal structure. Here, an experimental observation of ferroelectric order at room temperature by introducing Eshelby twist in Germanium sulfide (GeS) nanowires is reported. The twisted nanowires are synthesized by low-pressure chemical vapor deposition. The existence of room temperature ferroelectricity in a single nanowire is confirmed by electrical measurements, piezoelectric force microscopy, and second harmonic generation spectroscopy. Density functional theory calculations reveal that the twist in the GeS nanowires breaks the inversion symmetry where the inversion symmetry breaking phonon modes get hardened giving rise to ferroelectricity. These results are expected to be useful in making non-volatile memory devices, flexible electronics, electronic sensors, and neuromorphic computing.
Description:	Only IISER Mohali authors are available in the record.
URI:	<a href="https://doi.org/10.1002/aelm.202101158">https://doi.org/10.1002/aelm.202101158</a> ( <a href="https://doi.org/10.1002/aelm.202101158">https://doi.org/10.1002/aelm.202101158</a> ) <a href="http://hdl.handle.net/123456789/5087">http://hdl.handle.net/123456789/5087</a> ( <a href="http://hdl.handle.net/123456789/5087">http://hdl.handle.net/123456789/5087</a> )
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