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Title:	Reconfigurable Noise-Assisted Logic Gates Exploiting Nonlinear Transformation of Input Signals
Authors:	Sinha, Sudeshna (/jspui/browse?type=author&value=Sinha%2C+Sudeshna)
Keywords:	Noise-Assisted Logic Gates Exploiting Nonlinear
Issue Date:	2022
Publisher:	American Physical Society
Citation:	Physical Review Applied, 1 (1), 14061
Abstract:	We demonstrate the direct implementation of all basic logical operations utilizing a single bistable system driven by nonlinearly transformed input signals, in the presence of noise. Exploiting the hopping between the dynamical states of the bistable system, assisted by the noise floor, in response to the transformed inputs, allows the implementation of the full set of logic operations. So this idea can form the basis of the design of a dynamical computing element that can be rapidly morphed to yield any desired logic gate by varying just a single control parameter. Further, the results are verified in electronic circuit experiments, demonstrating the robustness of the concept and the potential of this idea to be realized in wide-ranging systems.
Description:	Only IISER Mohali authors are available in the record.
URI:	https://doi.org/10.1103/PhysRevApplied.18.014061 (https://doi.org/10.1103/PhysRevApplied.18.014061) http://hdl.handle.net/123456789/4743 (http://hdl.handle.net/123456789/4743)
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