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Title:	Noise Enhanced Logic Gates						
Authors:	Sinha, Sudeshna (/jspui/browse?type:	=author&value	=Sinha	%2C+Sudeshna)			
Issue Date:	2011						
Publisher:	American Institute of Physics						
Citation:	AIP Conf. Proc. 1339, pp. 67-77						
Abstract:	It was observed recently that, when one drives a two-state system with two square wave as input, the response of the system mirrors a logical output (NOR/OR). The probability of obtaining the correct logic response is controlled by the interplay between the noise-floor and the nonlinearity. As one increases the noise intensity, the probability of the output reflecting a NOR/OR operation increases to unity in a window of moderate noise, and then decreases to zero again for very large noise. This concept of noise enhanced logic gate performance under optimal noise was called: Logical Stochastic Resonance (LSR). Here we review this concept of Logical Stochastic Resonance and provide details of suitable electronic circuit systems demonstrating LSR. We also review the extension of this approach to realize logic gates with Coherence Resonance Systems (LCR).						
Description:	Only IISERM authors are available in the record.						
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