First Year (Semester I) B.Tech.

Basic Electrical and Electronics Engineering

Experiment No: 06
Rectifier Circuit

Name: Ayush Jain

SAP No.: 60004200132

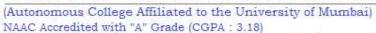
Date of performance: 10/04/2021

Signature of teacher-in-charge:



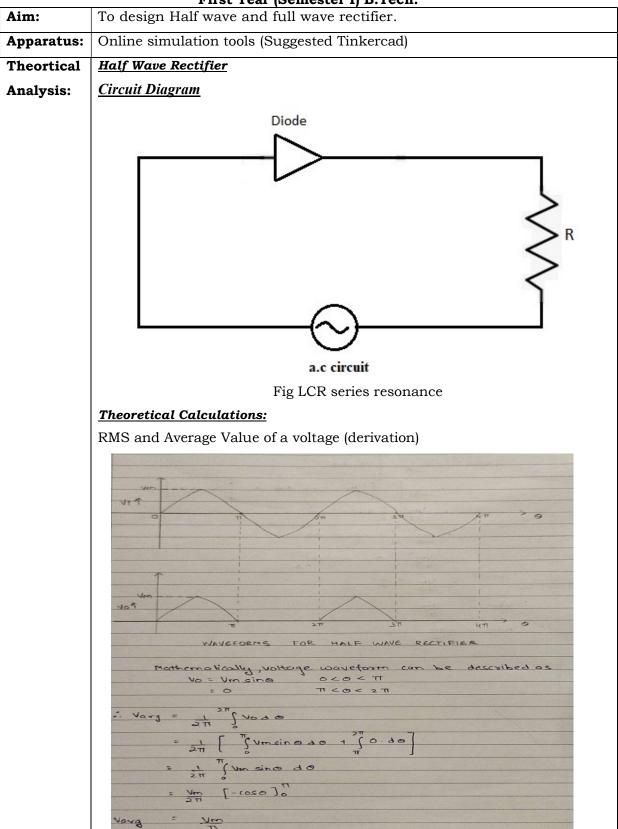
Shri Vile Parle Kelavani Mandal's

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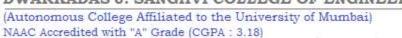




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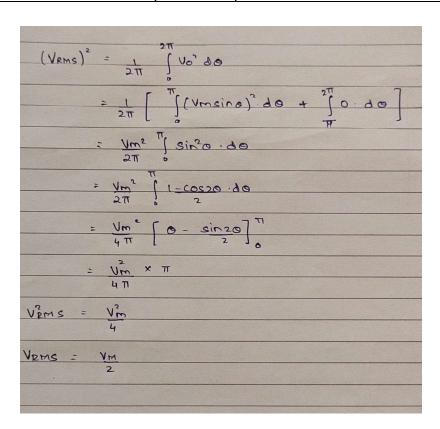


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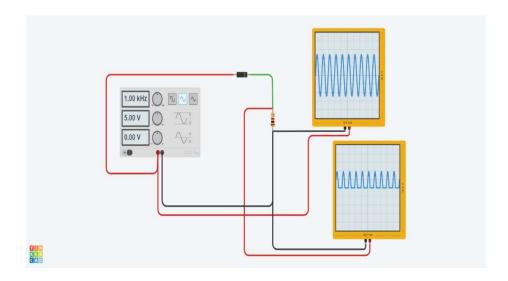




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Observation waveform

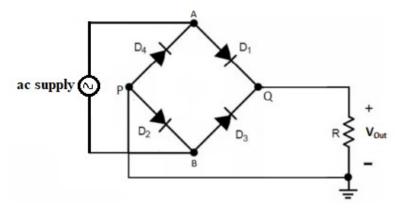




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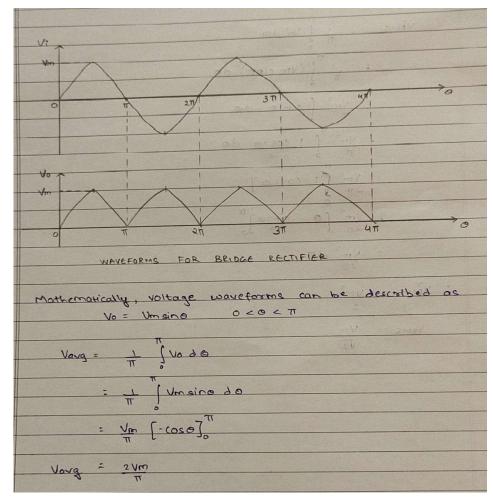
Full wave rectifier

Bridge Rectifier

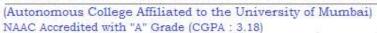


Theoretical Calculations:

RMS and Average Value of a voltage (derivation)

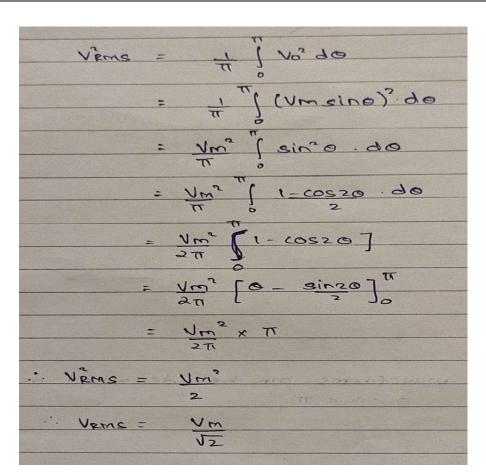


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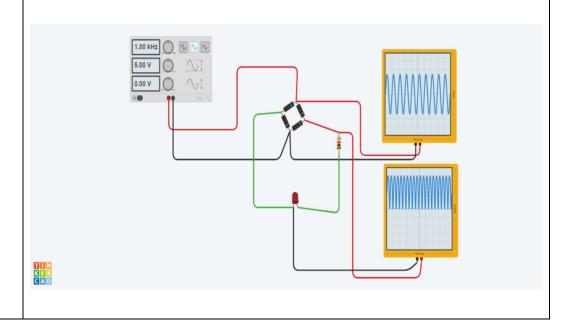




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Observation waveform



	First Year (Semester I) B.Tech.
Conc	clusion:
•	The circuits are made using an online simulation tool, Tinkercad
•	
•	This experiment helped us study the waveforms generated by a half wa
	and full wave (bridge) rectifier by implementing the circuit and making
	measurements using an oscilloscope.