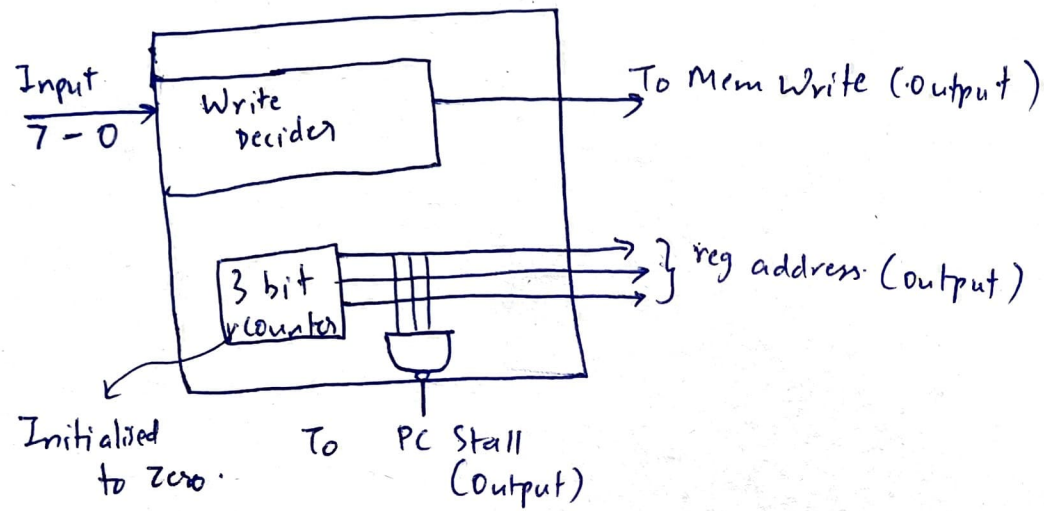


LM - SM Block Design



→ The write decoder decides which registers will write to memory for load multiple, & which registers will be written (for store multiple)

Write Decoder Design

