Priyadarshini College of Engineering, Nagpur

Sessional Examination (2023-24) ODD Semester B.Tech. Third Semester(Information Technology)(C.B.C.S) System Programming

Due credit will be given to neatness and adequate dimensions

All questions carry marks as indicated.
 Solve Question 1 or Question 2.
 Solve Question 3 or Question 4.
 Solve Question 5 or Question 6.
 Solve Question 7 or Question 8.
 Solve Question 9 or Question 10.

P. Pages: 2

Duration: Three Hours

PCE/KW/23/BEIT305T

Max. Marks: 70

		8. Assume suitable data wherever necessary.9. Illustrate your answers whenever necessary with the help of neat sket	ches.		
Q. No	D.	Question	со	вт	Marks
Q1	کھیے	Draw and Explain General Machine Structure IBM 360/370.	CO1	2	7
	by	Explain key steps in System software development.	CO1	2	7
Q2	a) b)	Explain in details the variuos data formats used in assembler? Explain. Differentiate between the following:	CO1	2	7
		a. Compiler and Interpreter.b. Process and program	CO1	3	7
Q3	م	Illustrate the elements of assembly language programming.	CO2	3	7
~	by	Draw and explain flowchart of Pass I Assembler	CO2	2	7
Q4	a)	OR Explain flowchart and algorithm of Pass II Assembler.	CO2	2	7
		Give the elements of assembly language programming.	CO2	2	7
Q5 (•	Explain one pass macroprocessor.	СОЗ	2	7
5	pr	Explain Conditional Macro expansion with example.	CO3	2	7
		OR OR			
Q6	a)	Explain the Macro Definition and Macro calls in detail.	CO3	2	7
	b)	GET MDT. MNT and ALA for the following code fragment.	CO3	~	

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Q7	al	Define Loader? Explain task of Loader.	CO4	2	7
	المطر	Explain Absolute Loader Scheme with advantages and Disadvantages.	CO4	2	7
		- OR	- F		
Q8	a)	Explain General loader scheme in detail.	CO4	2	7
	b)	Explain compile and go loaders in detail	CO4	2	7
Q9	a)	What is debugger & explain debugging procedure in detail.	CO5	2	7
	b)	Describe Java Virtual machine.	CO5	2 .	7
		OR	**	* - L	
Q10) a)	Explain errors and its types.	CO5	2	7
	J	What are Different phases of Compiler design.	CO5	2.	7

*** ALL THE BEST ***