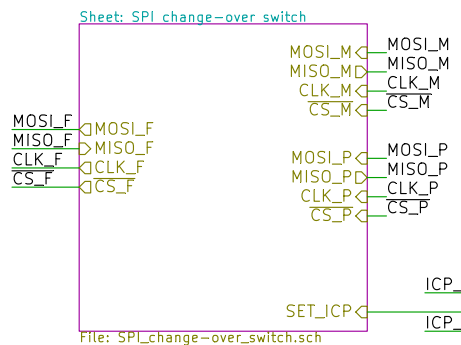
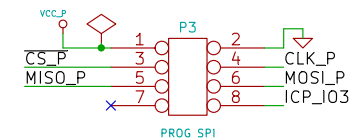
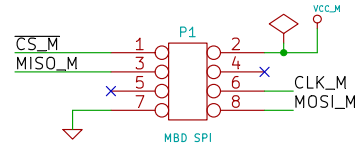


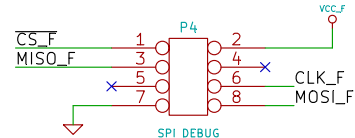
alternate SOIC-8 footprint



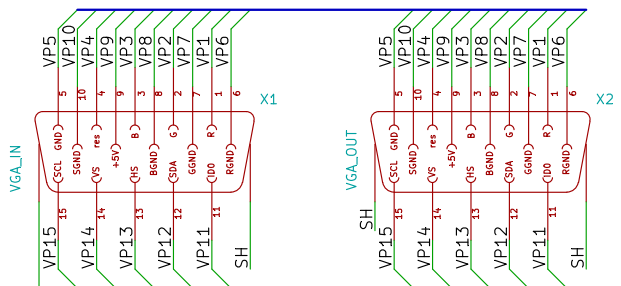
SPI\_Change-over switch:  
Connects flash to motherboard when ICP is low  
or to programmer when ICP is high. Includes  
voltage level translation to maximize compatibility  
between programmer, flash and test target.



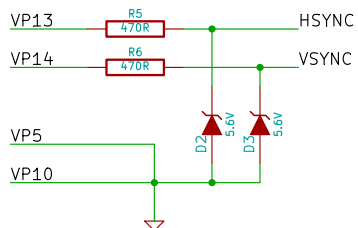
JP1: ICP control  
1-2: Internal (from controller)  
2-3: External (manual/from programmer)



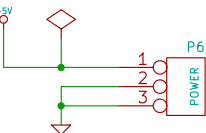
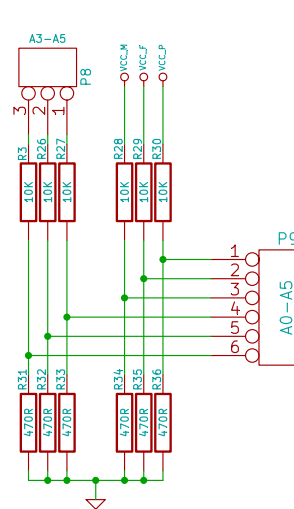
To watch SPI communication  
if needed.



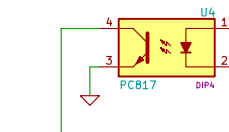
X1, X2: VGA in and out ports.  
V/H sync lines are tapped for use by video detector



Free ADC channels



Arduino's internal pullup used in PWR\_LED pin



ICP Indicator

P11, P12, P13, P14: Arduino Headers  
Compatible with Duemilanove, UNO and Leonardo

Ayush Sagar (ayush@ieee.org)

GSoc coreboot

File: test-interface-board.sch

Sheet: /

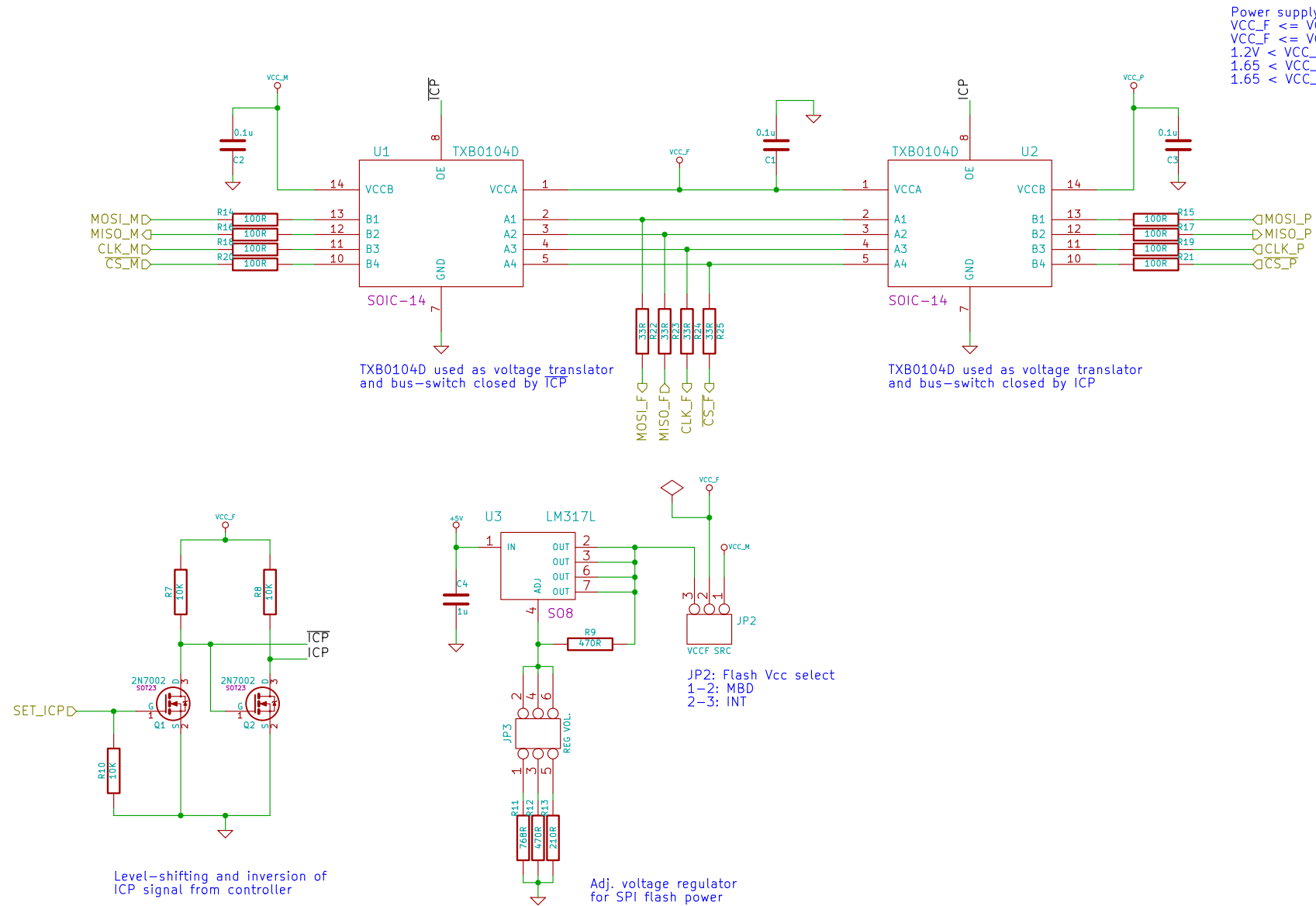
Title: Motherboard firmware test interface

Size: A4 Date: 9 sep 2013

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Rev: 28 Aug 2013

Id: 1/2



Ayush Sagar (ayush@ieee.org)

# **GSoC coreboot**

File: SPI\_change-over\_switch.sch

Sheet: /SPI change-over switch/

## **Title: SPI change-over switch**

Size: A4 Date: 9 sep 2013

Rev: 28 Aug 2013

KiCad E.D.A. eeschema (2013-07-07 BZR 4022)-stable

Id: 2/2