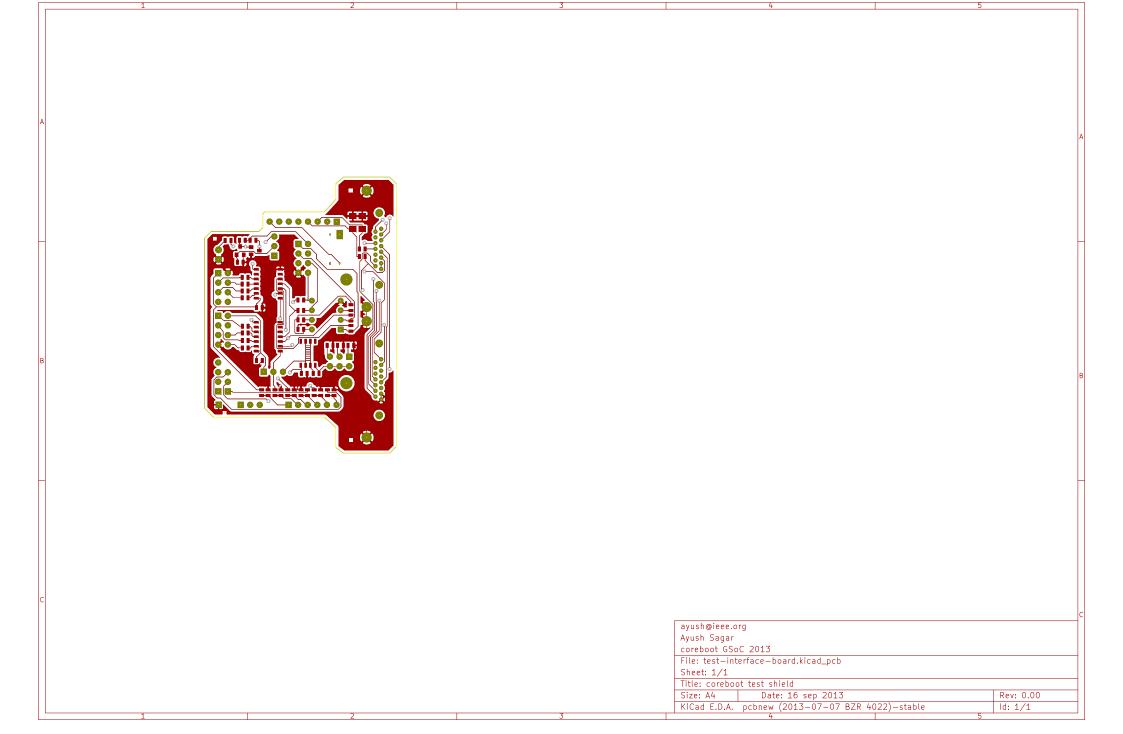
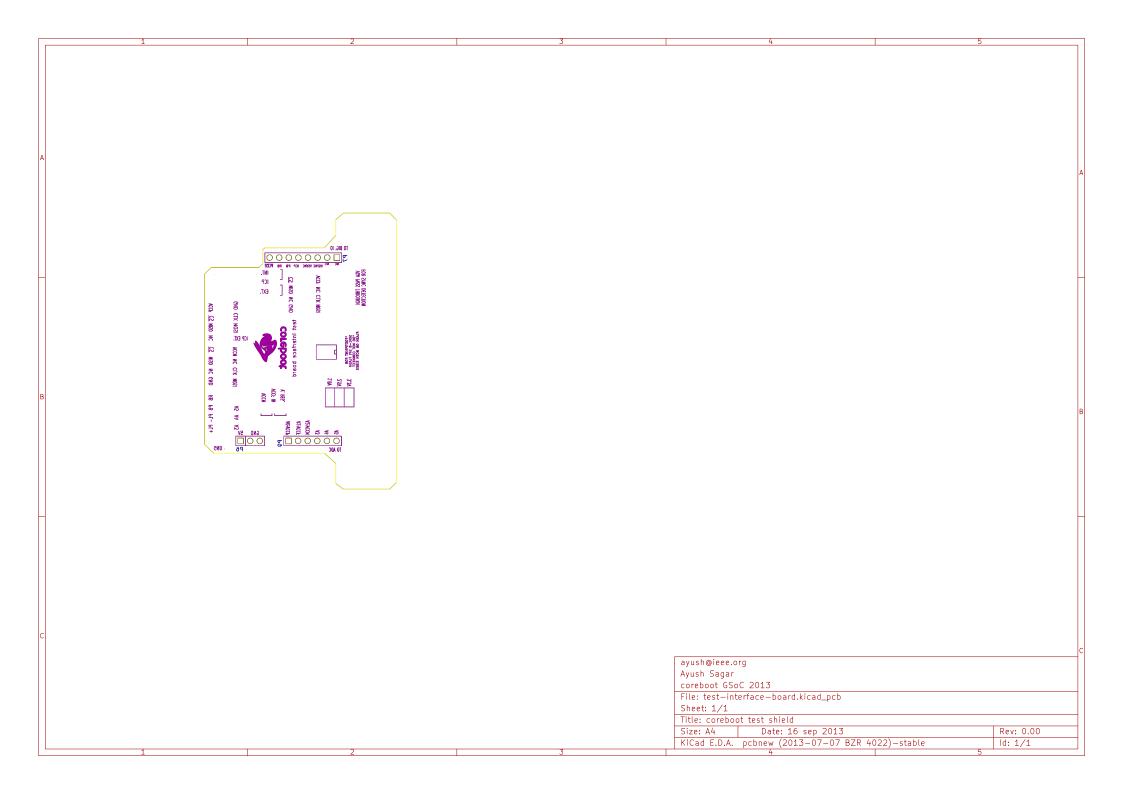
ayush@ieee.org Ayush Sagar coreboot GSoC 2013
File: test-interface-board.kicad_pcb Sheet: 1/1 Title: coreboot test shield Size: A4 Date: 16 sep 2013
KiCad E.D.A. pcbnew (2013-07-07 BZR 4022)-stable Rev: 0.00 ld: 1/1





8 8 | Care PLED/PRIN/RERIN | HEBD SFP | PRIOS PC817 U4 PC817 OO V9A SPI Flash 813 812 811 8 8 8 ayush@ieee.org Ayush Sagar coreboot GSoC 2013 File: test-interface-board.kicad_pcb Sheet: 1/1 Title: coreboot test shield Date: 16 sep 2013 Rev: 0.00 Size: A4 KiCad E.D.A. pcbnew (2013-07-07 BZR 4022)-stable ld: 1/1

