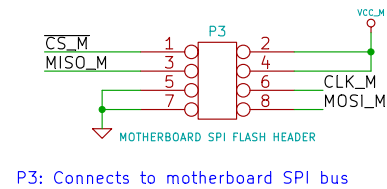
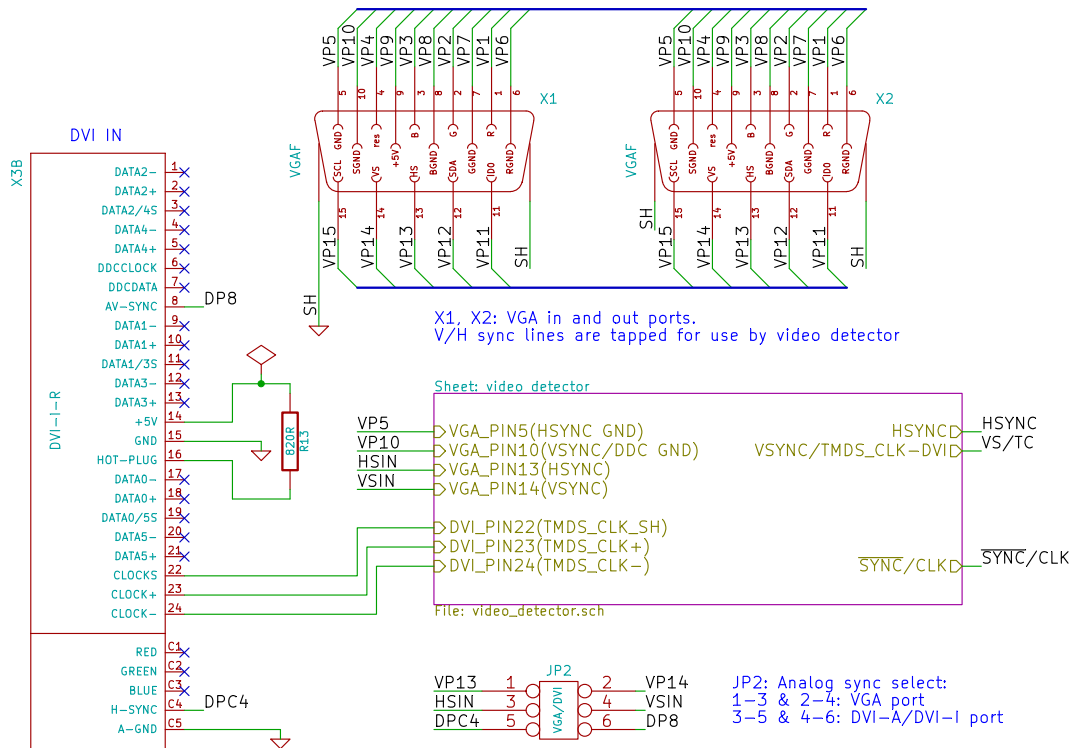
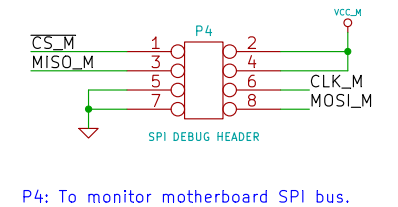
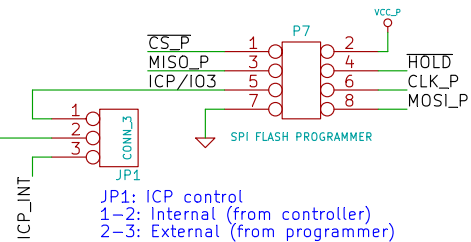


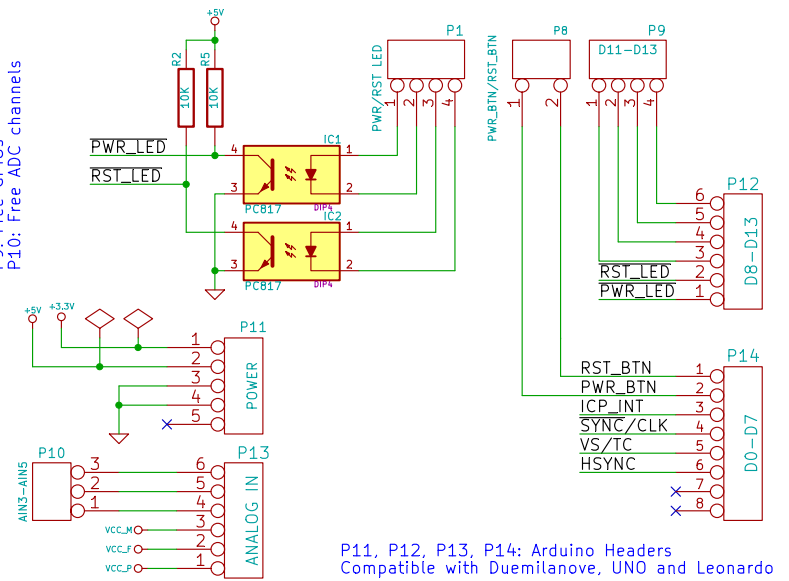
Change-over switch:
Connects flash to motherboard when ICP is high or to programmer when ICP is low. Includes voltage level translation to ensure compatibility between programmer and target system.



G1: must be connected to motherboard's digital ground through a separate wire.



P8: Open drain outputs
1: Soft power switch control
2: Reset switch control
P9: Free GPIOs
P10: Free ADC channels



Ayush Sagar (ayush@ieee.org)

GSoc coreboot

File: test-interface-board.sch

Sheet: /

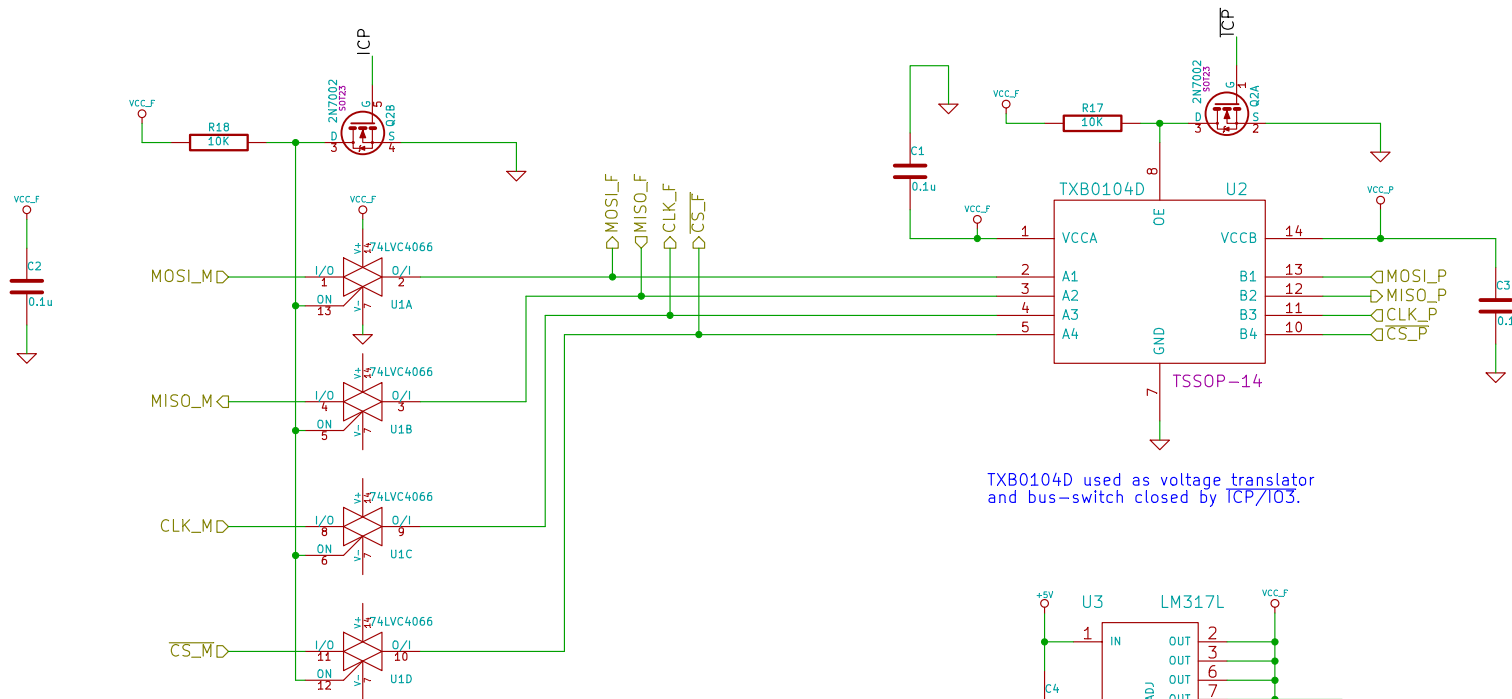
Title: Motherboard firmware test interface

Size: A4 Date: 6 aug 2013

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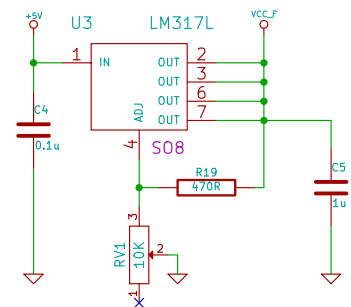
Rev: 5 Aug 2013

Id: 1/3



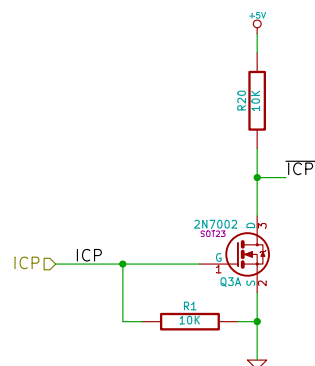
TXB0104D used as voltage translator and bus-switch closed by ICP/IO3.

74LVC4066 quad bilateral-switch closed by ICP/IO3



Adj. Voltage Regulator for SPI flash Vcc

Power supply constraints:
 $VCC_F = VCC_M$
 $VCC_F, VCC_M \leq VCC_P$
 $1.2V < VCC_M < 3.6V$
 $1.65 < VCC_P < 5.5V$



Inverter to get TCP

Ayush Sagar (ayush@ieee.org)

GSoC coreboot

File: SPI_change-over_switch.sch

Sheet: /SPI change-over switch/

Title: SPI change-over switch

Size: A4 Date: 6 aug 2013

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Rev: 4 Aug 2013

Id: 2/3

