

Course Content for Sessional

Lecture	Topic
1	Introduction
2	What is Coal, Why Assembly Language, RAM structure, CPU Structure, Buses, Fetch Decode Execute cycle, von Neumann architecture, 16,32, and 64 bit system, .cpp to compiler to assembler to machine code cycle, mov ax,25 instruction introduction
3	Start of NASAM, assembly code, afd debugger, assembly to machine code, interrupts
4	read and write data in RAM, memory allocation byte, word
5	Segments, memory frame, CS and DS, physical address, logical address
6	Ah,Al, Bh,BI, CH,CI,Dh,DI registers, direct addressing, indirect/register based addressing, ZF(Zero flag) , loop
7	cmp, JNE, Carry flag, zero flag, sign flag, over flow flag
8	Unconditional JUMP
9	Sorting, Nested loop
10	Revision of all topics