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| | FTf | 5 V-tolerant I/O, FM+ capable |
| | TTa | 3.3 V-tolerant I/O directly connected to ADC |
| | RST | Bidirectional reset pin with embedded weak pull-up resistor |
| | FT | 5 V-tolerant I/O |
| | | |

| Pin # | Pin name | Pin type | Structure | Pin alt functions |
|-----------|--------------------|-------------------|-----------|--|
| 1 | PB8 - BOOT0 | I/O | FTf | I2C1_SCL, CEC, TIM16_CH1, TSC_SYNC, CAN_RX |
| 2 | PF0 - OSC_IN | I/O | FTf | CRS_SYNC, I2C1_SDA OSC_IN |
| 3 | PF1 - OSC_OUT | I/O | FTf | I2C1_SCL OSC_OUT |
| 4 | NRST | I/O | RST | Device reset input / internal reset output (active low) |
| 5 | VDDA | Supply pin | - | Analog power supply |
| 6 | PA0 | I/O | TTa | USART2_CTS, TIM2_CH1_ETR, TSC_G1_IO1 RTC_TAMP2, WKUP1, ADC_IN0 |
| 7 | PA1 | I/O | TTa | USART2_RTS, TIM2_CH2, TSC_G1_IO2, EVENTOUT ADC_IN1 |
| 8 | PA2 | I/O | TTa | USART2_TX, TIM2_CH3, TSC_G1_IO3 ADC_IN2, WKUP4 |
| 9 | PA3 | I/O | TTa | USART2_RX, TIM2_CH4, TSC_G1_IO4 ADC_IN3 |
| 10 | PA4 | I/O | TTa | SPI1_NSS, I2S1_WS, TIM14_CH1, TSC_G2_IO1, USART2_CK, USB_NOE ADC_IN4 |
| 11 | PA5 | I/O | TTa | SPI1_SCK, I2S1_CK, CEC, TIM2_CH1_ETR, TSC_G2_IO2 ADC_IN5 |
| 12 | PA6 | I/O | TTa | SPI1_MISO, I2S1_MCK, TIM3_CH1, TIM1_BKIN, TIM16_CH1, TSC_G2_IO3, EVENTOUT ADC_IN6 |
| 13 | PA7 | I/O | TTa | SPI1_MOSI, I2S1_SD, TIM3_CH2, TIM14_CH1, TIM1_CH1N, TIM17_CH1, TSC_G2_IO4, EVENTOUT ADC_IN7 |
| 14 | PB1 | I/O | TTa | TIM3_CH4, TIM14_CH1, TIM1_CH3N, TSC_G3_IO3 ADC_IN9 |
| 15 | VSS | Supply pin | - | Ground |
| 16 | VDD | Supply pin | - | Digital power supply |
| 17 | PA9 / PA11 | I/O | FTf | USART1_TX, TIM1_CH2, TSC_G4_IO1, I2C1_SCL USB_DM |
| 18 | PA10 / PA12 | I/O | FTf | USART1_RX, TIM1_CH3, TIM17_BKIN, TSC_G4_IO2, I2C1_SDA USB_DP |
| 19 | PA13 | I/O | FT | IR_OUT, SWDIO USB_NOE |
| 20 | PA14 | I/O | FT | USART2_TX, SWCLK |