10: Model Summary
Model: "model"

Layer (type)	Output Shape	Param #	Connected to
======================================	[(None, 224, 224, 3)	0	=========
conv1_pad (ZeroPadding2D)	(None, 230, 230, 3)	0	input_1[0][0]
conv1_conv (Conv2D)	(None, 112, 112, 64)	9472	conv1_pad[0][0]
conv1_bn (BatchNormalization)	(None, 112, 112, 64)	256	conv1_conv[0][0]
conv1_relu (Activation)	(None, 112, 112, 64)	0	conv1_bn[0][0]
pool1_pad (ZeroPadding2D)	(None, 114, 114, 64)	0	conv1_relu[0][0]
pool1_pool (MaxPooling2D)	(None, 56, 56, 64)	0	pool1_pad[0][0]
conv2_block1_1_conv (Conv2D)	(None, 56, 56, 64)	4160	pool1_pool[0][0]
<pre>conv2_block1_1_bn (BatchNormali conv2_block1_1_conv[0][0]</pre>	(None, 56, 56, 64)	256	
<pre>conv2_block1_1_relu (Activation conv2_block1_1_bn[0][0]</pre>	(None, 56, 56, 64)	0	
conv2_block1_2_conv (Conv2D) conv2_block1_1_relu[0][0]	(None, 56, 56, 64)	36928	

conv2_block1_2_bn (BatchNormali conv2_block1_2_conv[0][0]	(None,	56,	56,	64)	256	
conv2_block1_2_relu (Activation conv2_block1_2_bn[0][0]	(None,	56,	56,	64)	0	
conv2_block1_0_conv (Conv2D)	(None,	56,	56,	256)	16640	pool1_pool[0][0]
conv2_block1_3_conv (Conv2D) conv2_block1_2_relu[0][0]	(None,	56,	56,	256)	16640	
conv2_block1_0_bn (BatchNormali conv2_block1_0_conv[0][0]	(None,	56,	56,	256)	1024	
conv2_block1_3_bn (BatchNormali conv2_block1_3_conv[0][0]	(None,	56,	56,	256)	1024	
conv2_block1_add (Add) conv2_block1_0_bn[0][0]	(None,	56,	56,	256)	0	
conv2_block1_3_bn[0][0]						
conv2_block1_out (Activation) conv2_block1_add[0][0]	(None,	56,	56,	256)	0	
conv2_block2_1_conv (Conv2D) conv2_block1_out[0][0]	(None,	56,	56,	64)	16448	
conv2_block2_1_bn (BatchNormali conv2_block2_1_conv[0][0]	(None,	56,	56,	64)	256	
conv2_block2_1_relu (Activation conv2_block2_1_bn[0][0]	(None,	56,	56,	64)	0	
conv2_block2_2_conv (Conv2D) conv2_block2_1_relu[0][0]	(None,	56,	56,	64)	36928	

		_bn (BatchNormali _conv[0][0]	(None,	56,	56,	64)	256
	_block2_2 _block2_2	_relu (Activation _bn[0][0]	(None,	56,	56,	64)	0
_		_conv (Conv2D) _relu[0][0]	(None,	56,	56,	256)	16640
_		_bn (BatchNormali _conv[0][0]	(None,	56,	56,	256)	1024
_	_block2_a _block1_o		(None,	56,	56,	256)	0
conv2	_block2_3	_bn[0][0]					
_	_block2_o _block2_a	ut (Activation) dd[0][0]	(None,	56,	56,	256)	0
_	_block3_1 _block2_o	_conv (Conv2D) ut[0][0]	(None,	56,	56,	64)	16448
		_bn (BatchNormali _conv[0][0]	(None,	56,	56,	64)	256
	_block3_1 _block3_1	_relu (Activation _bn[0][0]	(None,	56,	56,	64)	0
_		_conv (Conv2D) _relu[0][0]	(None,	56,	56,	64)	36928
_		_bn (BatchNormali _conv[0][0]	(None,	56,	56,	64)	256
_	_block3_2 _block3_2	_relu (Activation _bn[0][0]	(None,	56,	56,	64)	0

conv2_block3_3_conv (Conv2D) conv2_block3_2_relu[0][0]	(None,	56,	56,	256)	16640
conv2_block3_3_bn (BatchNormaliconv2_block3_3_conv[0][0]	(None,	56,	56,	256)	1024
conv2_block3_add (Add) conv2_block2_out[0][0]	(None,	56,	56,	256)	0
conv2_block3_3_bn[0][0]					
conv2_block3_out (Activation) conv2_block3_add[0][0]	(None,	56,	56,	256)	0
conv3_block1_1_conv (Conv2D) conv2_block3_out[0][0]	(None,	28,	28,	128)	32896
conv3_block1_1_bn (BatchNormaliconv3_block1_1_conv[0][0]	(None,	28,	28,	128)	512
conv3_block1_1_relu (Activation conv3_block1_1_bn[0][0]	(None,	28,	28,	128)	0
conv3_block1_2_conv (Conv2D) conv3_block1_1_relu[0][0]	(None,	28,	28,	128)	147584
conv3_block1_2_bn (BatchNormaliconv3_block1_2_conv[0][0]	(None,	28,	28,	128)	512
conv3_block1_2_relu (Activation conv3_block1_2_bn[0][0]	(None,	28,	28,	128)	0
conv3_block1_0_conv (Conv2D) conv2_block3_out[0][0]	(None,	28,	28,	512)	131584
<pre>conv3_block1_3_conv (Conv2D) conv3_block1_2_relu[0][0]</pre>	(None,	28,	28,	512)	66048

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conv3_block1_0_bn (BatchNormali (None, 28, 28, 512)
                                                     2048
conv3_block1_0_conv[0][0]
conv3_block1_3_bn (BatchNormali (None, 28, 28, 512)
conv3_block1_3_conv[0][0]
conv3_block1_add (Add)
                                (None, 28, 28, 512) 0
conv3_block1_0_bn[0][0]
conv3_block1_3_bn[0][0]
conv3_block1_out (Activation)
                                (None, 28, 28, 512) 0
conv3_block1_add[0][0]
conv3_block2_1_conv (Conv2D)
                                (None, 28, 28, 128)
                                                     65664
conv3_block1_out[0][0]
conv3 block2 1 bn (BatchNormali (None, 28, 28, 128)
                                                      512
conv3_block2_1_conv[0][0]
conv3_block2_1_relu (Activation (None, 28, 28, 128)
conv3_block2_1_bn[0][0]
conv3 block2 2 conv (Conv2D)
                                (None, 28, 28, 128)
                                                     147584
conv3_block2_1_relu[0][0]
conv3_block2_2_bn (BatchNormali (None, 28, 28, 128)
conv3_block2_2_conv[0][0]
conv3_block2_2_relu (Activation (None, 28, 28, 128)
conv3_block2_2_bn[0][0]
conv3_block2_3_conv (Conv2D)
                                (None, 28, 28, 512)
                                                     66048
conv3_block2_2_relu[0][0]
conv3_block2_3_bn (BatchNormali (None, 28, 28, 512)
conv3_block2_3_conv[0][0]
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conv3_block2_add (Add) conv3_block1_out[0][0]	(None,	28,	28,	512)	0
conv3_block2_3_bn[0][0]					
conv3_block2_out (Activation) conv3_block2_add[0][0]	(None,	28,	28,	512)	0
conv3_block3_1_conv (Conv2D) conv3_block2_out[0][0]	(None,	28,	28,	128)	65664
conv3_block3_1_bn (BatchNormali conv3_block3_1_conv[0][0]	(None,	28,	28,	128)	512
conv3_block3_1_relu (Activation conv3_block3_1_bn[0][0]	(None,	28,	28,	128)	0
conv3_block3_2_conv (Conv2D) conv3_block3_1_relu[0][0]	(None,	28,	28,	128)	147584
conv3_block3_2_bn (BatchNormali conv3_block3_2_conv[0][0]	(None,	28,	28,	128)	512
conv3_block3_2_relu (Activation conv3_block3_2_bn[0][0]	(None,	28,	28,	128)	0
conv3_block3_3_conv (Conv2D) conv3_block3_2_relu[0][0]	(None,	28,	28,	512)	66048
conv3_block3_3_bn (BatchNormali conv3_block3_3_conv[0][0]	(None,	28,	28,	512)	2048
conv3_block3_add (Add) conv3_block2_out[0][0]	(None,	28,	28,	512)	0
conv3_block3_3_bn[0][0]					
					

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conv3 block3 out (Activation)
                                (None, 28, 28, 512) 0
conv3_block3_add[0][0]
conv3_block4_1_conv (Conv2D)
                                (None, 28, 28, 128) 65664
conv3_block3_out[0][0]
conv3_block4_1_bn (BatchNormali (None, 28, 28, 128)
conv3_block4_1_conv[0][0]
conv3_block4_1_relu (Activation (None, 28, 28, 128) 0
conv3_block4_1_bn[0][0]
conv3_block4_2_conv (Conv2D)
                                (None, 28, 28, 128)
                                                     147584
conv3_block4_1_relu[0][0]
conv3_block4_2_bn (BatchNormali (None, 28, 28, 128)
conv3_block4_2_conv[0][0]
conv3 block4 2 relu (Activation (None, 28, 28, 128) 0
conv3_block4_2_bn[0][0]
conv3_block4_3_conv (Conv2D)
                                (None, 28, 28, 512)
                                                     66048
conv3_block4_2_relu[0][0]
conv3_block4_3_bn (BatchNormali (None, 28, 28, 512)
conv3_block4_3_conv[0][0]
                                (None, 28, 28, 512) 0
conv3_block4_add (Add)
conv3_block3_out[0][0]
conv3_block4_3_bn[0][0]
conv3_block4_out (Activation)
                                (None, 28, 28, 512) 0
conv3_block4_add[0][0]
conv4_block1_1_conv (Conv2D)
                                (None, 14, 14, 256) 131328
conv3_block4_out[0][0]
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conv4 block1 1 bn (BatchNormali (None, 14, 14, 256)
conv4_block1_1_conv[0][0]
conv4_block1_1_relu (Activation (None, 14, 14, 256) 0
conv4_block1_1_bn[0][0]
conv4_block1_2_conv (Conv2D)
                                (None, 14, 14, 256)
                                                     590080
conv4 block1 1 relu[0][0]
conv4_block1_2_bn (BatchNormali (None, 14, 14, 256) 1024
conv4_block1_2_conv[0][0]
conv4_block1_2_relu (Activation (None, 14, 14, 256) 0
conv4_block1_2_bn[0][0]
conv4_block1_0_conv (Conv2D)
                                (None, 14, 14, 1024) 525312
conv3_block4_out[0][0]
conv4 block1 3 conv (Conv2D)
                                (None, 14, 14, 1024) 263168
conv4_block1_2_relu[0][0]
conv4_block1_0_bn (BatchNormali (None, 14, 14, 1024) 4096
conv4_block1_0_conv[0][0]
conv4 block1 3 bn (BatchNormali (None, 14, 14, 1024) 4096
conv4_block1_3_conv[0][0]
conv4_block1_add (Add)
                                (None, 14, 14, 1024) 0
conv4_block1_0_bn[0][0]
conv4_block1_3_bn[0][0]
conv4_block1_out (Activation)
                                (None, 14, 14, 1024) 0
conv4_block1_add[0][0]
conv4_block2_1_conv (Conv2D)
                                (None, 14, 14, 256) 262400
conv4_block1_out[0][0]
```

```
conv4 block2 1 bn (BatchNormali (None, 14, 14, 256)
conv4_block2_1_conv[0][0]
conv4_block2_1_relu (Activation (None, 14, 14, 256) 0
conv4_block2_1_bn[0][0]
conv4 block2 2 conv (Conv2D)
                                (None, 14, 14, 256)
                                                     590080
conv4 block2 1 relu[0][0]
conv4_block2_2_bn (BatchNormali (None, 14, 14, 256) 1024
conv4_block2_2_conv[0][0]
conv4_block2_2_relu (Activation (None, 14, 14, 256) 0
conv4_block2_2_bn[0][0]
conv4_block2_3_conv (Conv2D)
                                (None, 14, 14, 1024) 263168
conv4_block2_2_relu[0][0]
conv4 block2 3 bn (BatchNormali (None, 14, 14, 1024) 4096
conv4_block2_3_conv[0][0]
conv4_block2_add (Add)
                                (None, 14, 14, 1024) 0
conv4_block1_out[0][0]
conv4_block2_3_bn[0][0]
conv4_block2_out (Activation)
                                (None, 14, 14, 1024) 0
conv4_block2_add[0][0]
conv4 block3_1_conv (Conv2D)
                                (None, 14, 14, 256) 262400
conv4_block2_out[0][0]
conv4_block3_1_bn (BatchNormali (None, 14, 14, 256) 1024
conv4_block3_1_conv[0][0]
conv4_block3_1_relu (Activation (None, 14, 14, 256) 0
conv4_block3_1_bn[0][0]
```

```
conv4 block3 2 conv (Conv2D)
                                (None, 14, 14, 256)
                                                     590080
conv4_block3_1_relu[0][0]
conv4_block3_2_bn (BatchNormali (None, 14, 14, 256) 1024
conv4_block3_2_conv[0][0]
conv4_block3_2_relu (Activation (None, 14, 14, 256) 0
conv4_block3_2_bn[0][0]
conv4_block3_3_conv (Conv2D)
                                (None, 14, 14, 1024) 263168
conv4_block3_2_relu[0][0]
conv4_block3_3_bn (BatchNormali (None, 14, 14, 1024) 4096
conv4_block3_3_conv[0][0]
conv4_block3_add (Add)
                                (None, 14, 14, 1024) 0
conv4_block2_out[0][0]
conv4_block3_3_bn[0][0]
conv4_block3_out (Activation)
                                (None, 14, 14, 1024) 0
conv4_block3_add[0][0]
conv4_block4_1_conv (Conv2D)
                                (None, 14, 14, 256) 262400
conv4_block3_out[0][0]
conv4_block4_1_bn (BatchNormali (None, 14, 14, 256) 1024
conv4_block4_1_conv[0][0]
conv4_block4_1_relu (Activation (None, 14, 14, 256) 0
conv4_block4_1_bn[0][0]
conv4_block4_2_conv (Conv2D)
                                (None, 14, 14, 256)
                                                     590080
conv4_block4_1_relu[0][0]
conv4_block4_2_bn (BatchNormali (None, 14, 14, 256) 1024
conv4_block4_2_conv[0][0]
```

```
conv4 block4 2 relu (Activation (None, 14, 14, 256) 0
conv4_block4_2_bn[0][0]
conv4_block4_3_conv (Conv2D)
                                (None, 14, 14, 1024) 263168
conv4_block4_2_relu[0][0]
conv4_block4_3_bn (BatchNormali (None, 14, 14, 1024) 4096
conv4_block4_3_conv[0][0]
conv4_block4_add (Add)
                                (None, 14, 14, 1024) 0
conv4_block3_out[0][0]
conv4_block4_3_bn[0][0]
conv4 block4 out (Activation)
                                (None, 14, 14, 1024) 0
conv4_block4_add[0][0]
conv4_block5_1_conv (Conv2D)
                                (None, 14, 14, 256) 262400
conv4_block4_out[0][0]
conv4_block5_1_bn (BatchNormali (None, 14, 14, 256) 1024
conv4_block5_1_conv[0][0]
conv4_block5_1_relu (Activation (None, 14, 14, 256) 0
conv4_block5_1_bn[0][0]
conv4_block5_2_conv (Conv2D)
                                (None, 14, 14, 256)
                                                     590080
conv4_block5_1_relu[0][0]
conv4_block5_2_bn (BatchNormali (None, 14, 14, 256) 1024
conv4_block5_2_conv[0][0]
conv4_block5_2_relu (Activation (None, 14, 14, 256) 0
conv4_block5_2_bn[0][0]
conv4_block5_3_conv (Conv2D)
                                (None, 14, 14, 1024) 263168
conv4_block5_2_relu[0][0]
```

```
conv4 block5 3 bn (BatchNormali (None, 14, 14, 1024) 4096
conv4_block5_3_conv[0][0]
conv4_block5_add (Add)
                                (None, 14, 14, 1024) 0
conv4_block4_out[0][0]
conv4_block5_3_bn[0][0]
conv4_block5_out (Activation)
                                (None, 14, 14, 1024) 0
conv4_block5_add[0][0]
conv4_block6_1_conv (Conv2D)
                                (None, 14, 14, 256) 262400
conv4_block5_out[0][0]
conv4_block6_1_bn (BatchNormali (None, 14, 14, 256) 1024
conv4_block6_1_conv[0][0]
conv4_block6_1_relu (Activation (None, 14, 14, 256) 0
conv4_block6_1_bn[0][0]
conv4_block6_2_conv (Conv2D)
                                (None, 14, 14, 256) 590080
conv4_block6_1_relu[0][0]
conv4_block6_2_bn (BatchNormali (None, 14, 14, 256)
                                                     1024
conv4_block6_2_conv[0][0]
conv4_block6_2_relu (Activation (None, 14, 14, 256) 0
conv4_block6_2_bn[0][0]
conv4_block6_3_conv (Conv2D)
                                (None, 14, 14, 1024) 263168
conv4_block6_2_relu[0][0]
conv4_block6_3_bn (BatchNormali (None, 14, 14, 1024) 4096
conv4_block6_3_conv[0][0]
conv4_block6_add (Add)
                                (None, 14, 14, 1024) 0
conv4_block5_out[0][0]
conv4_block6_3_bn[0][0]
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conv4_block6_out (Activation) conv4_block6_add[0][0]	(None,	14, 14	, 1024)	0
conv5_block1_1_conv (Conv2D) conv4_block6_out[0][0]	(None,	7, 7,	512)	524800
<pre>conv5_block1_1_bn (BatchNormali conv5_block1_1_conv[0][0]</pre>	(None,	7, 7,	512)	2048
<pre>conv5_block1_1_relu (Activation conv5_block1_1_bn[0][0]</pre>	(None,	7, 7,	512)	0
conv5_block1_2_conv (Conv2D) conv5_block1_1_relu[0][0]	(None,	7, 7,	512)	2359808
<pre>conv5_block1_2_bn (BatchNormali conv5_block1_2_conv[0][0]</pre>	(None,	7, 7,	512)	2048
conv5_block1_2_relu (Activation conv5_block1_2_bn[0][0]	(None,	7, 7,	512)	0
conv5_block1_0_conv (Conv2D) conv4_block6_out[0][0]	(None,	7, 7,	2048)	2099200
conv5_block1_3_conv (Conv2D) conv5_block1_2_relu[0][0]	(None,	7, 7,	2048)	1050624
<pre>conv5_block1_0_bn (BatchNormali conv5_block1_0_conv[0][0]</pre>	(None,	7, 7,	2048)	8192
<pre>conv5_block1_3_bn (BatchNormali conv5_block1_3_conv[0][0]</pre>	(None,	7, 7,	2048)	8192
conv5_block1_add (Add) conv5_block1_0_bn[0][0]	(None,	7, 7,	2048)	0
conv5_block1_3_bn[0][0]				

conv5_block1_out (Activation) conv5_block1_add[0][0]	(None,	7,	7,	2048)	0
conv5_block2_1_conv (Conv2D) conv5_block1_out[0][0]	(None,	7,	7,	512)	1049088
conv5_block2_1_bn (BatchNormali conv5_block2_1_conv[0][0]	(None,	7,	7,	512)	2048
conv5_block2_1_relu (Activation conv5_block2_1_bn[0][0]	(None,	7,	7,	512)	0
conv5_block2_2_conv (Conv2D) conv5_block2_1_relu[0][0]	(None,	7,	7,	512)	2359808
conv5_block2_2_bn (BatchNormali conv5_block2_2_conv[0][0]	(None,	7,	7,	512)	2048
conv5_block2_2_relu (Activation conv5_block2_2_bn[0][0]	(None,	7,	7,	512)	0
conv5_block2_3_conv (Conv2D) conv5_block2_2_relu[0][0]	(None,	7,	7,	2048)	1050624
conv5_block2_3_bn (BatchNormali conv5_block2_3_conv[0][0]	(None,	7,	7,	2048)	8192
conv5_block2_add (Add) conv5_block1_out[0][0]	(None,	7,	7,	2048)	0
conv5_block2_3_bn[0][0]					
conv5_block2_out (Activation) conv5_block2_add[0][0]	(None,	7,	7,	2048)	0
conv5_block3_1_conv (Conv2D) conv5_block2_out[0][0]	(None,	7,	7,	512)	1049088

conv5_block3_1_bn (BatchNormali conv5_block3_1_conv[0][0]	(None,	7, 7, 512)	2048	
<pre>conv5_block3_1_relu (Activation conv5_block3_1_bn[0][0]</pre>	(None,	7, 7, 512)	0	
conv5_block3_2_conv (Conv2D) conv5_block3_1_relu[0][0]	(None,	7, 7, 512)	2359808	
<pre>conv5_block3_2_bn (BatchNormali conv5_block3_2_conv[0][0]</pre>	(None,	7, 7, 512)	2048	
conv5_block3_2_relu (Activation conv5_block3_2_bn[0][0]	(None,	7, 7, 512)	0	
conv5_block3_3_conv (Conv2D) conv5_block3_2_relu[0][0]	(None,	7, 7, 2048)	1050624	
conv5_block3_3_bn (BatchNormali conv5_block3_3_conv[0][0]	(None,	7, 7, 2048)	8192	
conv5_block3_add (Add) conv5_block2_out[0][0]	(None,	7, 7, 2048)	0	
conv5_block3_3_bn[0][0]				
conv5_block3_out (Activation) conv5_block3_add[0][0]	(None,	7, 7, 2048)	0	
flatten (Flatten) conv5_block3_out[0][0]	(None,	100352)	0	
dense (Dense)	(None,	256)	25690368	flatten[0][0]
dense_1 (Dense)	(None,	128)	32896	dense[0][0]

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dense_2 (Dense)
                      (None, 3)
                                             dense_1[0][0]
                                     387
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=========
Total params: 49,311,363
Trainable params: 25,723,651
Non-trainable params: 23,587,712
13: Executing the Model
Epoch 1/5
94/94 [================ ] - 245s 3s/step - loss: 14.2512 - accuracy:
0.6986 - val loss: 0.2349 - val accuracy: 0.9613
Epoch 2/5
0.9655 - val_loss: 0.2298 - val_accuracy: 0.9687
Epoch 3/5
0.9793 - val_loss: 0.0701 - val_accuracy: 0.9833
Epoch 4/5
0.9928 - val_loss: 0.1178 - val_accuracy: 0.9783
Epoch 5/5
0.9165 - val_loss: 0.0845 - val_accuracy: 0.9823
15: Confusion Matrix
[[975
    1 24]
[ 2 998
        0]
[ 26
     0 974]]
Classification Report
         precision
                  recall f1-score
                               support
                           0.97
      aca
             0.97
                    0.97
                                  1000
             1.00
                    1.00
                           1.00
                                  1000
       n
             0.98
                    0.97
                           0.97
                                  1000
      SCC
                           0.98
                                  3000
  accuracy
  macro avg
             0.98
                    0.98
                           0.98
                                  3000
weighted avg
             0.98
                    0.98
                           0.98
                                  3000
16: Results
24/24 [============= ] - 29s 1s/step - loss: 0.0845 - accuracy:
0.9823
test loss, test accuracy [0.08446120470762253, 0.9823333621025085]
```