AZIF NIZAM

Electronics and Communication Engineer

• Thadathil (H), Chittoor ward, Pathanamthitta PO, Kerala, 689645

• +91 7736804803 • azifniz.work@gmail.com • www.linkedin.com/in/azif-nizam-5448141b9

CAREER OBJECTIVE

Passionate about leveraging my knowledge and skills in Electronics and Communication to contribute to cutting-edge technological advancements, while continuously expanding my expertise in Embedded system design and development in a dynamic and challenging work environment.

SKILLS

- Keysight Advanced Design System (ADS)
- Ansys High Frequency Simulation Software (HFSS)• Project management
- Cadence OrCAD
- Python using Ai
- Embedded C using Ai
- MS (PowerPoint, Word, Excel)

- Leadership
- Team player
- Management

EDUCATION

Bachelor of Technology (Electronics and Communication)

2021 - 2025

Amal Jyothi College of Engineering (Autonomous), Kottayam, Kerala

CGPA 7.58/10

(Affiliated to APJ Abdul Kalam Technological University)

12th grade - Central Board of Secondary Education

2020 - 2021

Bhavans vidya mandir, Pathanamthitta, Kerala

CGPA 7.4/10 2018 - 2019

10th grade - Central Board of Secondary Education

Bhavans vidya mandir, Pathanamthitta, Kerala

CGPA 9.02/10

PROJECTS

L-S band Power Amplifier Integrated Antenna

June 2024 – March 2025

- To design a class AB power amplifier and match its output impedance with the input of a microstrip patch antenna and to integrate it. The IC is aimed to build a portable electromagnetic imaging device.
- Integrated Circuit | Power Electronics | Signal processing | Power amplifier | Antenna

Automated Traffic Alert System (ATAS)

June 2023 – *March* 2024

- The device which is equipped inside the vehicle alerts the driver about incoming emergency vehicles. The emergency vehicles can communicate with other public vehicles on road.
- Embedded system | Microcontrollers | IoT | Transmitter and Receiver | ESP 32

INTERNSHIP AND WORKSHOP

- Internship (2023): Revertech IT Solutions-PCB Designing
- Workshop (2024): VLSI Design using Verilog HDL by Maven Silicon and ASAP
- Workshop (2024): VLSI for beginners by NIELIT Ministry of Electronics and IT, Government of India
- Workshop (2023): Foundation of Electric and Hybrid Vehicles by Techmaghi and Tryst'24 IIT Delhi
- Workshop (2023): Robotics Workshop by IEEE Kerala Section, IEEE LINK and IEEE RAS Kerala

CERTIFICATION

- NPTEL (April 2024) A brief introduction of micro sensors, IISER, Bhopal
- Honours Course (2023) FPGA based system design
- Honours Course (2022) Nanoelectronics

ACHIEVEMENTS

Young Innovators Program Kerala Development & Innovation Strategic Council (2023): Certificate of recognition

REFERENCES

Dr. Geevarghese Titus, Head of Dept. of ECE,

Amal Jyothi College of Engineering (Autonomous), Kanjirapally, Kerala, India.

Ph no: 9447370152, geevarghesetitus@amaljyothi.ac.in