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               //EE469 Lab1
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               //This module adds two 32-bit numbers and a given carry-in value.
               //Inputs: Two 32-bits A, B (inputs to be added), one 1-bit cin (carry-in value).
  8
               //Outputs: One 32-bit sum (sum), one 1-bit cout (carry-out value).
  9
              module fulladder32 (A, B, cin, sum, cout);
10
                        input logic [31:0] A;
input logic [31:0] B;
11
12
                        input loğic cin;
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15
                        output logic [31:0] sum;
16
                        output logic cout;
17
               logic c0, c1, c2, c3, c4, c5, c6, c7, c8, c9, c10, c11, c12, c13, c14, c15, c16, c17, c18
, c19, c20, c21, c22, c23, c24, c25, c26, c27, c28, c29, c30;
18
                      //chains together 32 instantiations of a 1-bit adder fulladder FA0(.A(A[0]), B(B[0]), cin(cin), .sum(sum[1]), .cout(c0)); fulladder FA1(.A(A[1]), B(B[1]), .cin(c0), .sum(sum[1]), .cout(c1)); fulladder FA2(.A(A[2]), B(B[2]), .cin(c1), .sum(sum[2]), .cout(c2)); fulladder FA3(.A(A[2]), B(B[3]), .cin(c2), .sum(sum[3]), .cout(c3)); fulladder FA4(.A(A[4]), B(B[4]), .cin(c3), .sum(sum[4]), .cout(c4)); fulladder FA5(.A(A[5]), B(B[5]), .cin(c4), .sum(sum[5]), .cout(c5)); fulladder FA6(.A(A[6]), B(B[6]), .cin(c5), .sum(sum[6]), .cout(c6)); fulladder FA7(.A(A[7]), B(B[7]), .cin(c6), .sum(sum[7]), .cout(c7)); fulladder FA8(.A(A[8]), B(B[8]), .cin(c7), .sum(sum[9]), .cout(c8)); fulladder FA9(.A(A[9]), B(B[9]), .cin(c6), .sum(sum[9]), .cout(c8)); fulladder FA9(.A(A[1]), B(B[10]), .cin(c9), .sum(sum[10]), .cout(c10)); fulladder FA1(.A(A[1]), B(B[11]), .cin(c10), .sum(sum[10]), .cout(c10)); fulladder FA1(.A(A[1]), B(B[11]), .cin(c10), .sum(sum[11]), .cout(c11)); fulladder FA1(.A(A[1]), B(B[13]), .cin(c11), .sum(sum[12]), .cout(c12)); fulladder FA1(.A(A[1]), B(B[13]), .cin(c11), .sum(sum[11]), .cout(c12)); fulladder FA1(.A(A[1]), B(B[14]), .cin(c13), .sum(sum[15]), .cout(c13)); fulladder FA1(.A(A[1]), B(B[14]), .cin(c13), .sum(sum[15]), .cout(c15)); fulladder FA1(.A(A[1]), B(B[14]), .cin(c15), .sum(sum[15]), .cout(c15)); fulladder FA1(.A(A[1]), B(B[14]), .cin(c10), .sum(sum[15]), .cout(c15)); fulladder FA1(.A(A[1]), B(B[14]), .cin(c10), .sum(sum[15]), .cout(c17)); fulladder FA1(.A(A[1]), B(B[14]), .cin(c10), .sum(sum[11]), .cout(c21)); fulladder FA1(.A(A[1]), B(B[14]), .cin(c10), .sum(sum[12]), .cout(c20)); fulladder FA2(.A(A[2]), B(B[24]), .cin(c20), .sum(sum[21]), .cout(c21)); fulladder FA2(.A(A[2]), B(B[24]), .cin(c20), .sum(sum[21]), .co
19
                         //Chains together 32 instantiations of a 1-bit adder.
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                        fulladder FA31(.A(A[<mark>31</mark>]), .B(B[<mark>31</mark>]), .cin(c30), .sum(sum[<mark>31</mark>]), .cout(cout));
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54
55
               endmodule
56
               //Tests fulladder32.
57
              module fulladder32_testbench();
58
59
                         logic [31:0] A, B, sum;
60
                        logic cin, cout;
61
62
                        fulladder32 dut (A, B, cin, sum, cout);
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65
                        integer i;
                        initial begin
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67
                                   A = 32'd15; B = 32'd16; cin = 1'b0; #10;
A = 32'd15; B = 32'd16; cin = 1'b1; #10;
A = 32'd15; B = 32'd163; cin = 1'b0; #10;
69
                                    A = 32'd15; B = 32'd216; cin = 1'b0; #10;
73
                                    A = 32'd15; B = 32'd146; cin = 1'b0; #10;
75
                        end //initial
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endmodule

Project: DE1_SoC