

# **Bangladesh University of Engineering and Technology**

Department of Computer Science and Engineering

## **Offline Assignment on Adder / Subtractor**

Deadline: Sunday, 17th December, 2023, 11:55 PM

### **Implement the following problems:**

Design a 1-bit full sub tractor circuit using basic logic gates. Inputs are P, Q and R denoting minuend, subtrahend and previous borrow respectively. The outputs are D and B representing the difference and output borrow.

**Submission Guideline:** Create a directory with your 7 digit student id as its name. Put the circuit files and a scanned PDF of your handwritten simplification into the directory created in step 1. Zip the directory (compress in .zip format; .rar, .7z or any other format is not acceptable), and upload the .zip file on MOODLE.