

guided by this video

Title		
Size A	Number	Revision
Date:	9/20/2024	Sheet of Azra Rangwala
File:	blockdiagram.SchDoc	Drawn By:

- 1 Mounting_Hole_M3
- 2 Mounting_Hole_M3
- 3 Mounting_Hole_M3
- 4 Mounting_Hole_M3

Subtractor

Peak Detector

threshold set

V+ - V-

MNT 1
MNT 2
PTV09A-4020F-A203

more current through diode,
lower attenuation

jack must be connected to work sidechain_2

If CV +, then signal > threshold then
diodes on, 0V in middle, lower
amplification

changing how strict the
difference between threshold
and signal is to be able to
clamp down on signal--> ratio
control. reduces distortion.

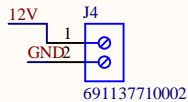
VCA

pick up output and
amplify it

lowers signal.

buffer

Jacks



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Date:	9/20/2024	Sheet of
File:	compressor_Schematic.SchDoc	Drawn By: