

Rev0

Original Rev for Memco

Took T3-8I8O design rev14 and reworked it for PWM project

Rev5

Done:R56,R58丝印错误

Rev1

Done: fix mux select lines

Done: require a TXEN line for RS485 com, pin3.2

Done: remove 470 current limiting resistors

Done: flip D33

Done: output feedback to pic should come from 5V side of OPamp

Done: One pin of each "hand" potentiometer must be tied to GND.

Done: Each "hand" potentiometer must be tied to 12V not 5V

Done: Connect analog GND to INPUT page

Done: update footprint of packet resistors

Done: R18 and R22 to move for cpu socket

Done: Outputs out of order on PCB

Done: Make sure all thru-holes have thermal relief

Done: move D33 to make space for pic socket

Done: crystal footprint updated

Done: move IN_CPU2 to Port0 given the internal pull-ups may affect Analog input reads

Done: remove pull up on P0 lines

Rev2

Done: added diode to decrease 5V line for 485 module, it would overheat otherwise

Done: timing cap for power chip changed to 120pF to reduce noise

Done: changed jumper select to switches

Done: changed the resistance for power to 1.89 and 5.96 for the 5V supply

Rev3

Done: rectify connection with Input Con. Was mis-labelled

Done: change cap by op-amp to 10uF. 100uF too slow

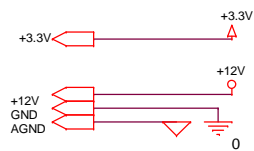
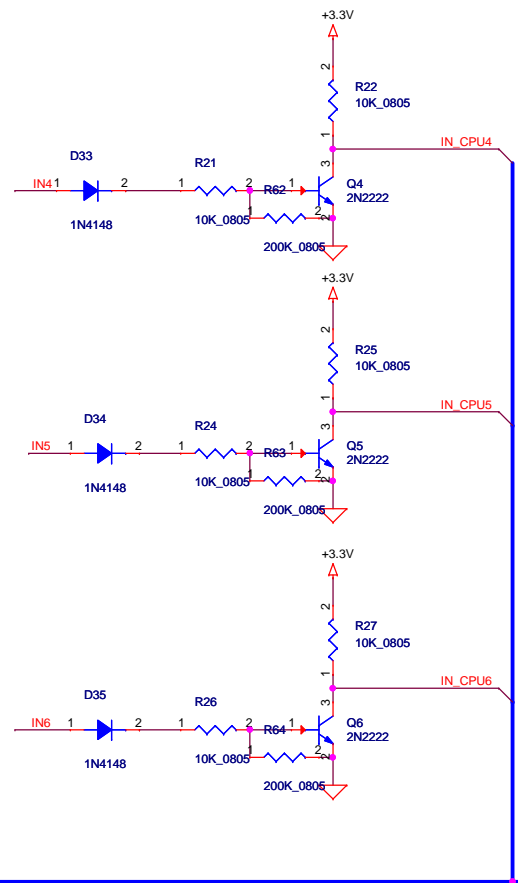
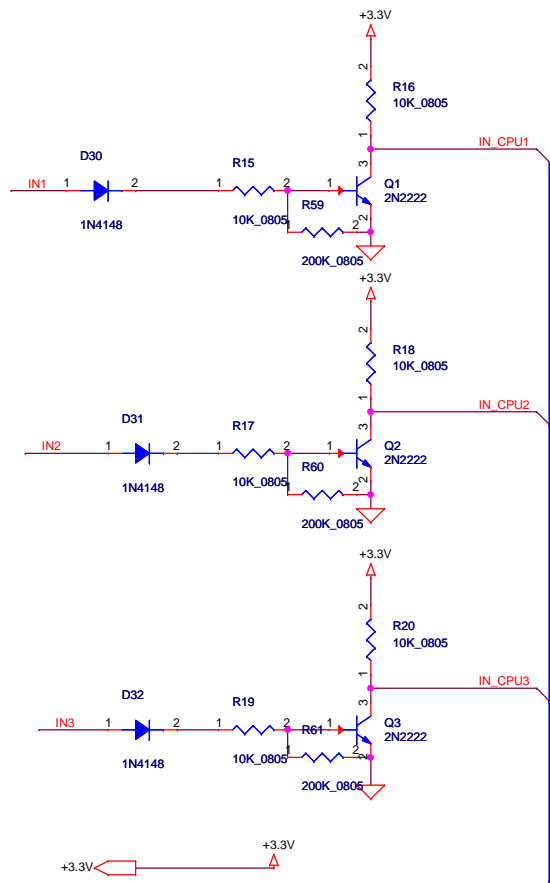
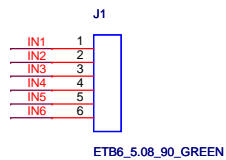
Done: fix footprint of switches

TBD: rectify footprint names of 2N2222, Q1, Q2 for BOM purposes

[Slick Screen on Board was mislabelled. Still reads Rev2](#)

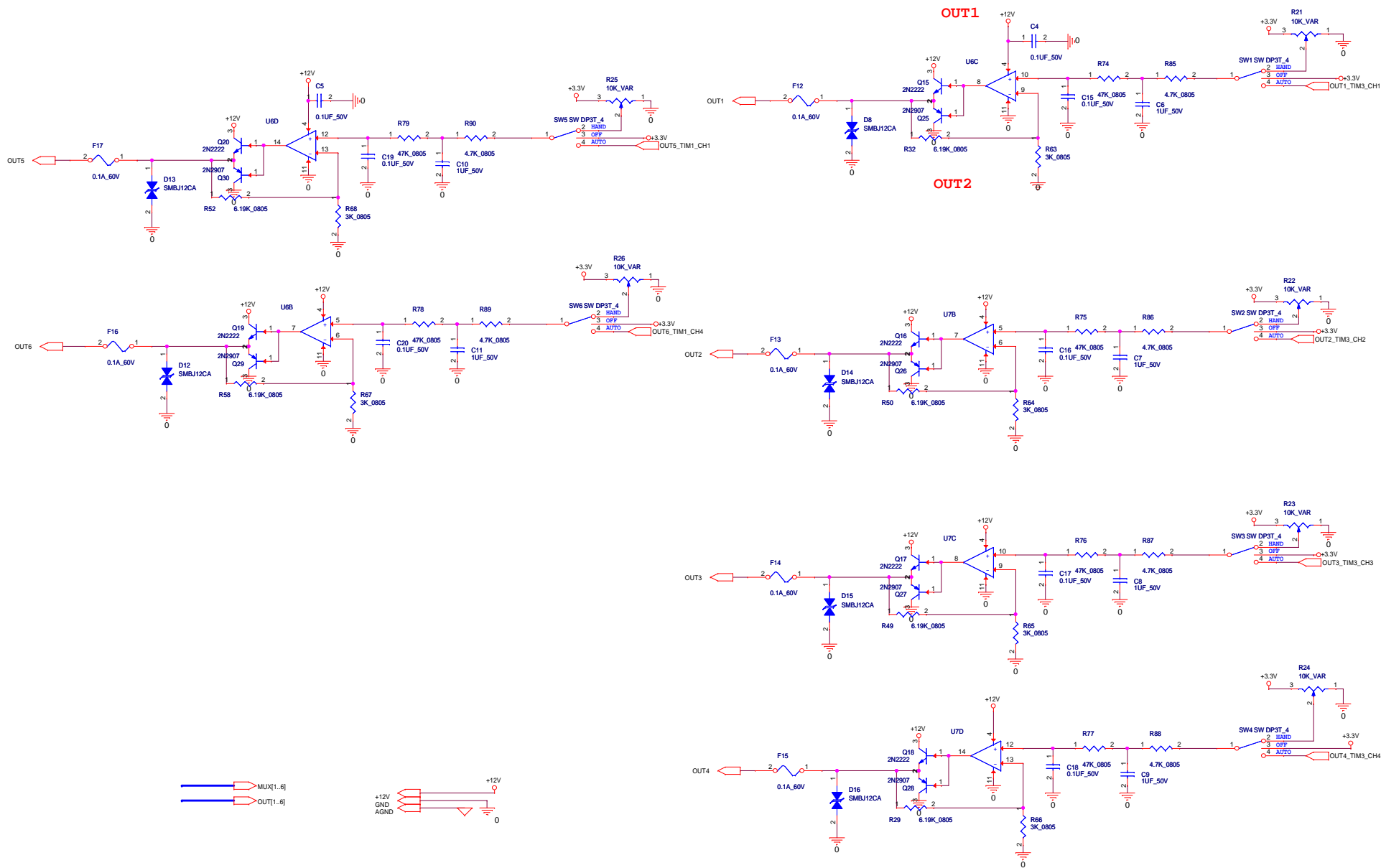
[note: make sure muxes are phillips brand. Does not work with Texas chips](#)

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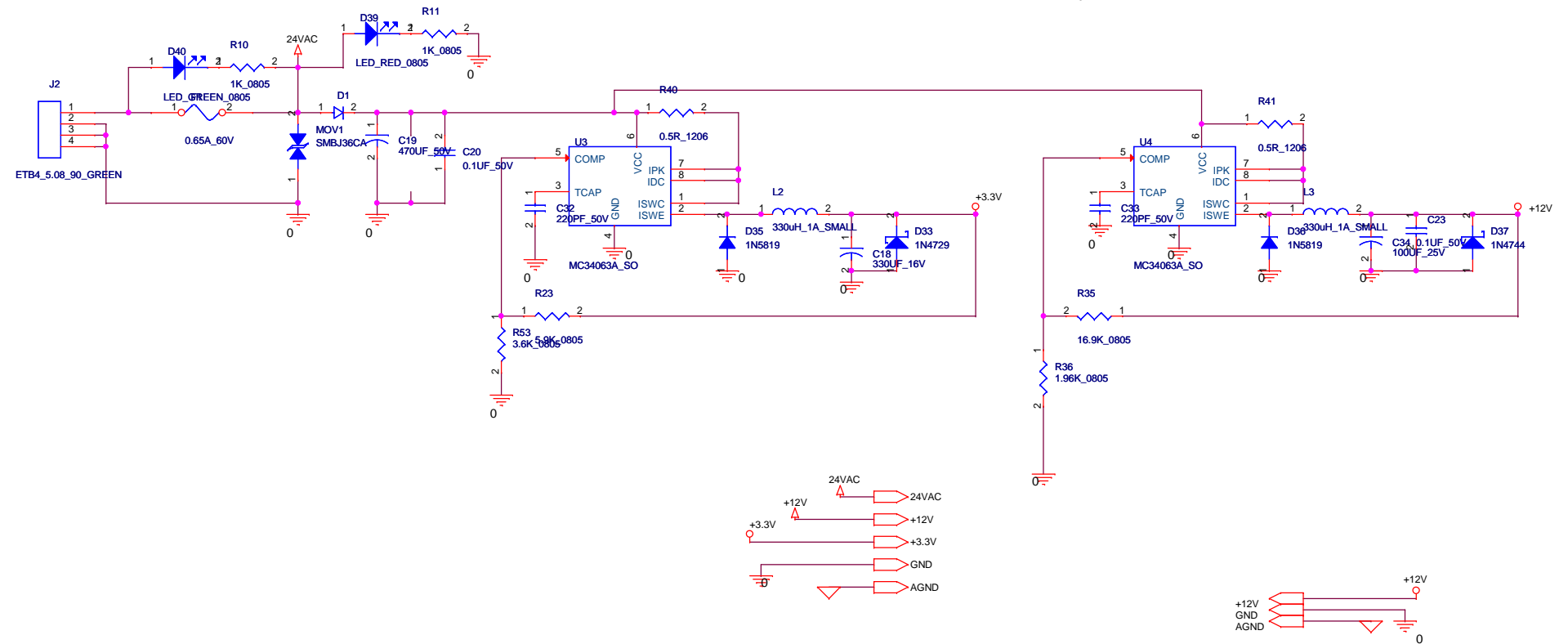


IN_CPU[1..6]

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POWER



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