







Rev0

Original Rev for Memco

Took T3-8I8O design rev14 and reworked it for PWM project

Rev1

Done: fix mux select lines

Done: require a TXEN line for RS485 com, pin3.2

Done: remove 470 current limitting resistors

Done: flip D33

Done: output feedback to pic should come from 5V side of OPamp Done: One pin of each "hand" potentiometer must be tied to GND.

Done: Each "hand" potentiometer must be tied to 12V not 5V

Done: Connect analog GND to INPUT page Done: update footprint of packet resistors Done: R18 and R22 to move for cpu socket

Done: Outputs out of order on PCB

Done: Make sure all thru-holes have thermal relief Done: move D33 to make space for pic socket

Done: crystal footprint updated

Done: move IN_CPU2 to Port0 given the internal pull-ups may affect Analog input reads

Done: remove pull up on P0 lines

Rev2

Done: changed the resistance for power to 1.89 and 5.96 for the 5V supply

Done: timing cap for power chip changed to 120pF to reduce noise

TBD: rectify footprint names of 2N2222, Q1, Q2

Rev3 (Not built yet)

MD: put back current limitting resistors on outputs, 470R