

1. For a circuit with k lines _____ single stuck at fault is possible.

- A) K
- B) 2k
- C) k/2
- D) k^2

Ans: B)2k

2. Single stuck at fault is technology independent

- True
- False

Ans: True

3. Decoder is constructed from _____

- A) Inverters
- B) AND gates
- C) Both A and B
- D) None of the above

Ans : c)Both A and B

4. Stuck at fault modelling comes under _____ level fault model.

- A) Behavioral
- B) Functional
- C) Structural
- D) Switch level

Ans: C) Structural

5. Fault test cases can be minimized by which of the following ?

- A) Fault deletion
- B) Fault rectification
- C) Fault equivalence
- D) None of the above