1.which of the following can be represented for decoder?

A) sequential circuit

B) combinational circuit

C) Logic Circuit

D) None of the above

Ans: B) combinational circuit

2. Decoder converts __ inputs to ___ outputs.

A) 2^N , N

B) N, 2^N

C)N,2N

D)2N,N

Ans:B)N , 2^N

3. Which type/s of stuck at fault model exhibit/s the reduced complexity level of test generation?

A) Multiple

B) Single

C) Both A and B

D) None of the above

Ans: A) Multiple

4. Which of the following is the application of decoder ?

A) Converts anlog audio to digital form

B) Converts computer instructions to CPU control signal.

C) Both A and B

D) None of these

Ans: C) Both A and B

5. A 4 x 16 decoder can be constructed (with enable input) by :

A) With five 2 x 4 decoders(each with enable inputs) only

B) Using two 3 x 8 decoder(each with enable) only

C) Both A and B

(D) All of the above

Ans : A