

32-1 1 bit muxxer

🕒 Created	@October 6, 2024 10:13 PM
🏷️ Tags	
🌟 Status	Not started

32-to-1 32-Bit Multiplexer Truth Table

S4	S3	S2	S1	S0	Selected Input	Y (Output)
0	0	0	0	0	$I_0[31:0]$	$I_0[31:0]$
0	0	0	0	1	$I_1[31:0]$	$I_1[31:0]$
0	0	0	1	0	$I_2[31:0]$	$I_2[31:0]$
0	0	0	1	1	$I_3[31:0]$	$I_3[31:0]$
0	0	1	0	0	$I_4[31:0]$	$I_4[31:0]$
0	0	1	0	1	$I_5[31:0]$	$I_5[31:0]$
0	0	1	1	0	$I_6[31:0]$	$I_6[31:0]$
0	0	1	1	1	$I_7[31:0]$	$I_7[31:0]$
0	1	0	0	0	$I_8[31:0]$	$I_8[31:0]$
0	1	0	0	1	$I_9[31:0]$	$I_9[31:0]$
0	1	0	1	0	$I_{10}[31:0]$	$I_{10}[31:0]$
0	1	0	1	1	$I_{11}[31:0]$	$I_{11}[31:0]$
0	1	1	0	0	$I_{12}[31:0]$	$I_{12}[31:0]$
0	1	1	0	1	$I_{13}[31:0]$	$I_{13}[31:0]$
0	1	1	1	0	$I_{14}[31:0]$	$I_{14}[31:0]$
0	1	1	1	1	$I_{15}[31:0]$	$I_{15}[31:0]$
1	0	0	0	0	$I_{16}[31:0]$	$I_{16}[31:0]$
1	0	0	0	1	$I_{17}[31:0]$	$I_{17}[31:0]$
1	0	0	1	0	$I_{18}[31:0]$	$I_{18}[31:0]$
1	0	0	1	1	$I_{19}[31:0]$	$I_{19}[31:0]$
1	0	1	0	0	$I_{20}[31:0]$	$I_{20}[31:0]$
1	0	1	0	1	$I_{21}[31:0]$	$I_{21}[31:0]$
1	0	1	1	0	$I_{22}[31:0]$	$I_{22}[31:0]$
1	0	1	1	1	$I_{23}[31:0]$	$I_{23}[31:0]$
1	1	0	0	0	$I_{24}[31:0]$	$I_{24}[31:0]$
1	1	0	0	1	$I_{25}[31:0]$	$I_{25}[31:0]$
1	1	0	1	0	$I_{26}[31:0]$	$I_{26}[31:0]$
1	1	0	1	1	$I_{27}[31:0]$	$I_{27}[31:0]$
1	1	1	0	0	$I_{28}[31:0]$	$I_{28}[31:0]$

1	1	1	0	1	$I_{29}[31:0]$	$I_{29}[31:0]$
1	1	1	1	0	$I_{30}[31:0]$	$I_{30}[31:0]$
1	1	1	1	1	$I_{31}[31:0]$	$I_{31}[31:0]$