DP_SingleBit_logic

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≔ Tags	
⚙ Status	Not started

Test Case	B	S0	S1	Output Y
0	0	0	1	1
1	0	1	0	0
2	0	1	1	1
3	1	0	0	0
4	1	0	1	0
5	1	1	0	1
6	1	1	1	1