

RF_Register32Bit_22336157_Doc

Created	@October 17, 2024 2:34 PM
Tags	
Status	Not started

CLK	Reset	Load	D (Input)	Q (Output)	Description
0	1	0	00000000000000000000000000000000	00000000000000000000000000000000	Reset register (Q = 0)
↑	0	1	00000001010101001101001010011101	00000001010101001101001010011101	Load StudentID into Q
↑	0	0	00000001010101001101001010011101	00000001010101001101001010011101	Hold loaded value in Q
↑	0	0	00000001010101001101001010011101	00000001010101001101001010011101	Continue holding value