

RF_Register32Bit_22336157_Doc

🕒 Created	@October 17, 2024 2:34 PM
🏷 Tags	
🚦 Status	Not started

CLK	Reset	Load	D (Input)	Q (Output)
0	1	0	00000000000000000000000000000000	00000000000000000000000000000000
↑	0	1	00000001010101001101001010011101	00000001010101001101001010011101
↑	0	0	00000001010101001101001010011101	00000001010101001101001010011101
↑	0	0	00000001010101001101001010011101	00000001010101001101001010011101