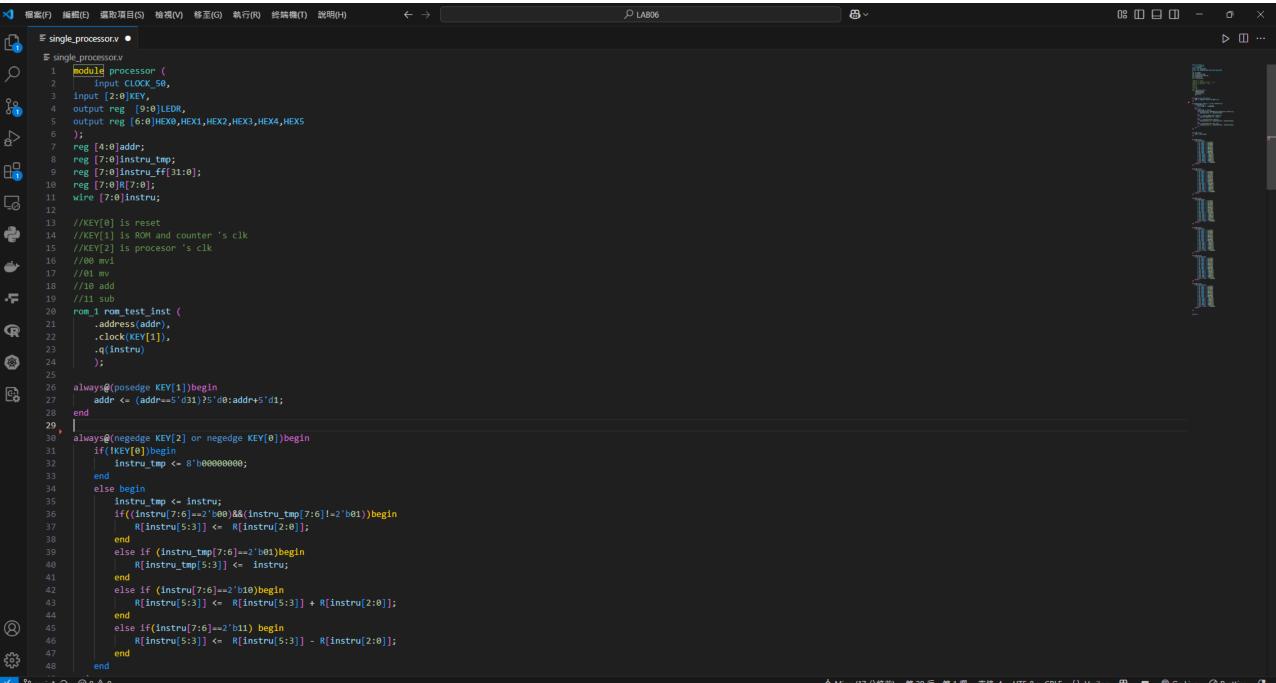
#### LAB06\_312651057

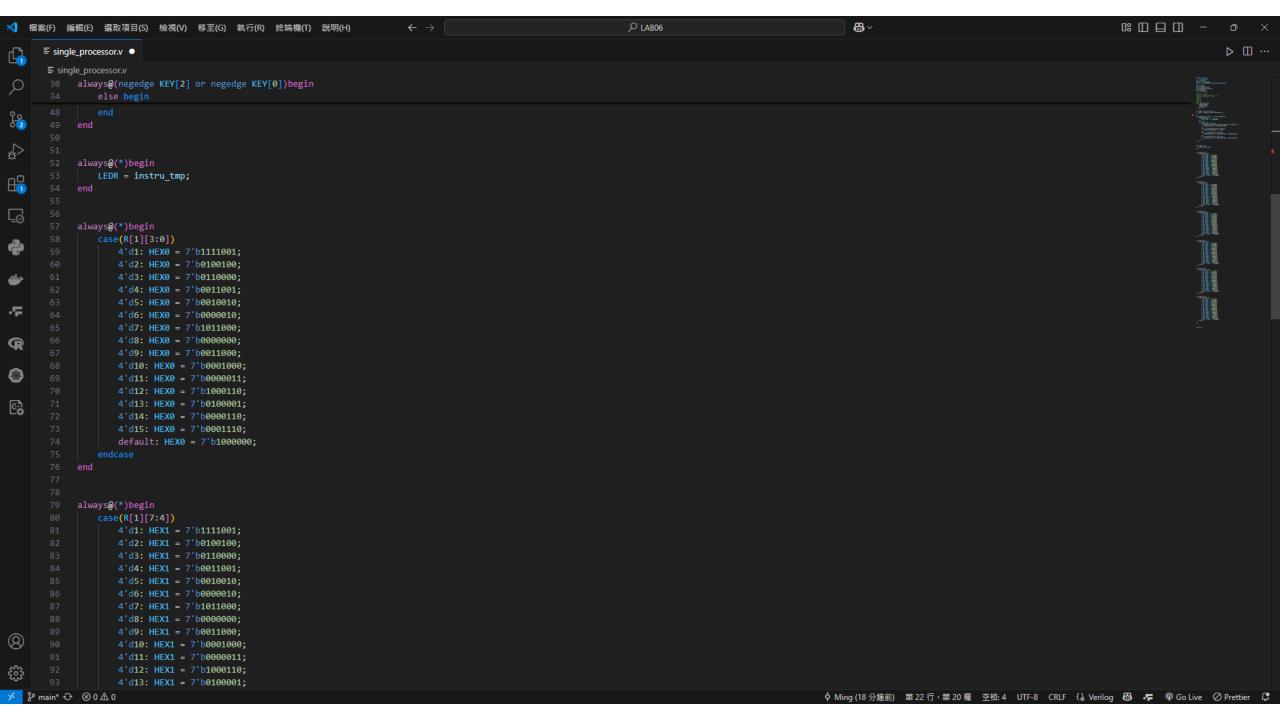
吳鴻明

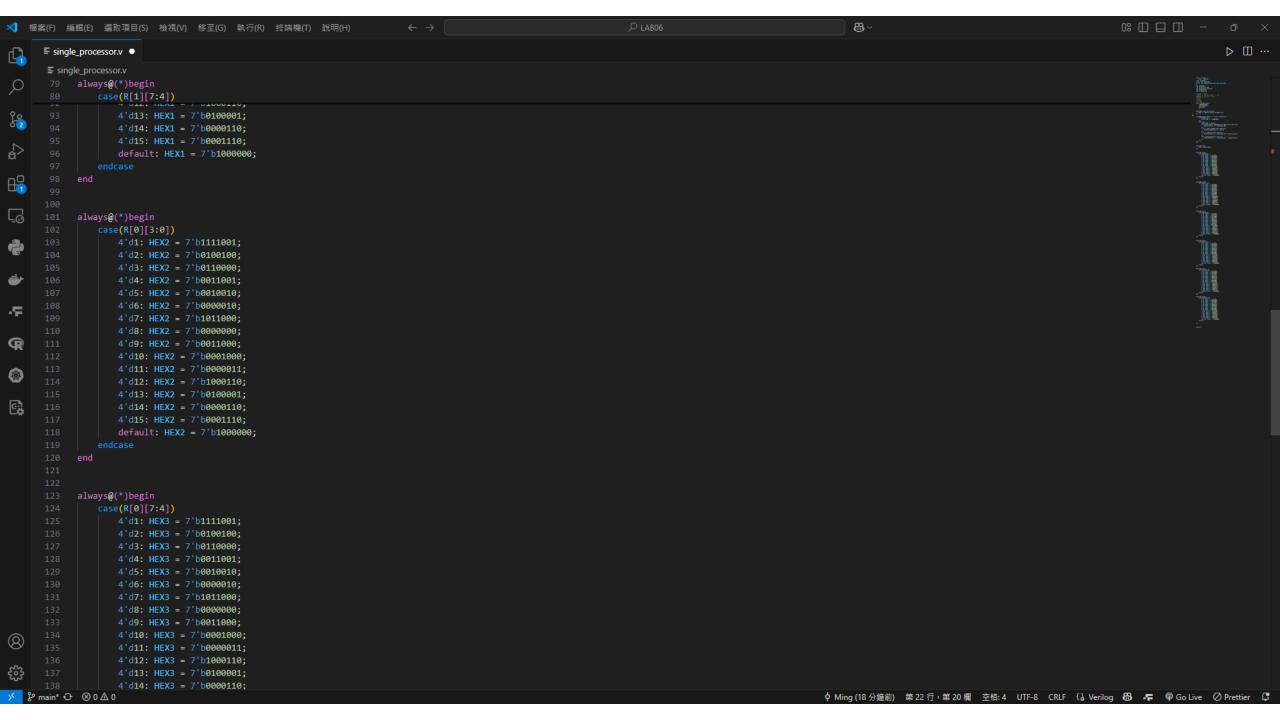
## 實驗目的

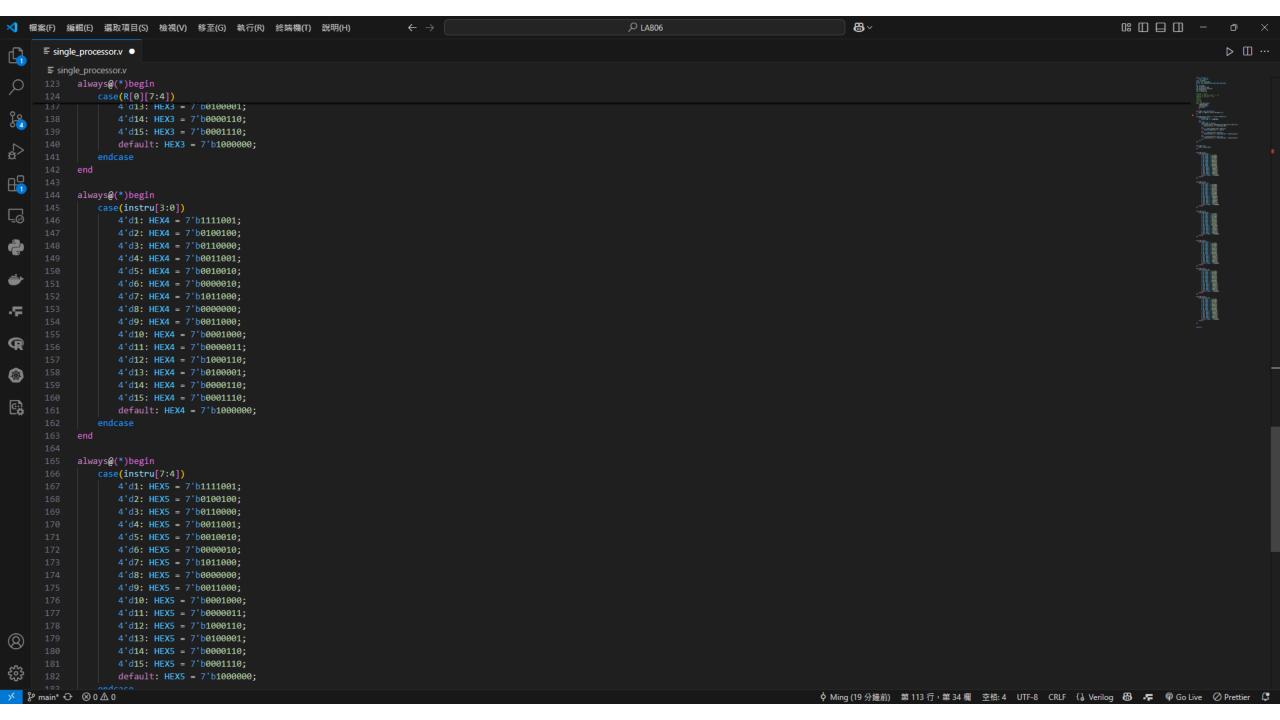
製作一個single processor使用single port ROM 當作 instruction memory,將IM裡面的指令讀出,並且透過decoder解碼,並做出相對應的運算。

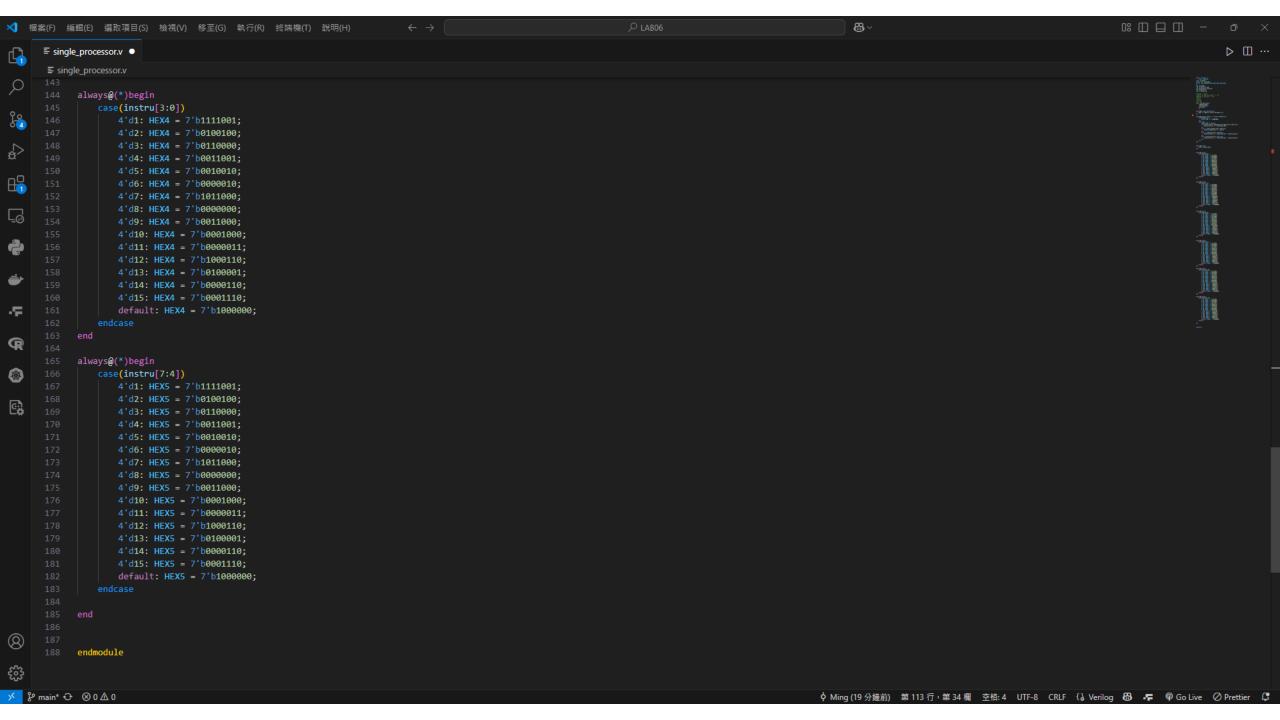
## 實驗程式碼



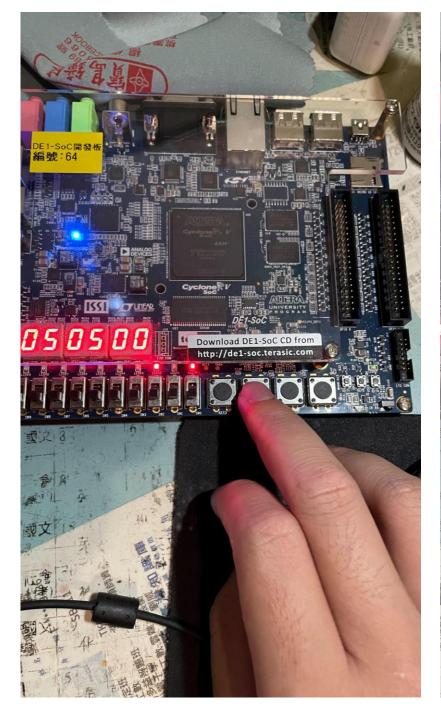


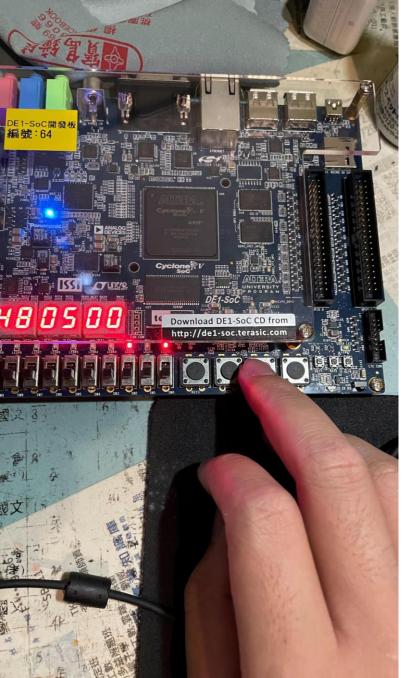


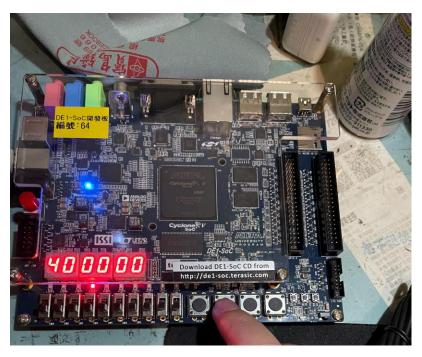


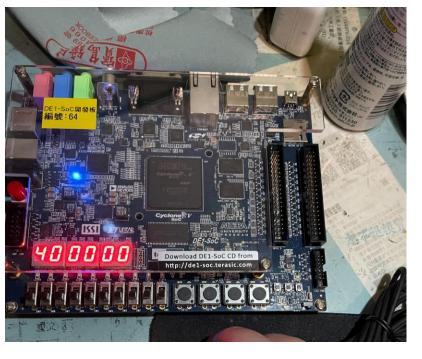


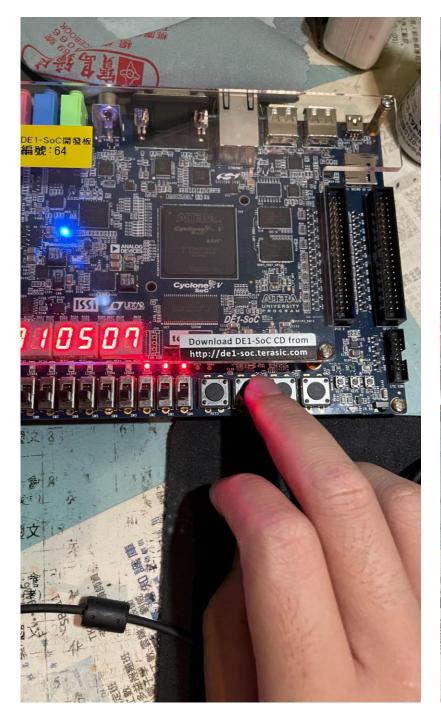
## 實驗結果照片

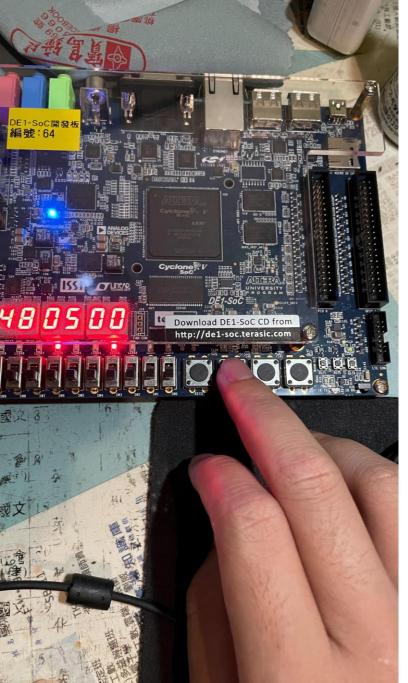






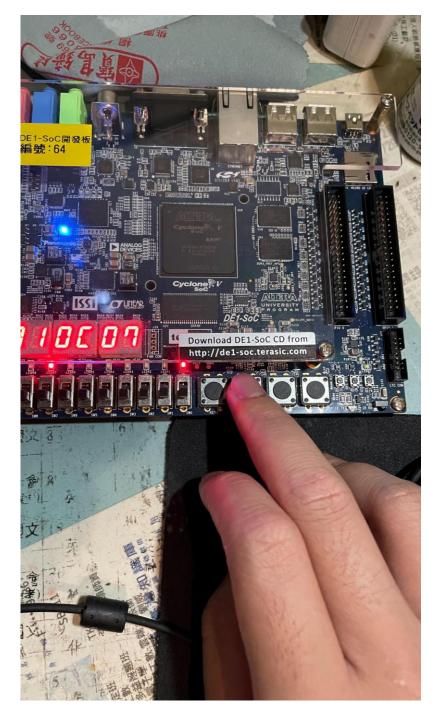


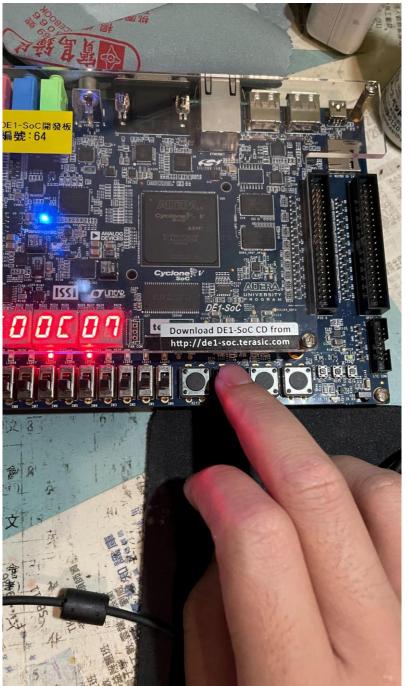








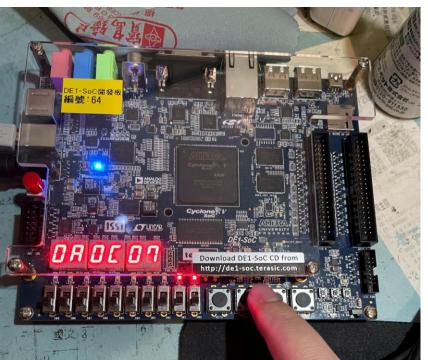


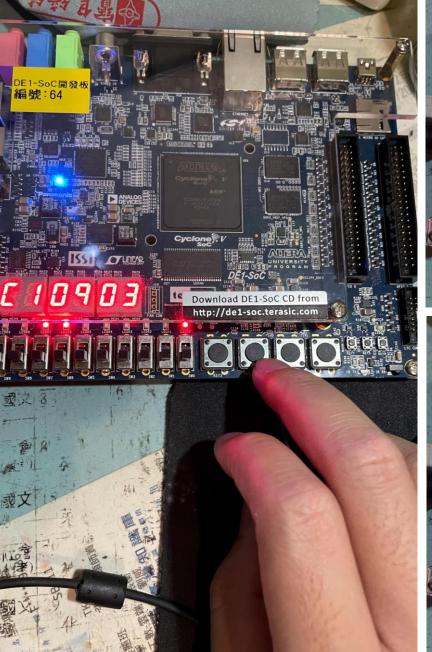
















# RTL佈局

如附檔RTL\_syn.pdf所示

### 問題與討論

問題:在撰寫CODE時,遇到ROM所讀出的第一個值不是IM中的第一個位置的值,以及在做mvi的時候其他R1會跳出奇怪得值。

討論: 在仔細端詳code後發覺, if-else寫的條件有誤 (解決: mvi R1出現錯誤)以及counter與ROM的CLK需 要都是正緣觸發,才可以正確地讀出第一個值。