

Resource usage

LUT	FF	BRAM	URAM	DSP
202	97	0	0	3

Site Type	Used	Fixed	Prohibited	Available	Util%
Slice LUTs*	202	0	0	53200	0.38
LUT as Logic	202	0	0	53200	0.38
LUT as Memory	0	0	0	17400	0.00
Slice Registers	97	0	0	106400	0.09
Register as Flip Flop	97	0	0	106400	0.09
Register as Latch	0	0	0	106400	0.00
F7 Muxes	0	0	0	26600	0.00
F8 Muxes	0	0	0	13300	0.00

Detailed RTL Component Info :

+---Adders :

2 Input	32 Bit	Adders := 1
2 Input	8 Bit	Adders := 5

+---Registers :

	32 Bit	Registers := 2
	8 Bit	Registers := 3
	1 Bit	Registers := 13

+---Multipliers :

	32x32	Multipliers := 1
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+---Muxes :

8 Input	32 Bit	Muxes := 1
2 Input	32 Bit	Muxes := 5
2 Input	8 Bit	Muxes := 7
8 Input	8 Bit	Muxes := 3
8 Input	3 Bit	Muxes := 1
2 Input	3 Bit	Muxes := 1
2 Input	2 Bit	Muxes := 2
2 Input	1 Bit	Muxes := 6
8 Input	1 Bit	Muxes := 19