

Part 2:

The screenshot shows the MVTSC-318T Lab 4/5 - RARS 1.5 interface. The main window displays assembly code with columns for Bkpt, Address, Code, Basic, and Source. The Data Segment window shows memory addresses and their corresponding values. The Registers and Floating Point window displays the state of various registers.

Name	Number	Value
ustatus	0	0x00000000
fflags	1	0x00000000
fpm	2	0x00000000
fcsr	3	0x00000000
uie	4	0x00000000
utvec	5	0x00000000
uscratch	64	0x00000000
urpc	65	0x00000000
ucasee	66	0x00000000
utval	67	0x00000000
nip	68	0x00000000
cycle	3072	0x00000000
time	3073	0x00000000
instret	3074	0x00000000
cycleh	3200	0x00000000
timeh	3201	0x00000000
instreth	3202	0x00000000

Part 3:

The screenshot shows the ITSC 3181 Lab 4/5 interface. The main window displays a C program named loop.c. The program includes a while loop that increments a counter until it reaches 60, then prints "Success" and returns 42. The Console window shows the output of the program, including the execution of gcc, ls, and the program's output.

```

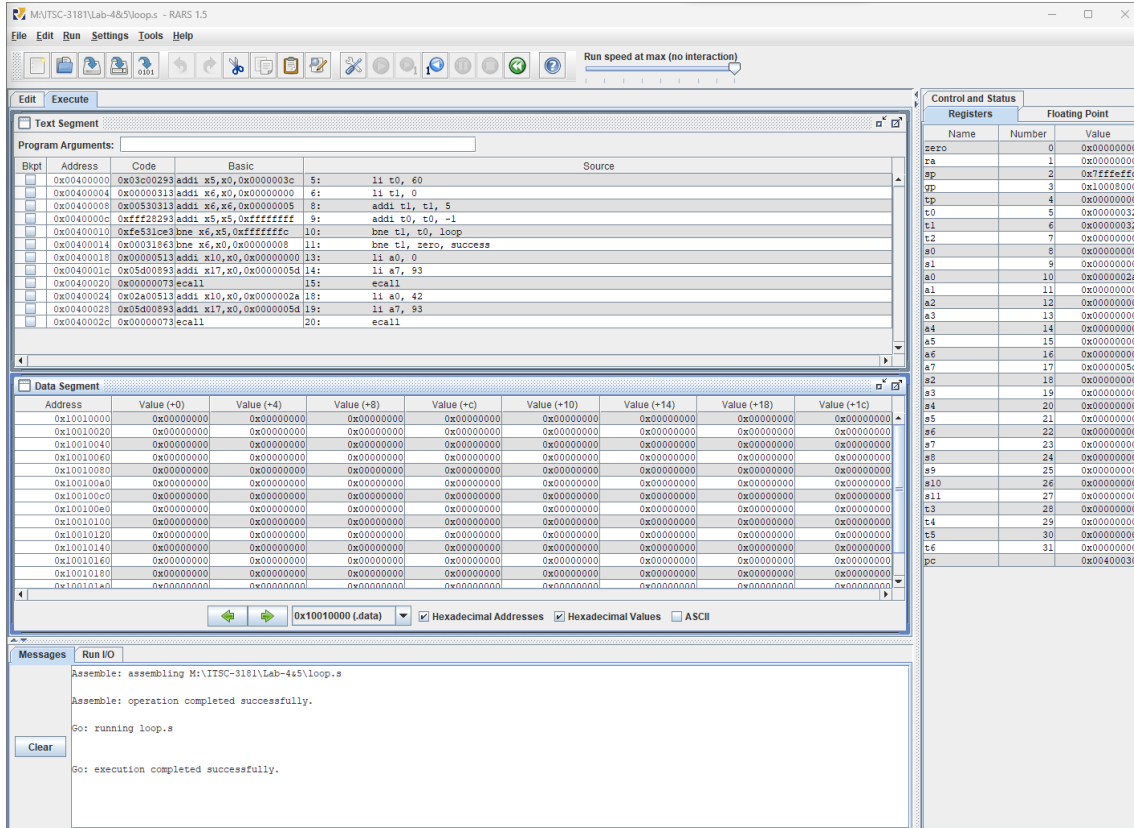
1 #include <stdio.h>
2
3 int main() {
4     int t0 = 60;
5     int t1 = 0;
6
7     while (t1 != t0) {
8         t1 += 5;
9         t0--;
10    }
11
12    if (t1 == 0) {
13        printf("Success\n");
14        return 42;
15    } else {
16        printf("Failure\n");
17        return 0;
18    }
19 }
20

```

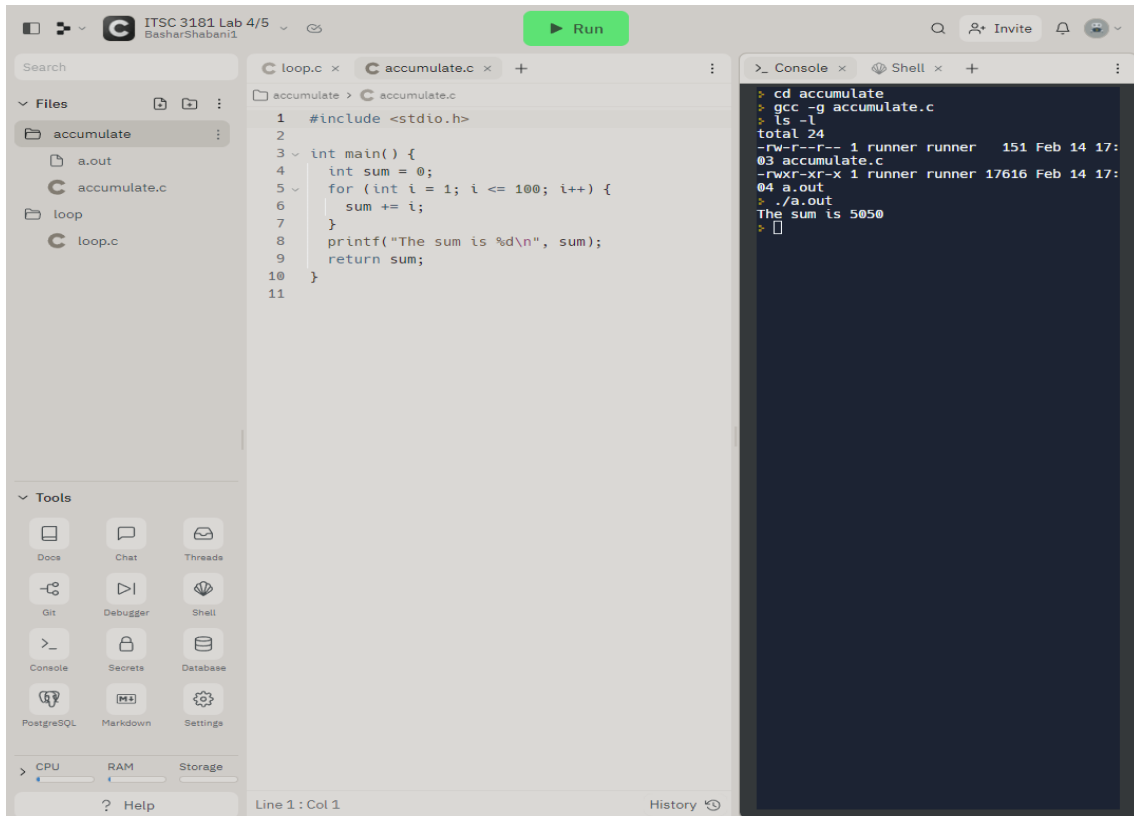
```

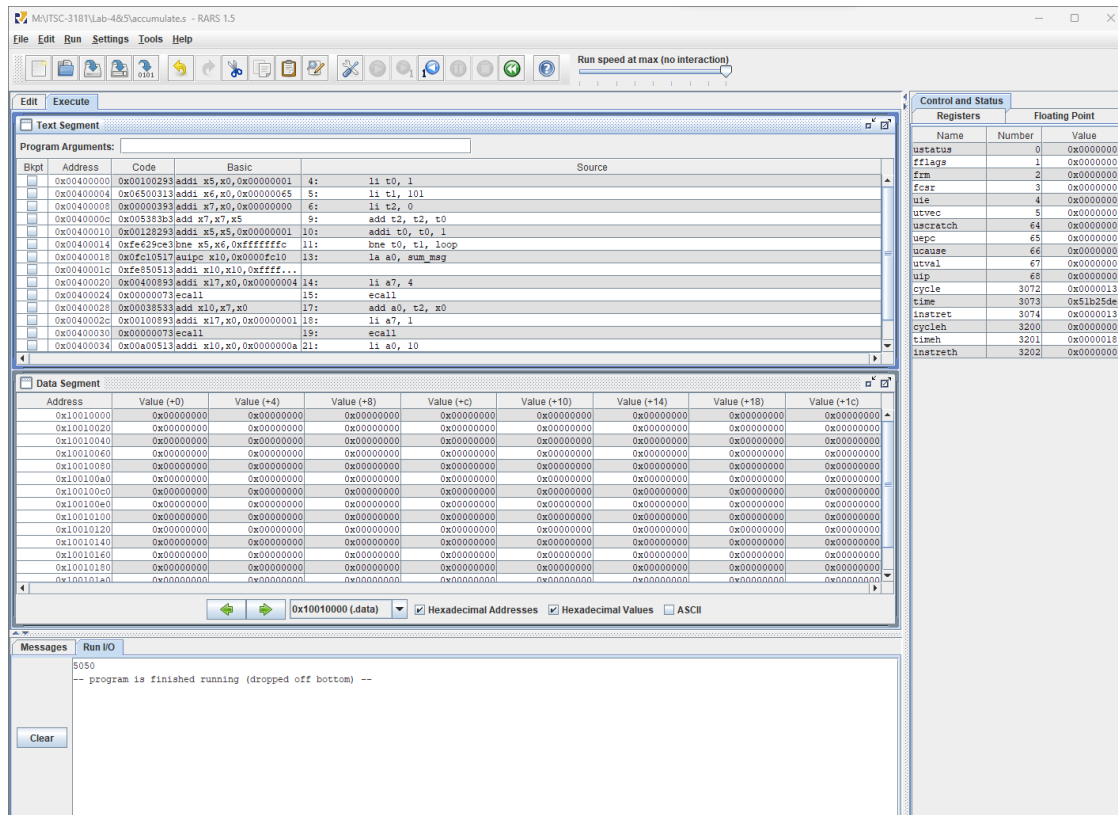
> gcc -g loop.c
> ls -l
total 88
-rwxr-xr-x 1 runner runner 17632 Feb 14 16:
55 a.out
-rw-r--r-- 1 runner runner 263 Feb 14 16:
50 loop.c
-rwxr-xr-x 1 runner runner 54536 Sep 8 14:
00 main
-rw-r--r-- 1 runner runner 411 Oct 13 22:
06 Makefile
-rw-r--r-- 1 runner runner 81 Jan 18 20
22 replit.nix
> ./a.out
Failure
>

```

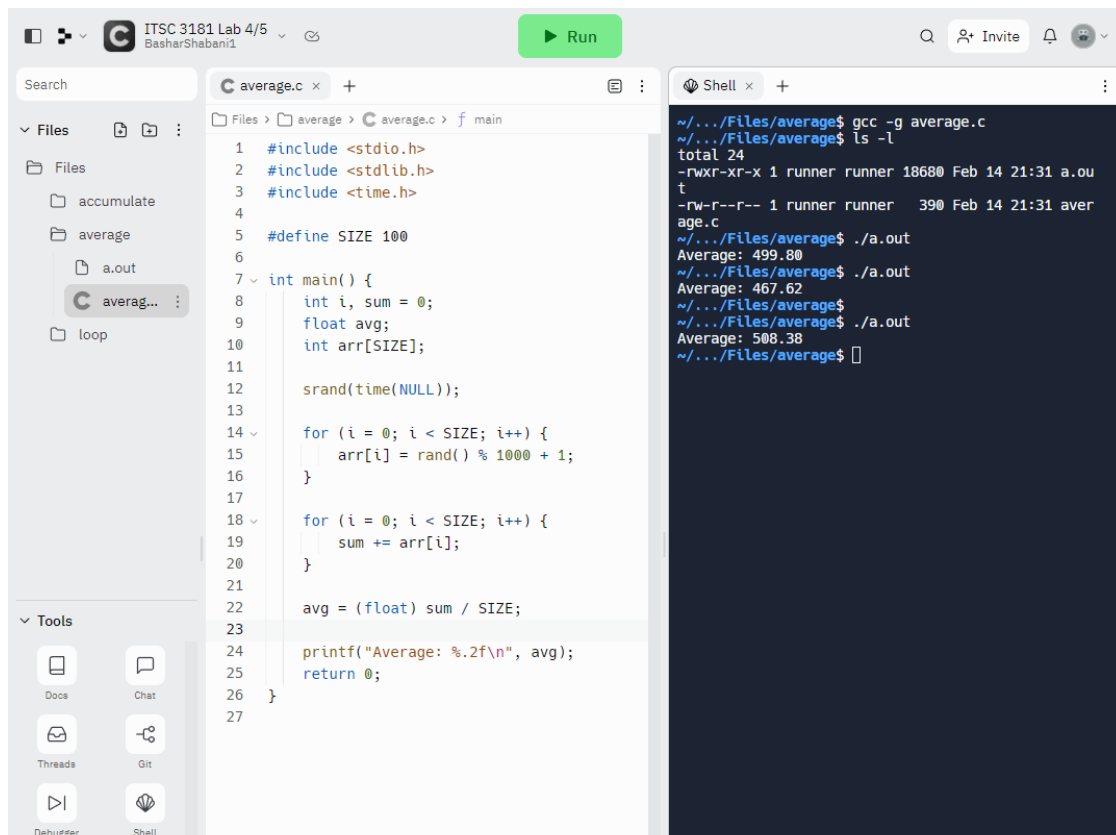


Part 4:





Part 5:



MITSC-3181-Lab-4B5-accumulates - RARS 1.5

File Edit Run Settings Tools Help

Run speed at max (no interaction)

Text Segment

Program Arguments:

Bkpt	Address	Code	Basic	Source
	0x00400000	0x00100299	addi x5,x0,0x00000001	4: li t0, 1
	0x00400004	0x00450013	addi x6,x0,0x00000005	5: li t1, 101
	0x00400008	0x00000393	addi x7,x0,0x00000000	6: li t2, 0
	0x0040000c	0x00533b3b	add x7,x7,x5	9: add t2, t2, t0
	0x00400010	0x00128293	addi x5,x5,0x00000001	10: addi t0, t0, 1
	0x00400014	0x0042f0e3	bne x5,x6,0xffffffff	11: bne t0, t1, loop
	0x00400018	0x00fc1051	auipc x10,0x00000fc10	13: la a0, sum_reg
	0x0040001c	0x00e50513	addi x10,x10,0xffffffff	
	0x00400020	0x00400893	addi x17,x0,0x00000004	14: li a7, 4
	0x00400024	0x00000073	ecall	15: ecall
	0x00400028	0x00088533	add x10,x7,x0	17: add a0, t2, x0
	0x0040002c	0x00100893	addi x17,x0,0x00000001	18: li a7, 1
	0x00400030	0x00000073	ecall	19: ecall
	0x00400034	0x00a00513	addi x10,x0,0x0000000a	21: li a0, 10

Data Segment

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010160	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010180	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100101a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

Control and Status

Registers	Number	Value
accstatus	0	0x00000000
fflags	1	0x00000000
fem	2	0x00000000
fcar	3	0x00000000
uie	4	0x00000000
utvec	5	0x00000000
unscratch	64	0x00000000
utpc	65	0x00000000
ucause	66	0x00000000
utval	67	0x00000000
uip	68	0x00000000
cycle	3072	0x00000139
time	3073	0x51b25dea
instret	3074	0x00000139
cycleh	3200	0x00000000
timeh	3201	0x00000134
instreth	3202	0x00000000

Messages

5050
-- program is finished running (dropped off bottom) --

Clear

Part 6:

ITSC 3181 Lab 4/5
BasharShabani1

Run

Search

Files

- 1-DStencil
- 1-DStencil...
- a.out
- accumulate
- average
- loop

Tools

- Docs
- Chat
- Threads
- Git
- Debugger
- Shell
- Console
- Secrets
- CPU
- RAM
- Storage

1-DStencil.c

```

1 #include <stdio.h>
2 #include <stdlib.h>
3 #include <time.h>
4
5 #define M 100
6
7 int main(void) {
8     int B[M];
9     int B2[M];
10
11     srand(time(NULL));
12
13     for (int i = 0; i < M; i++) {
14         B[i] = rand() % 100;
15     }
16
17     for (int i = 1; i < M - 1; i++) {
18         B2[i] = (B[i - 1] + B[i] + B[i + 1]) /
19 3;
20     }
21
22     B2[0] = B[0];
23     B2[M - 1] = B[M - 1];
24
25     printf("B: ");
26     for (int i = 0; i < M; i++) {
27         printf("%d ", B[i]);
28     }
29     printf("\n");
30
31     printf("B2:");
32     for (int i = 0; i < M; i++) {
33         printf("%d ", B2[i]);
34     }
35     printf("\n");
36     return 0;
37 }

```

Shell

```

~/ITSC-3181-Lab-45$ cd Files
~/ITSC-3181-Lab-45/Files$ cd 1-DStencil
~/.../Files/1-DStencil$ gcc -g 1-DStencil.c
~/.../Files/1-DStencil$ ls -l
total 24
-rw-r--r-- 1 runner runner 759 Feb 14 23:34 1-DStencil.c
-rwxr-xr-x 1 runner runner 19800 Feb 14 23:34 a.out
~/.../Files/1-DStencil$ ./a.out
B: 31 39 67 17 10 31 99 49 46 25 47 42 84 85 42 98
8: 29 8 97 28 15 47 94 60 23 76 98 83 79 56 54 77
32 54 86 44 30 66 27 12 29 89 10 9 96 64 46 77 24
43 57 40 42 51 0 65 27 51 1 7 59 55 36 43 61 23 88
43 41 67 7 70 56 18 31 4 82 77 82 6 72 91 98 15 4
3 51 32 22 2 85 81 13 41 18 56 54 93 96 50 34 64 5
7 56 72 27 39 77 9 17 11 68 41 2 18 8 97
B2:29 44 44 46 30 52 67 59 53 65 85 86 72 63 62 54
54 57 61 53 46 41 35 22 43 42 36 38 56 68 62 49 4
8 41 46 46 44 31 38 30 47 26 19 22 40 50 44 46 42
57 51 57 50 38 48 44 48 35 17 39 54 80 55 53 56 87
68 52 36 42 35 18 36 56 59 45 24 38 42 67 81 79 6
0 49 51 59 61 51 46 47 41 34 12 32 40 37 20 9 41 9
7
~/.../Files/1-DStencil$

```

Line 23 : Col 1

History

MVITSC-318T Lab-48.5.1-DStencils - RARS 1.5

File Edit Run Settings Tools Help

Run speed at max (no interaction)

loop.s accumulate.s riscv1.asm average.s 1-DStencils.s

```
19 loop:
20     add a0,t1,zero
21     addi a1,zero,100
22     li a7,42
23     scall
24     sw a0,(t0)
25     addi t0,t0,4
26     addi t1,t1,1
27     li t5,100
28     bne t1,t5,loop
29     la a0,array1
30     la a1,elem8
31     la a2,randArray
32     addi a2,a2,4
33     jal printArray
34     la a0,randArray
35     addi a0,a0,4
36     la a1,stencilArray
37     li s0,0
38     li s1,2
39     li s2,4
40 stencil:
41     li a5,0
42     addi t1,s0,-4
43     add t1,a0,t1
44
```

Line: 96 Column: 10 ☒ Show Line Numbers

Messages Run I/O

Elements of the array::
Element number 1::95
Element number 2::32
Element number 3::81
Element number 4::25
Element number 5::8
Element number 6::10
Element number 7::38
Element number 8::40
Element number 9::49
Element number 10::74
Element number 11::61
Element number 12::0
Element number 13::19
Element number 14::76
Element number 15::14
Element number 16::90
Element number 17::21
Element number 18::48
Element number 19::68
Element number 20::66

Registers Floating Point Control and Status

Name	Number	Value
ustatus	0	0x00000000
fflags	1	0x00000000
frm	2	0x00000000
fsr	3	0x00000000
uie	4	0x00000000
utvec	5	0x00000000
uscratch	64	0x00000000
usep	65	0x00000000
ucause	66	0x00000000
utval	67	0x00000000
usp	68	0x00000000
cycle	3072	0x000019f5
time	3073	0x53308849
instret	3074	0x000019f5
cycleh	3200	0x00000000
timeh	3201	0x0000018e
instreth	3202	0x00000000