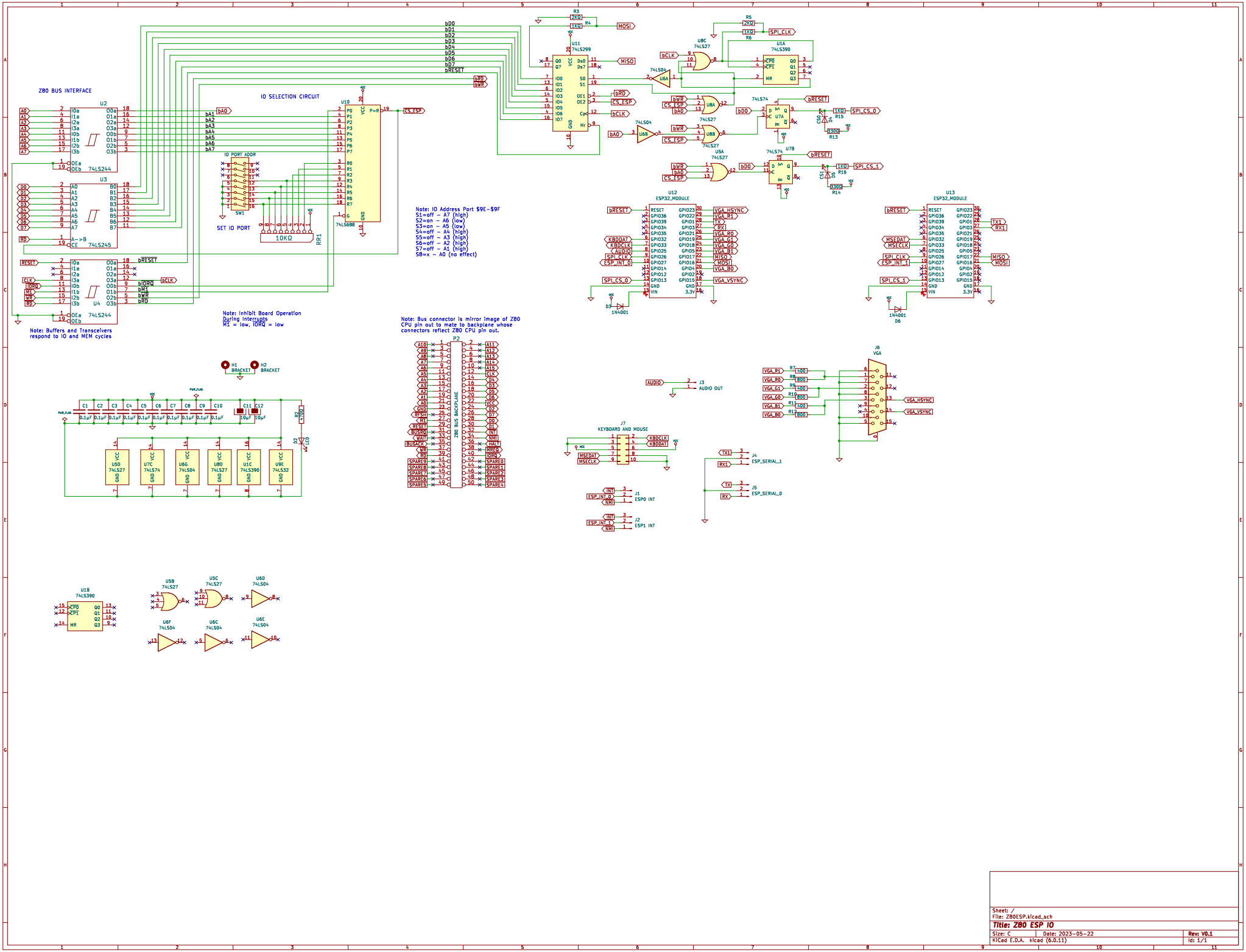
Z80 BUS INTERFACE
IO SELECTION CIRCUIT
IO PORT ADDR
SET IO PORT
Note: IO Address Port $9E-$9F
S1=off - A7 (high)
S2=on - A6 (low)
S3=on - A5 (low)
S4=off - A4 (high)
S5=off - A3 (high)
S6=off - A2 (high)
S7=off - A1 (high)
S8=x - A0 (no effect)
Note: Buffers and Transceivers respond to IO and MEM cycles
Note: Inhibit Board Operation During Interrupts
M1 = low, IORQ = low
Note: Bus connector is mirror image of Z80 CPU pin out to mate to backplane whose connectors reflect Z80 CPU pin out.
Z80 BUS BACKPLANE
KEYBOARD AND MOUSE
AUDIO OUT
VGA
ESP_SERIAL_1
ESP_SERIAL_0
ESP0 INT
ESP1 INT
Sheet: /
File: Z80ESP.kicad_sch
Title: Z80 ESP IO
Size: C
Date: 2023-05-22
Rev: V0.1
KiCad E.D.A. kicad (6.0.11)
Id: 1/1