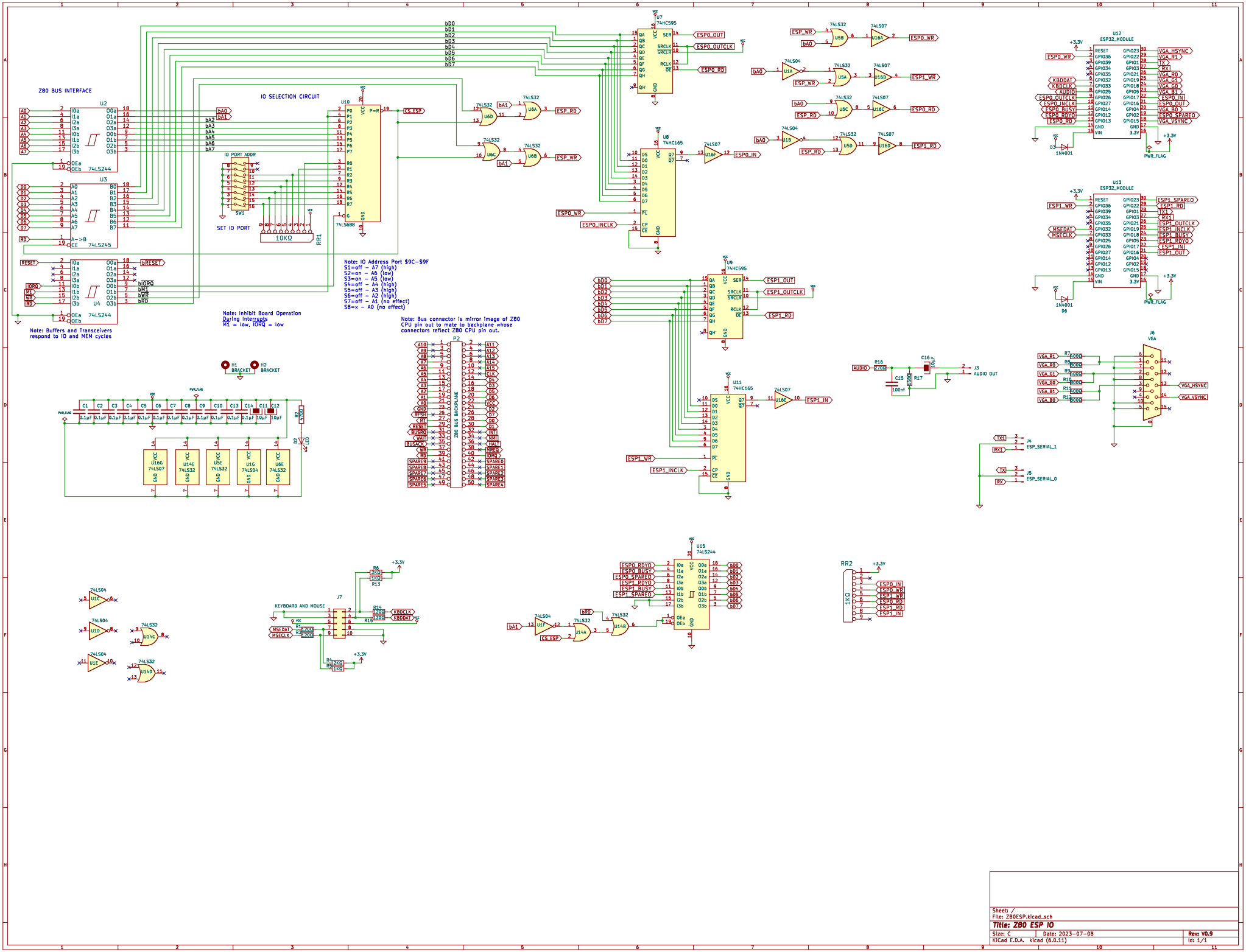
Z80 BUS INTERFACE
IO SELECTION CIRCUIT
SET IO PORT
IO PORT ADDR
10KΩ
Note: Buffers and Transceivers respond to IO and MEM cycles
Note: IO Address Port $9C-$9F
S1=off - A7 (high)
S2=on - A6 (low)
S3=on - A5 (low)
S4=off - A4 (high)
S5=off - A3 (high)
S6=off - A2 (high)
S7=off - A1 (no effect)
S8=x - A0 (no effect)
Note: Inhibit Board Operation During Interrupts
M1 = low, IORQ = low
Note: Bus connector is mirror image of Z80 CPU pin out to mate to backplane whose connectors reflect Z80 CPU pin out.
Z80 BUS BACKPLANE
KEYBOARD AND MOUSE
ESP32_MODULE
VGA
AUDIO OUT
ESP_SERIAL_1
ESP_SERIAL_0
Sheet: /
File: Z80ESP.kicad_sch
Title: Z80 ESP IO
Size: C
Date: 2023-07-08
Rev: V0.9
KiCad E.D.A. kicad (6.0.11)
Id: 1/1