



TRUTH TABLE A: RAM CHIP SELECT
CS_RAM# = 0 MEANS RAM SELECTED

M=MREQ#
B=BOOT#
L=LOW_RAM_EN
5=A15 (0=LOWER 32K PAGE, 1=UPPER 32K PAGE)
X=CS_RAM#

M	B	L	5	X	
0	0	0	0	1	: MEMORY ACCESS IN LOWER MEMORY WHEN LOW_RAM DISABLED & BOOT ACTIVE, DENIED
0	0	0	0	0	: MEMORY ACCESS IN UPPER MEMORY WHEN LOW_RAM DISABLED & BOOT ACTIVE, ENABLED
0	0	0	1	0	: MEMORY ACCESS IN LOWER MEMORY WHEN LOW_RAM ENABLED & BOOT ACTIVE, DENIED
0	0	1	0	0	: MEMORY ACCESS IN UPPER MEMORY WHEN LOW_RAM ENABLED & BOOT ACTIVE, DENIED
0	1	0	0	0	: BOOT NOT ACTIVE OR OVERIDDEN, DENIED
0	1	0	0	1	: BOOT NOT ACTIVE OR OVERIDDEN, DENIED
0	1	0	1	0	: BOOT NOT ACTIVE OR OVERIDDEN, DENIED
0	1	0	1	1	: BOOT NOT ACTIVE OR OVERIDDEN, DENIED
1	0	0	0	0	: NOT MEMORY ACCESS, DENIED
1	0	0	0	1	: NOT MEMORY ACCESS, DENIED
1	0	0	1	0	: NOT MEMORY ACCESS, DENIED
1	0	0	1	1	: NOT MEMORY ACCESS, DENIED
1	0	1	0	0	: NOT MEMORY ACCESS, DENIED
1	0	1	0	1	: NOT MEMORY ACCESS, DENIED
1	0	1	1	0	: NOT MEMORY ACCESS, DENIED
1	0	1	1	1	: NOT MEMORY ACCESS, DENIED

TRUTH TABLE B&C: DETERMINE WHICH RAM CHIP SELECTED

C=CS_RAM#
R=RAMSIZE (SELECT FIRST OR SECOND RAM CHIP)
5=A15 (0=LOWER 32K PAGE, 1=UPPER 32K PAGE)
R0=CS_RAMO#
R1=CS_RAM1#

C	R	5	R0	R1	
0	0	0	0	1	: RAM ACCESS, LOWER MEMORY, ENABLE FIRST CHIP
0	0	0	0	0	: RAM ACCESS, UPPER MEMORY, ENABLE FIRST CHIP
0	0	1	0	0	: RAM ACCESS, LOWER MEMORY, ENABLE SECOND CHIP
0	0	1	0	1	: RAM ACCESS, UPPER MEMORY, ENABLE SECOND CHIP
1	0	0	0	0	: NOT RAM ACCESS, DENIED
1	0	0	0	1	: NOT RAM ACCESS, DENIED
1	0	1	0	0	: NOT RAM ACCESS, DENIED
1	0	1	0	1	: NOT RAM ACCESS, DENIED

NOTE: UPPER RAM ACCESS (8000-FFFF) ALWAYS GOES TO SAME PAGE ON SAME CHIP NO MATTER WHAT