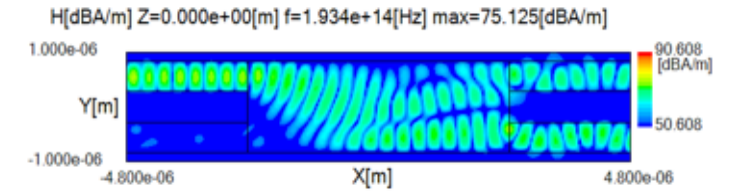
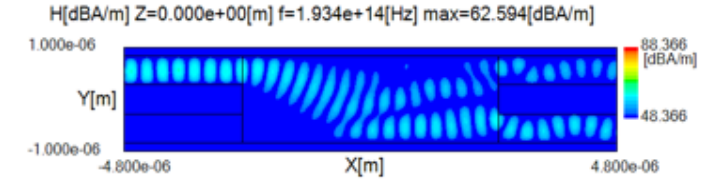
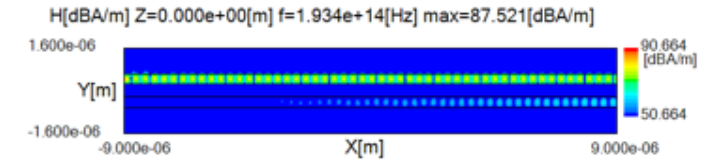
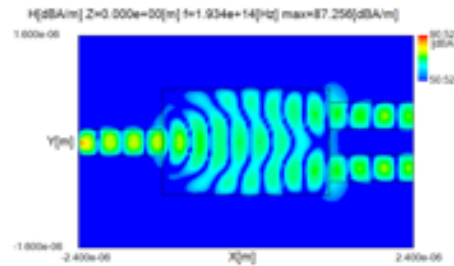
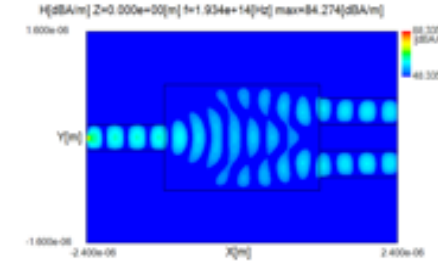
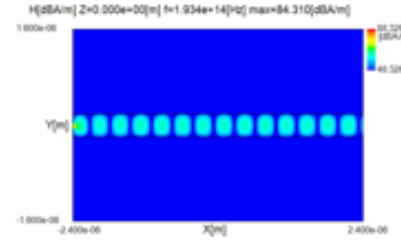


Objective

- 学習に使用する画像を収集

Key findings

コア径
400nm幅×220nm厚さ



Approach

- openFDTDでシリコン導波路の作成

Next action

- SRCNN
- openFDTD