



017 - CI SN74HC00

Portas lógicas NAND. Sinais lógicos em portas TTL. Existe realmente zero e um ?

TABELAS VERDADE

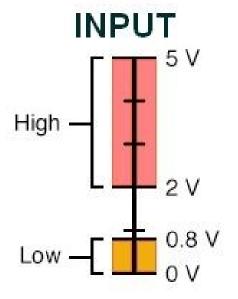


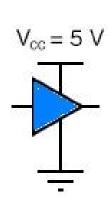
A	В	AND NAND	
0	0	0	1
0	1	0	1
1	0	0	1
1	1	1	0

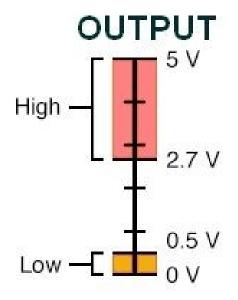




NÍVEIS DE SINAIS EM PORTAS TTL











VrPino	VResistor	2.5	1	'	,
50	3,04	3,5			
100	2,7			_	
127	2,37	3		Pre	visto
128	2.32				
150	2,05	2,5			
200		2,0			
250	0,13				
		2	lue		_
			U		
		1,5			
		2,0	4	_	
		_			
		1			
		0,5			
		0 +	F0	100	150
		0	50	100	150

Circuito esquemático – Porta NAND



