

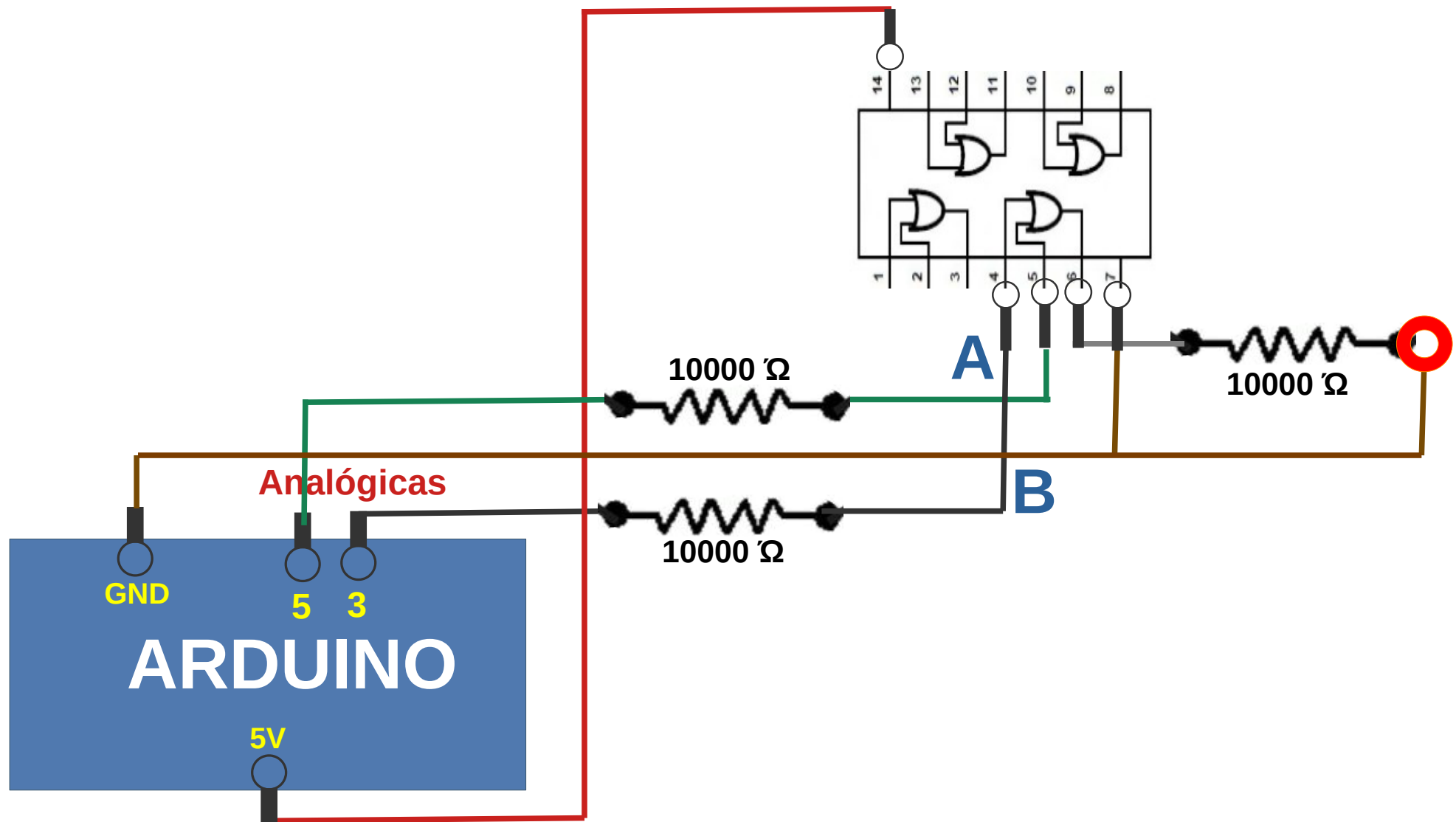
# 018 – CI SN74HC32

**Portas lógicas OR.**

**Sinais lógicos em portas TTL.**

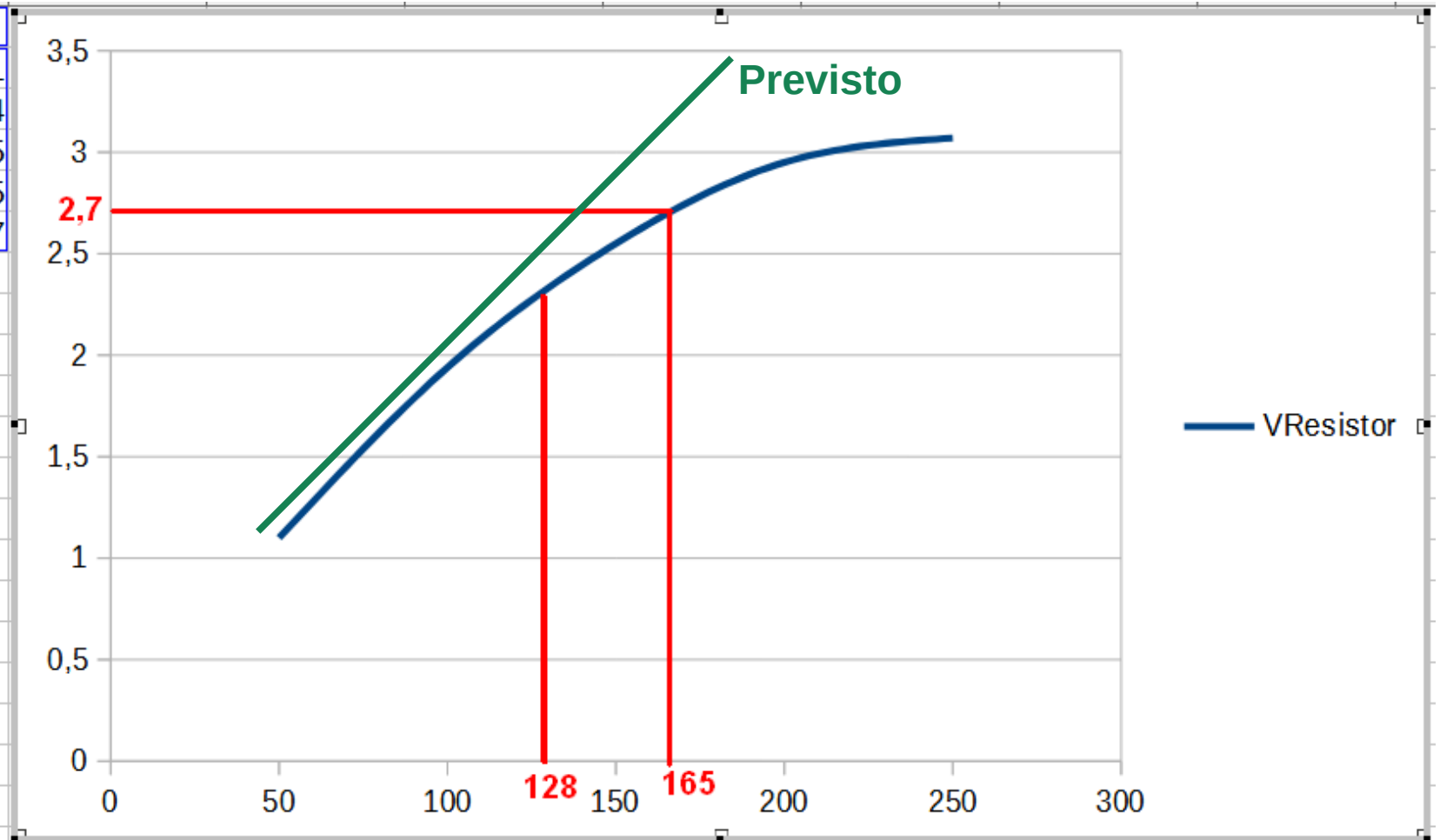
**Valores de zero e um ?**

# Circuito esquemático – Porta OR





VrPino	VResistor
50	1,1
100	1,94
150	2,55
200	2,95
250	3,07





## NÍVEIS DE SINAIS EM PORTAS TTL

