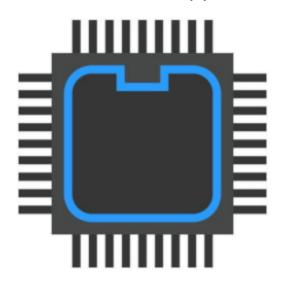
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U	11	1
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Memória Principal

0000	0
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0010	1
0011	ı
0100	•
0101	2
0110	_
0111	3
1000	4
1001	4
1010	5
1011	
1100	6
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1110	7
1111	7

Processador 11|0|0

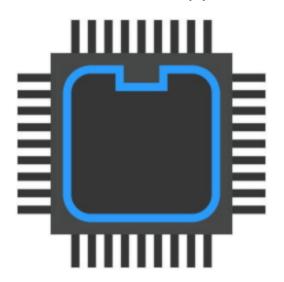


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Memória Principal

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0010	1
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0100	2
0101	2
0110	3
0111	3
1000	4
1001	4
1010	5
1011)
1100	6
1101	6
1110	7
1111	'

Processador 11|1|1

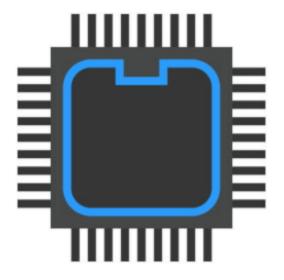


Bloco	ID	Palavra
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U	11	1
1	10	0
•		1

Memória Principal

0000	0
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0100	2
0101	4
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0111	3
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1010	- 5
1011	
1100	6
1101	
1110	7
1111] '

Processador 10|1|0

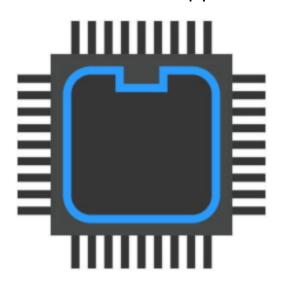


Bloco	ID	Palavra
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U	00	1
1	10	0
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Memória Principal

0000	0
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0010	1
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0100	2
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1000	4
1001	4
1010	5
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1100	6
1101	6
1110	7
1111	'

Processador 00|0|1

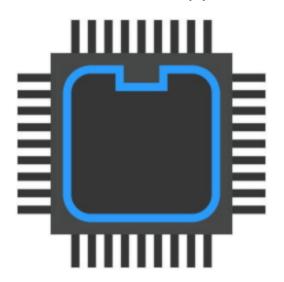


Bloco	ID	Palavra
0	00	0
U	00	1
1	00	0
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Memória Principal

0000	0
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0010	4
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0100	2
0101	2
0110	_
0111	3
1000	4
1001	4
1010	5
1011	5
1100	6
1101	6
1110	7
1111	7

Processador 00|1|0

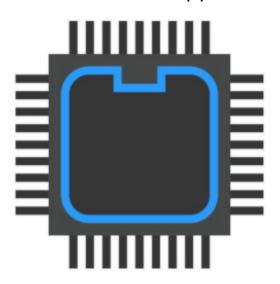


Bloco	ID	Palavra
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U	00	1
1	01	0
ı		1

Memória Principal

0000	0
0001	
0010	4
0011	I
0100	2
0101	2
0110	_
0111	3
1000	4
1001	4
1010	5
1011	5
1100	6
1101	6
1110	7
1111	7

Processador 01|1|0



Memória Cache

Bloco	ID	Palavra
0	11	0
		1
1	01	0
		1

Memória Principal

0000	0	
0001	U	
0010	1	
0011		
0100	2	
0101		
0110	3	
0111		
1000	4	
1001		
1010	5	
1011		
1100	6	
1101	O	
1110	7	
1111		

Processador 11|0|1

