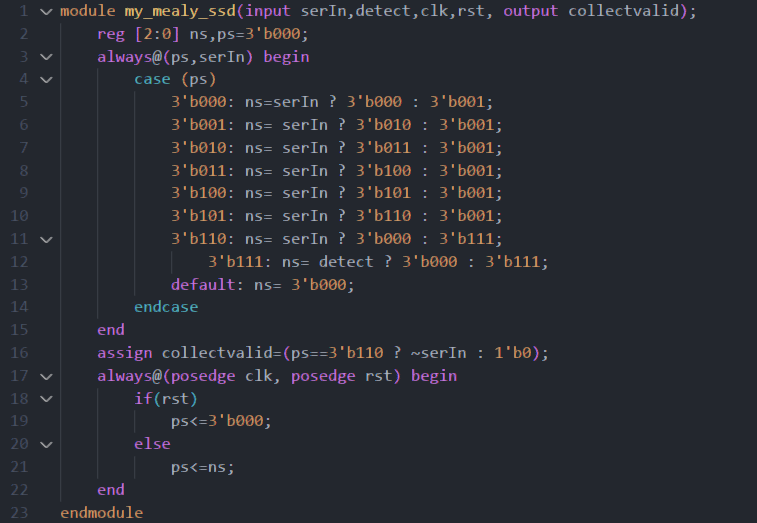
Report for computer assignment 5

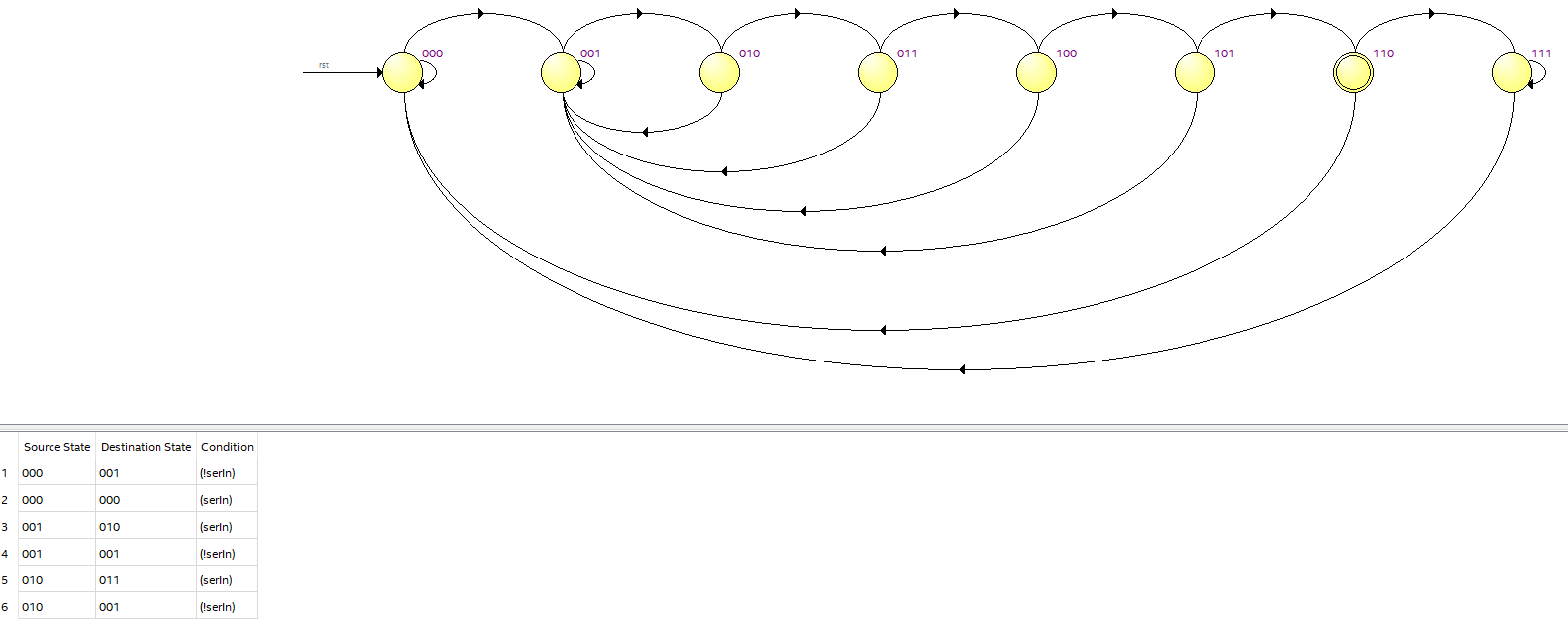
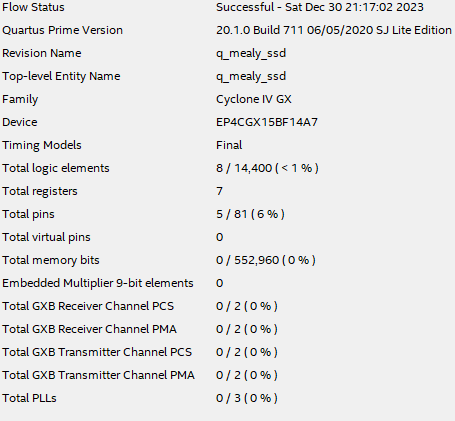
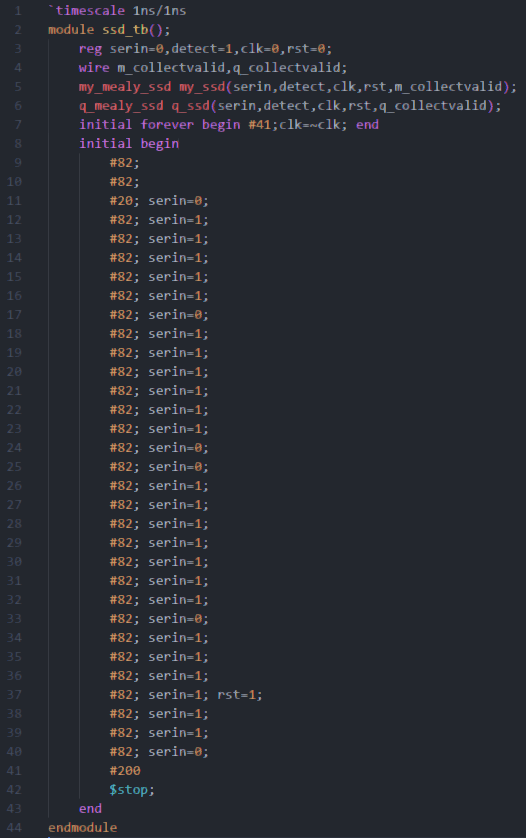
Babak Hosseini Mohtasham

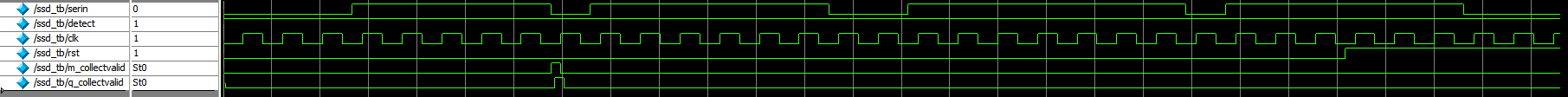
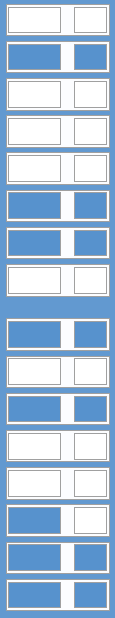
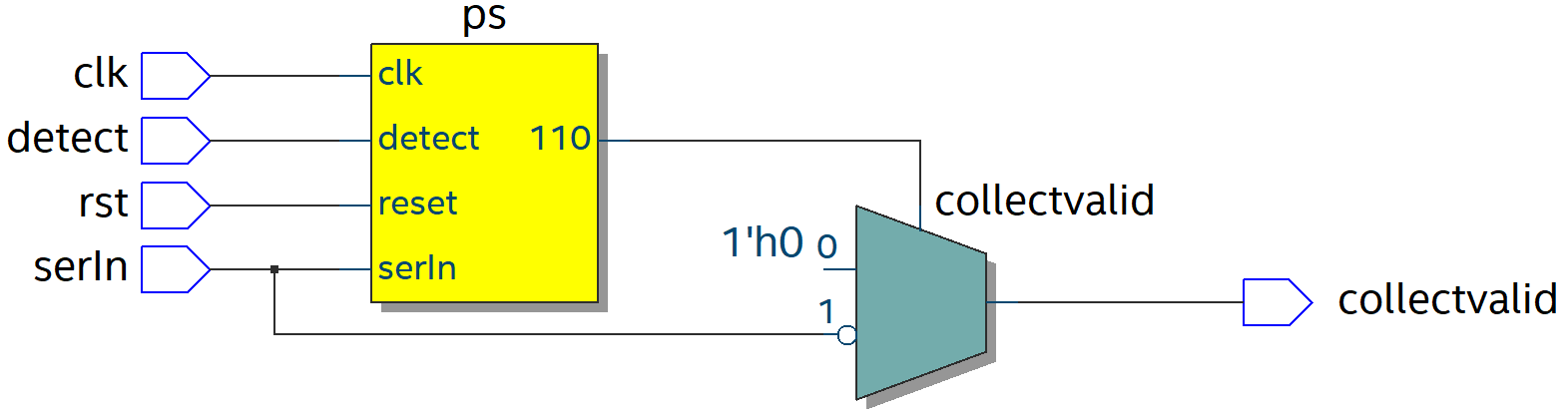
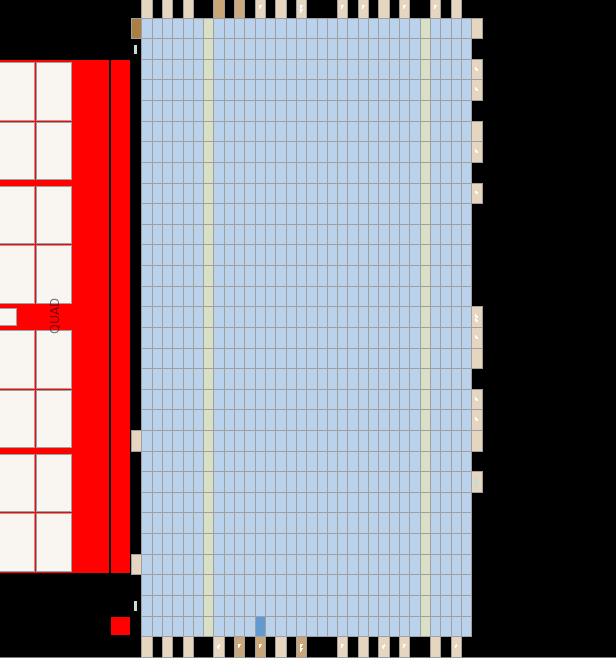
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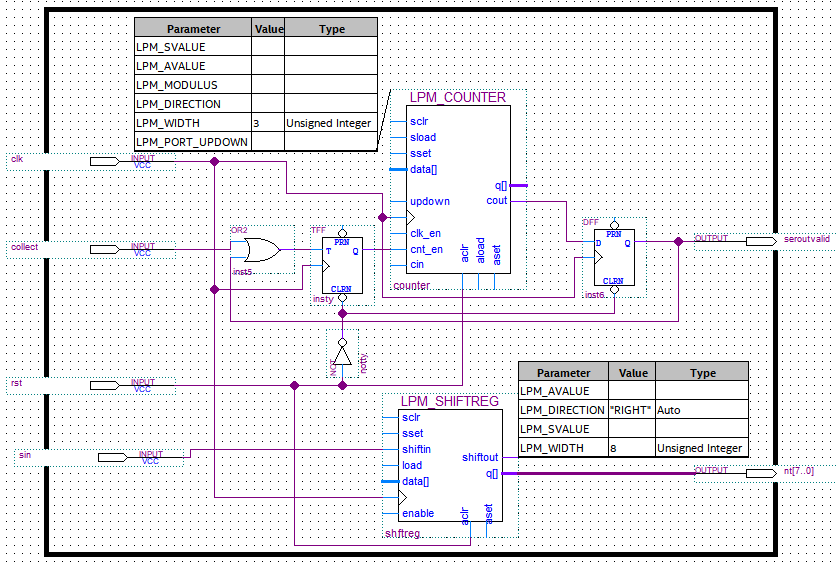
The Start Sequence Detector component:

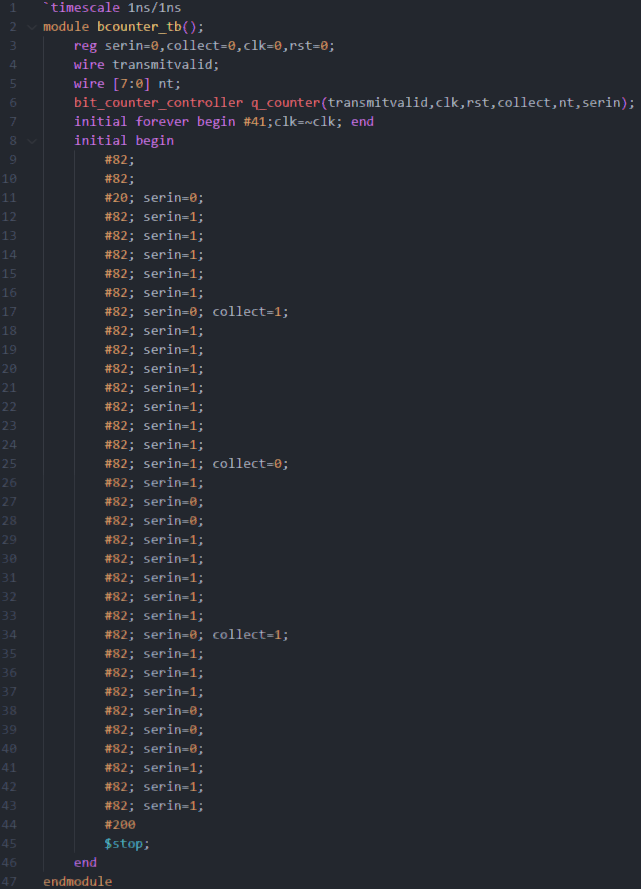
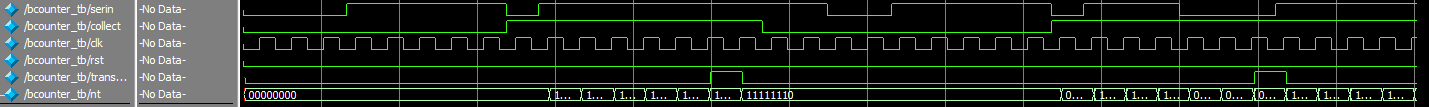
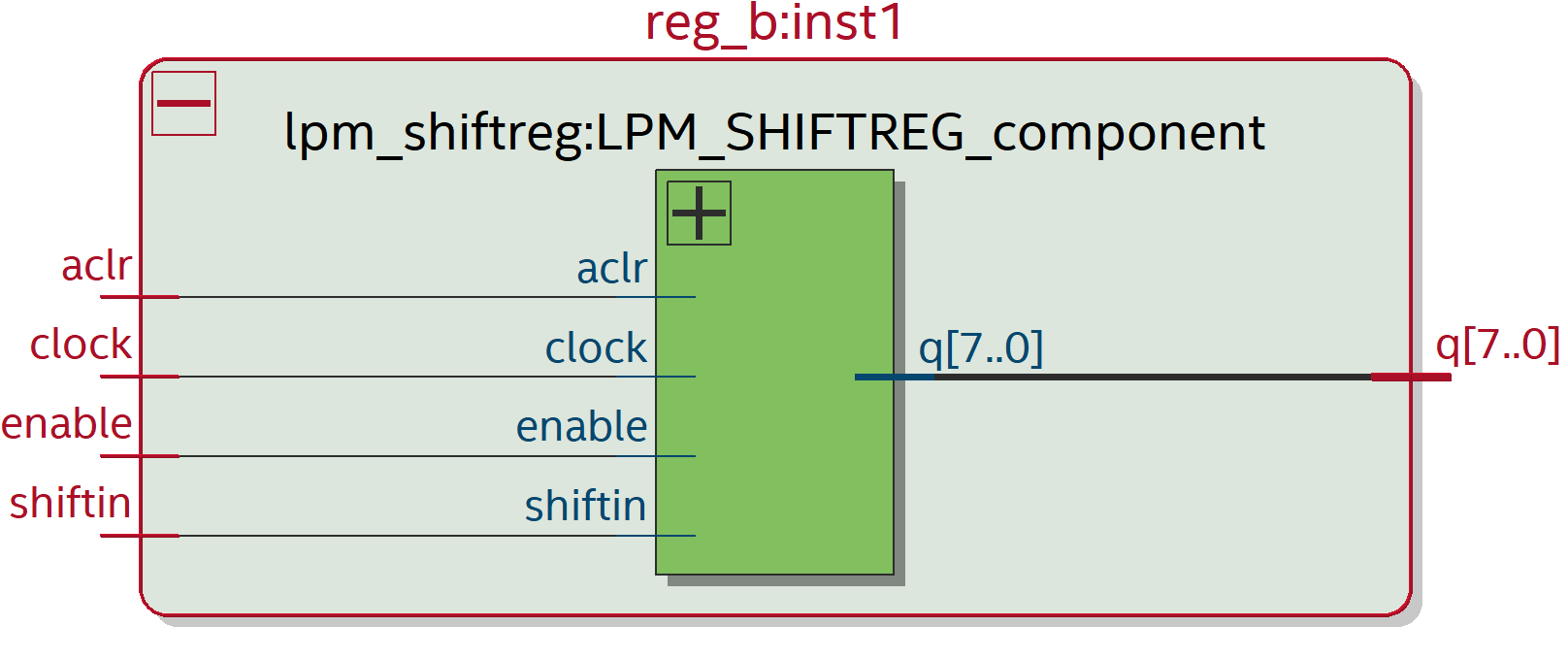
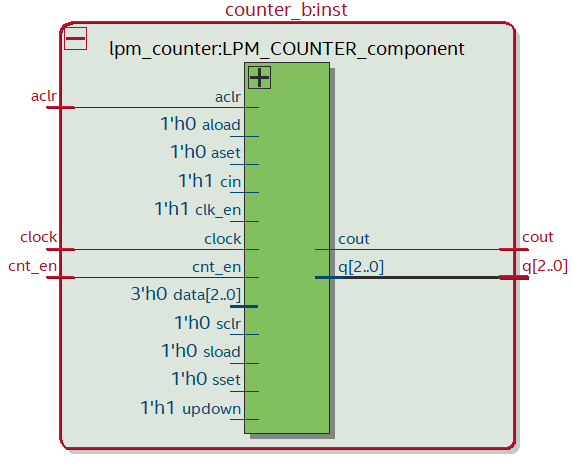
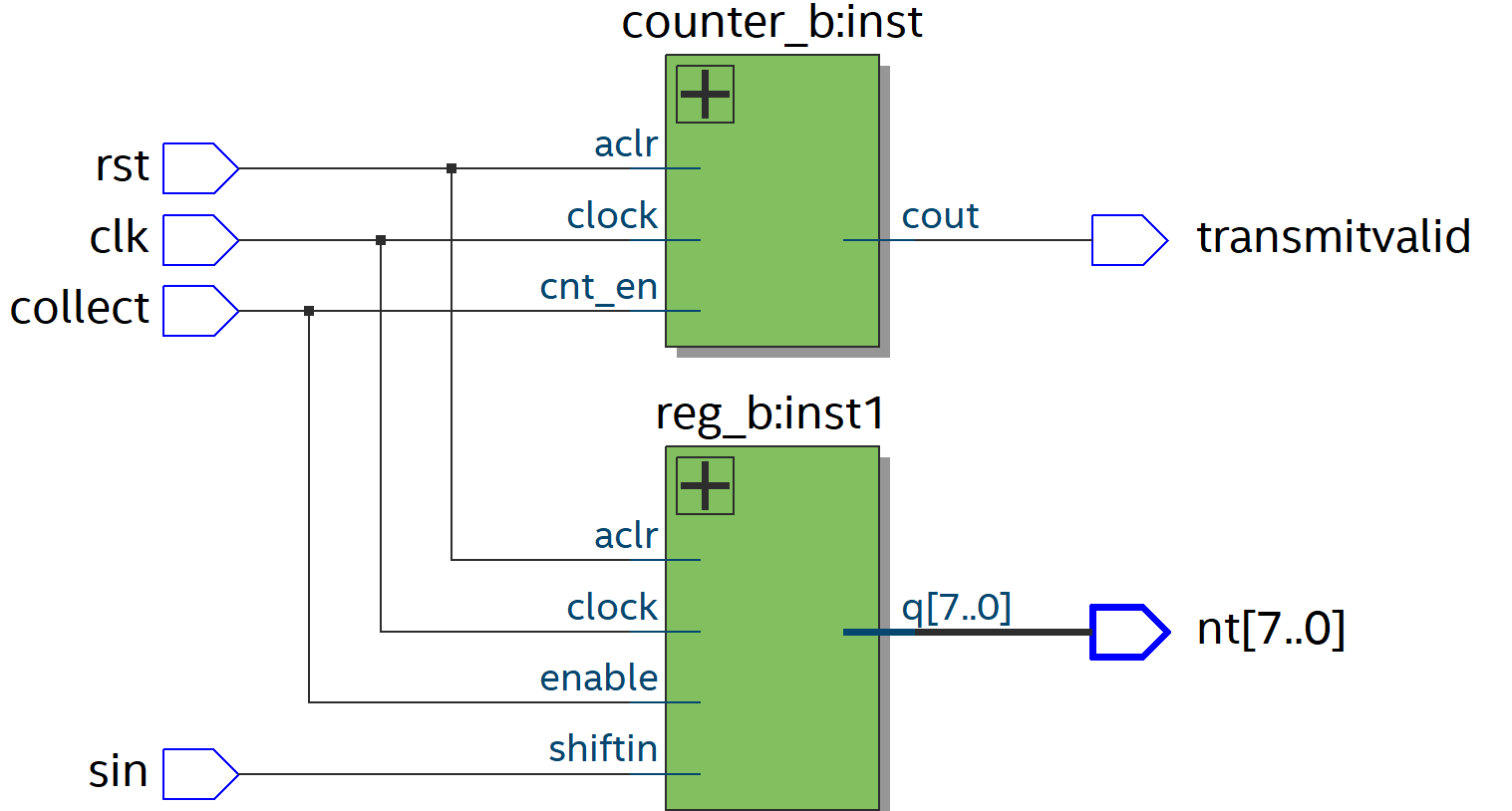
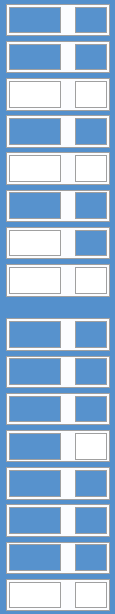
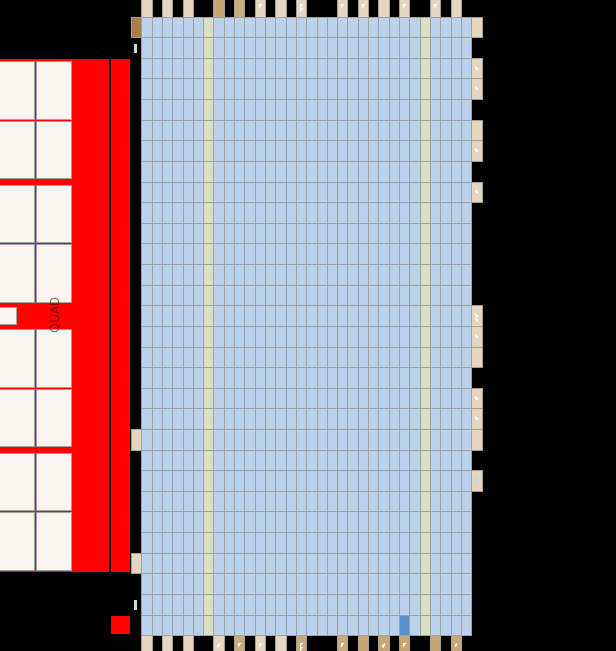
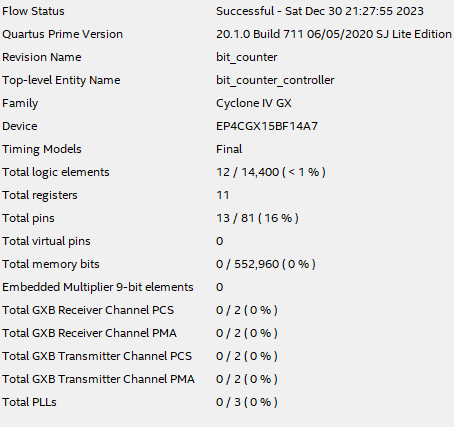
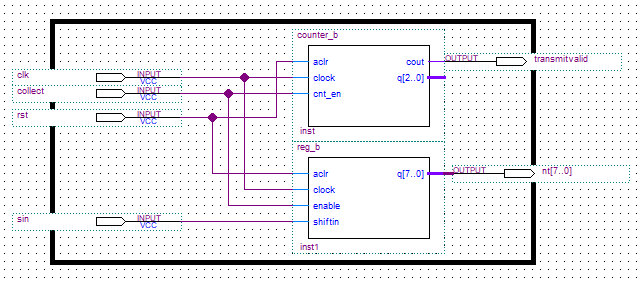




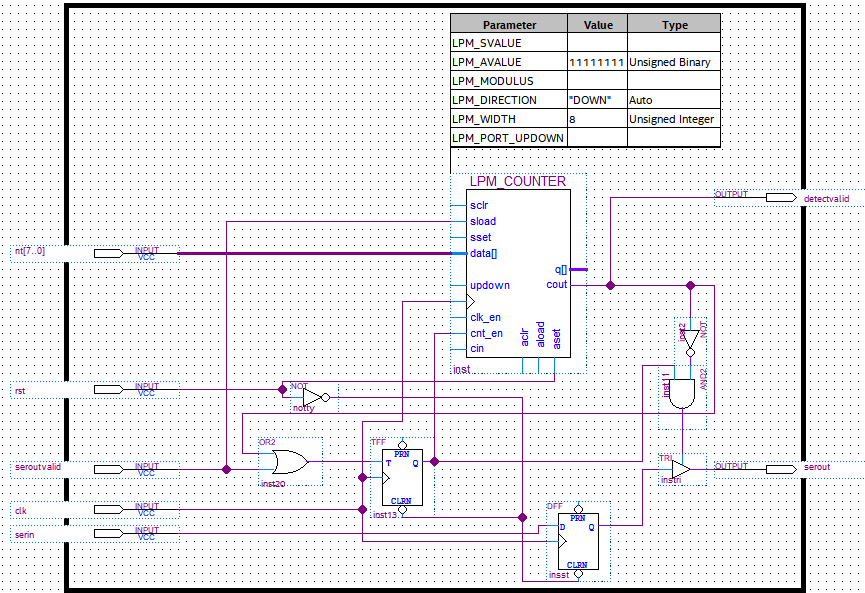


First attempt for the Bit Collector (without controller):

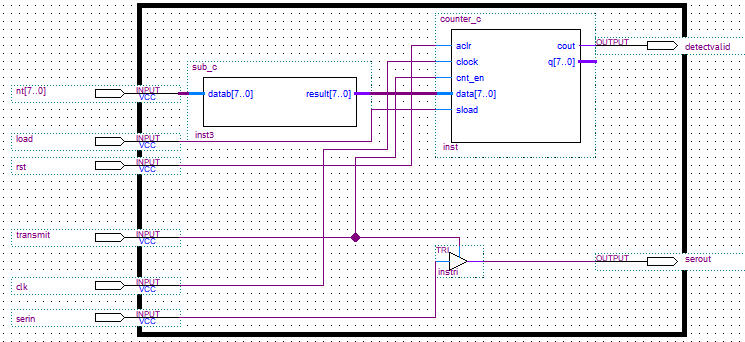


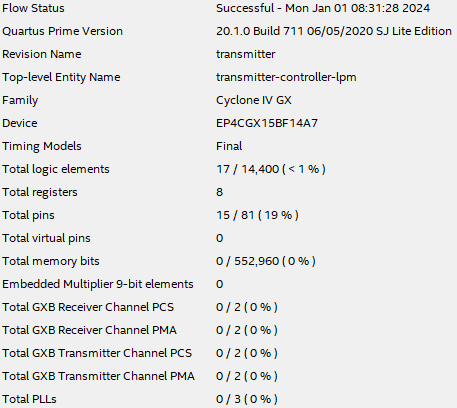
Second attempt for the Bit Collector (with controller):

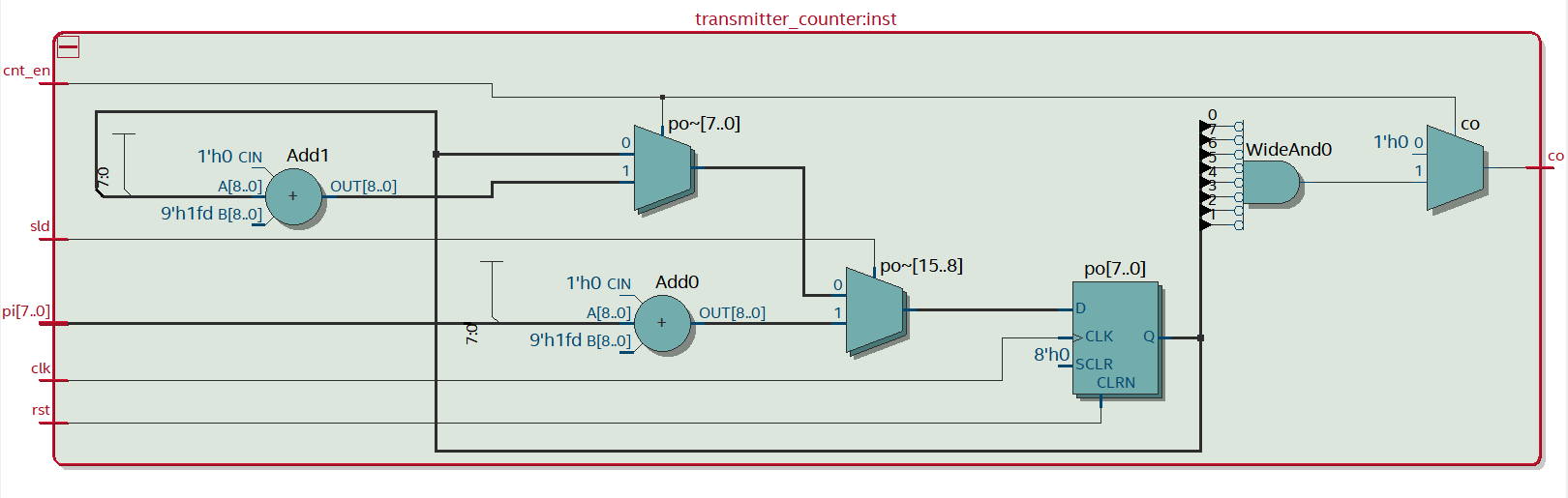
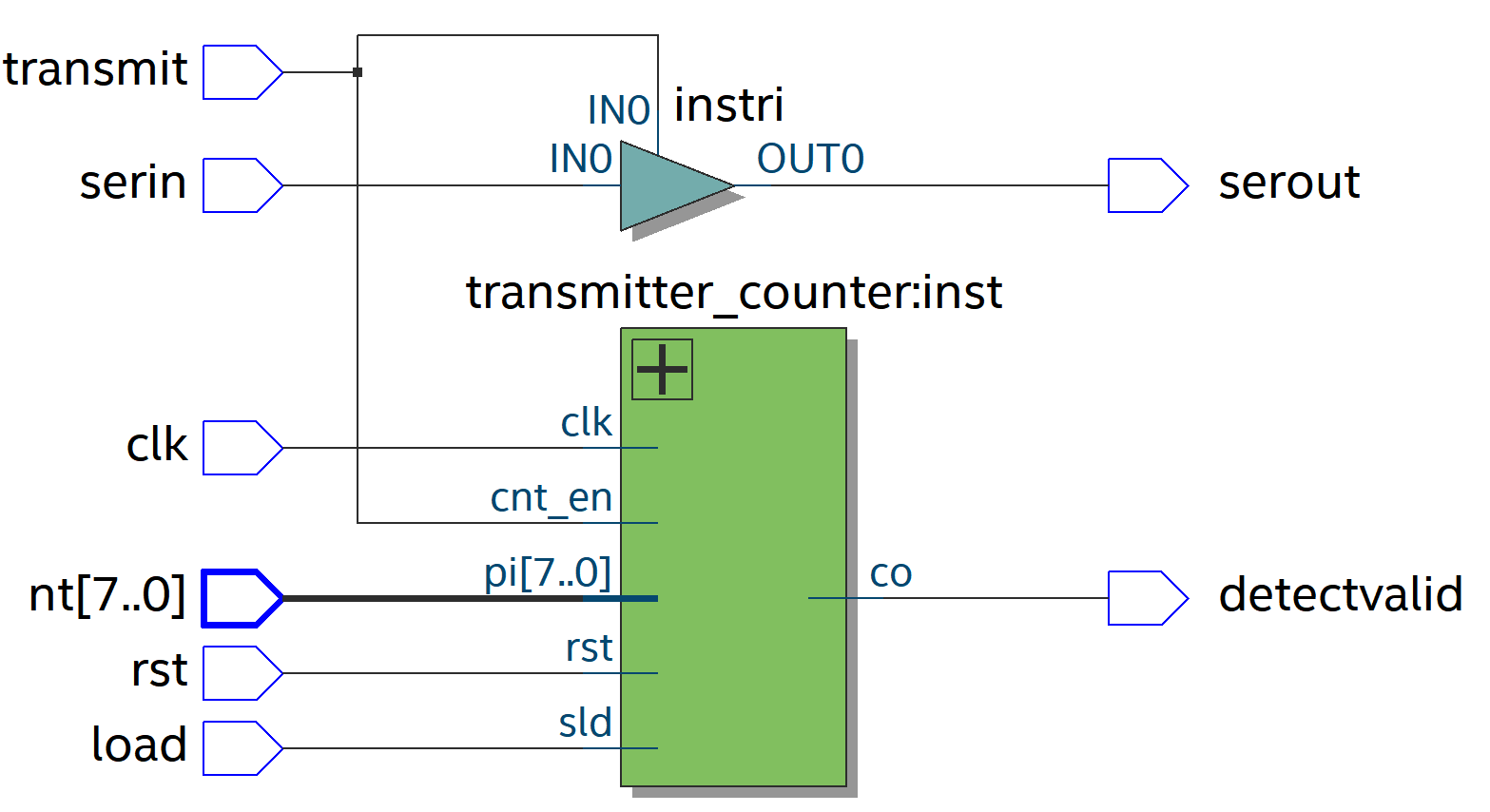
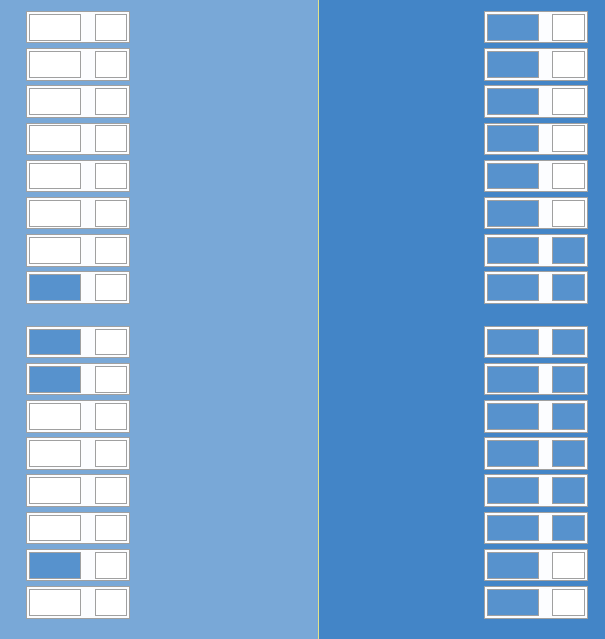
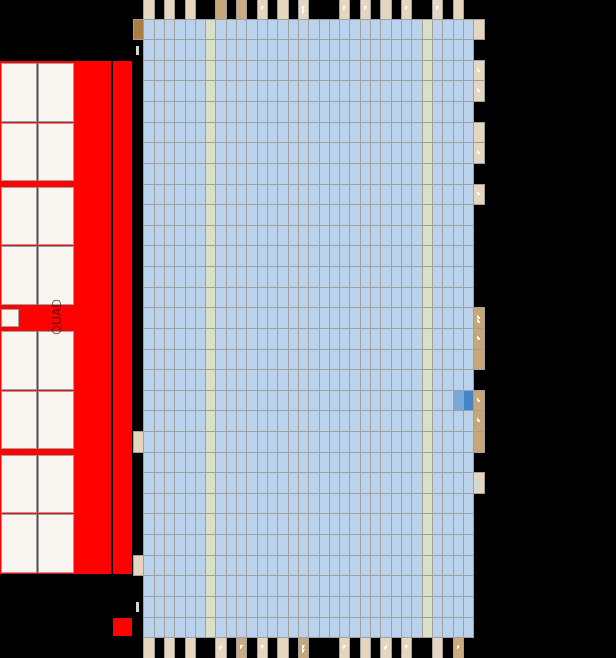
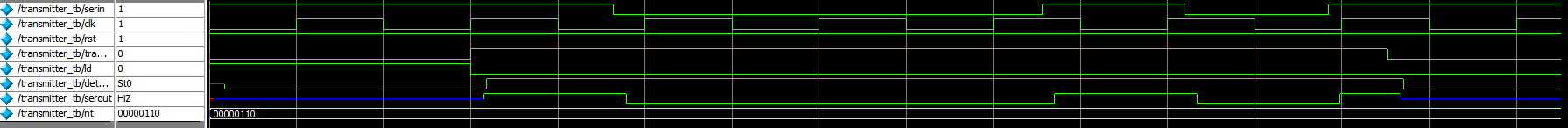
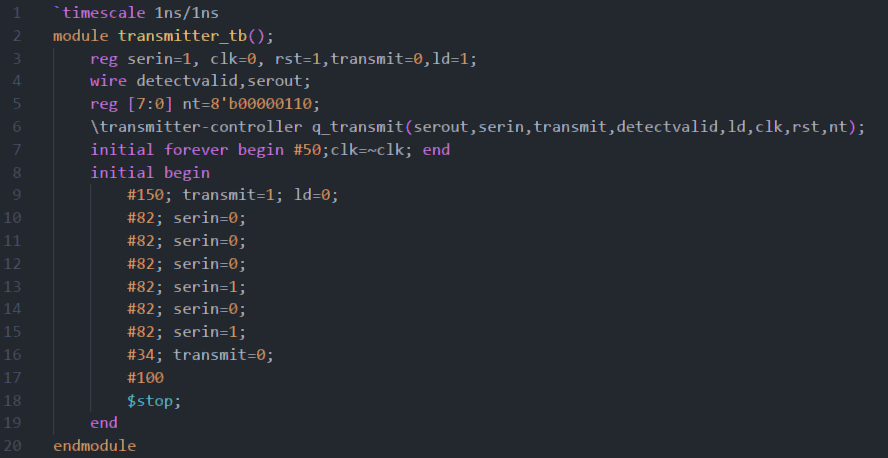
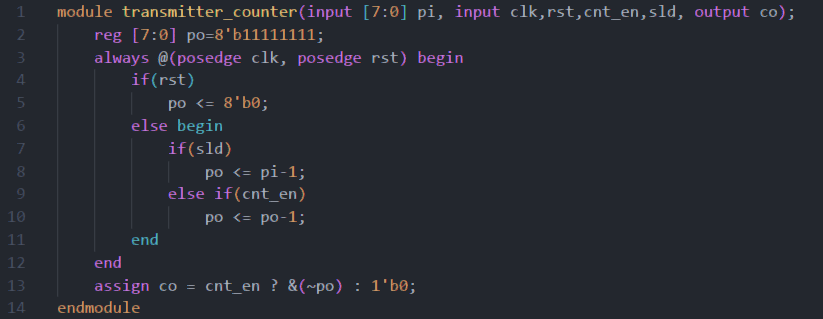
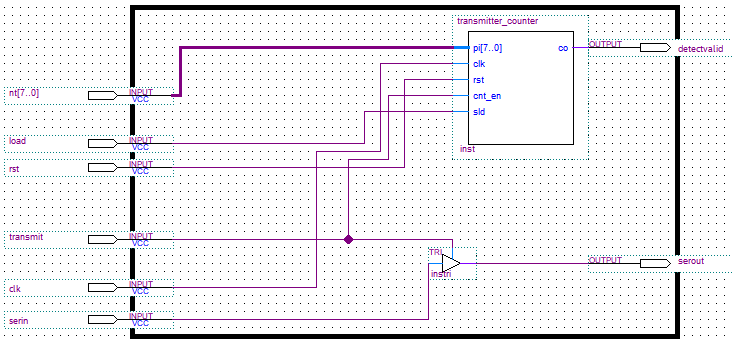
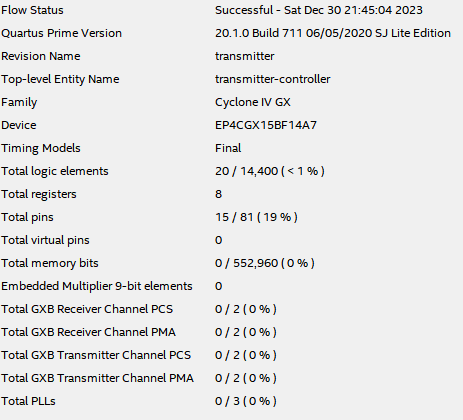
First attempt for the Transmitter (without controller):



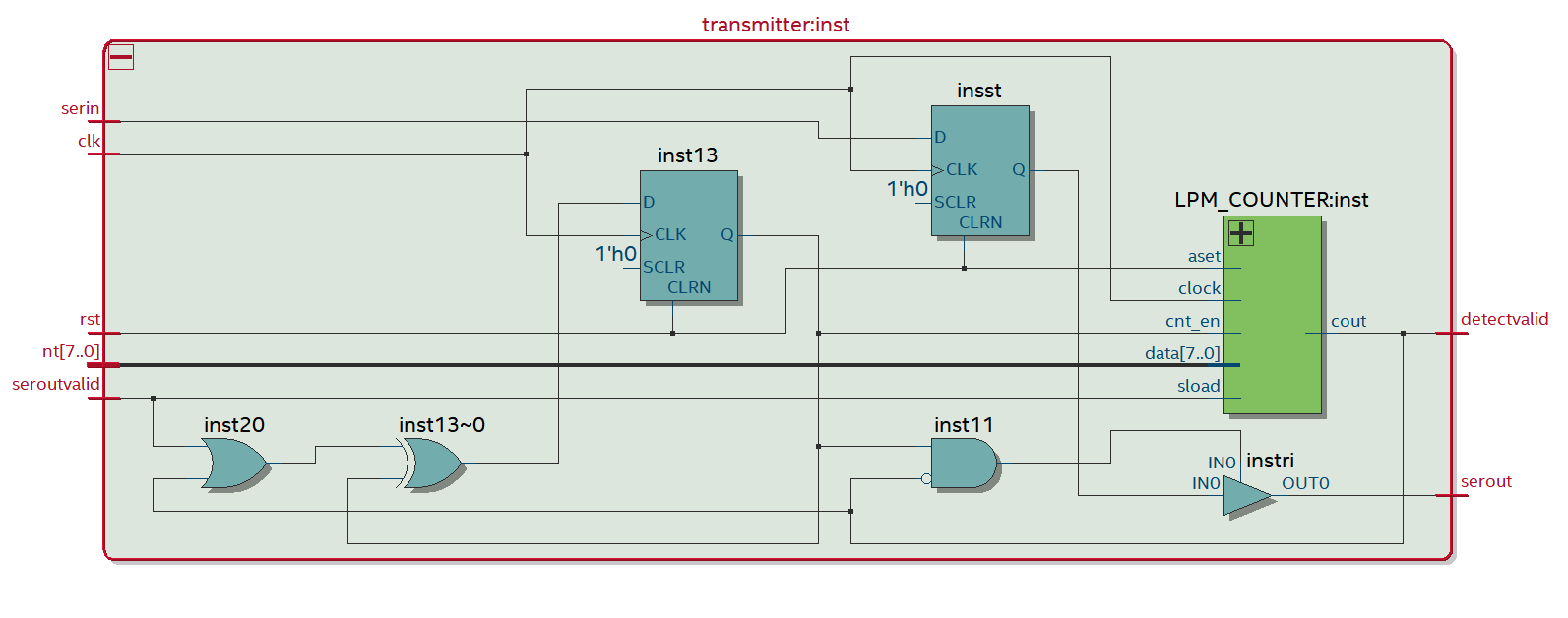
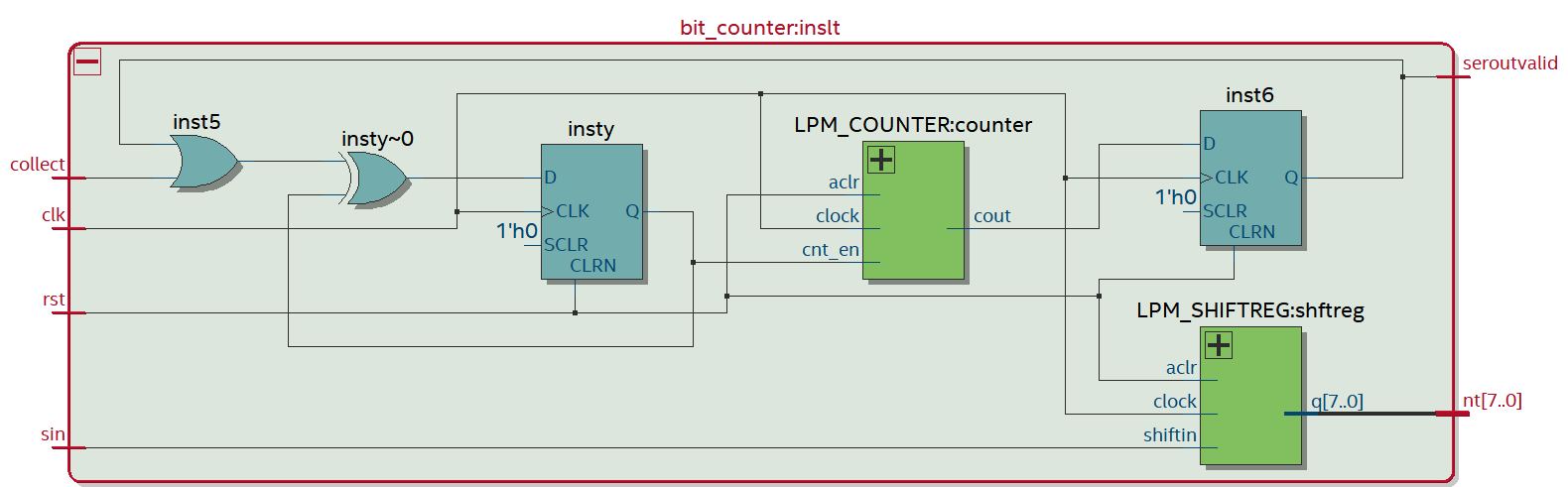
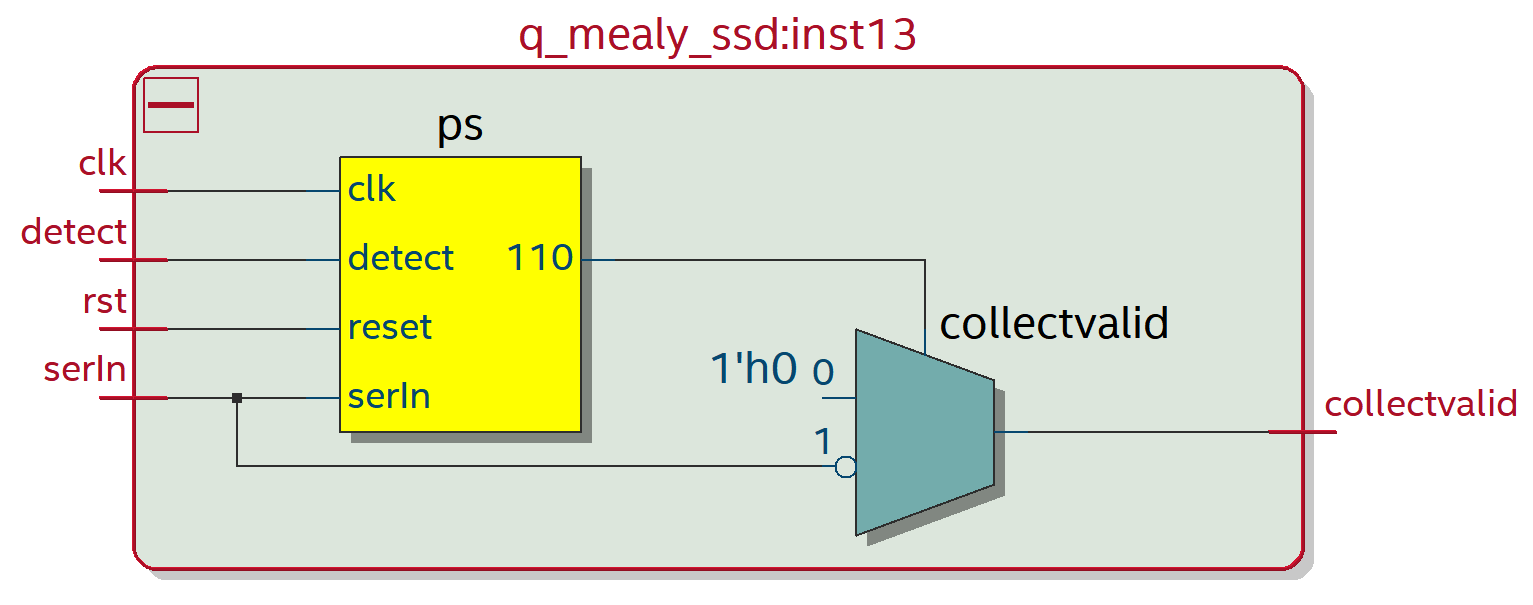
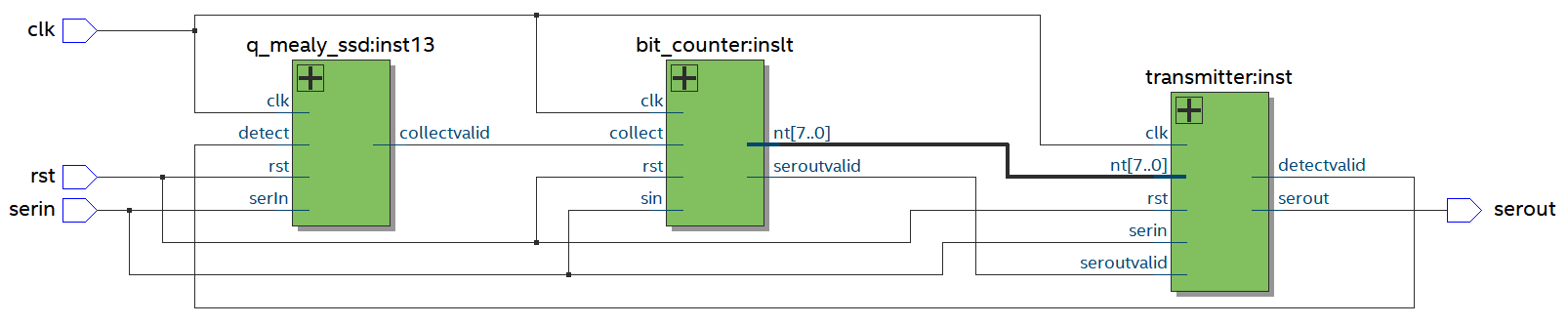
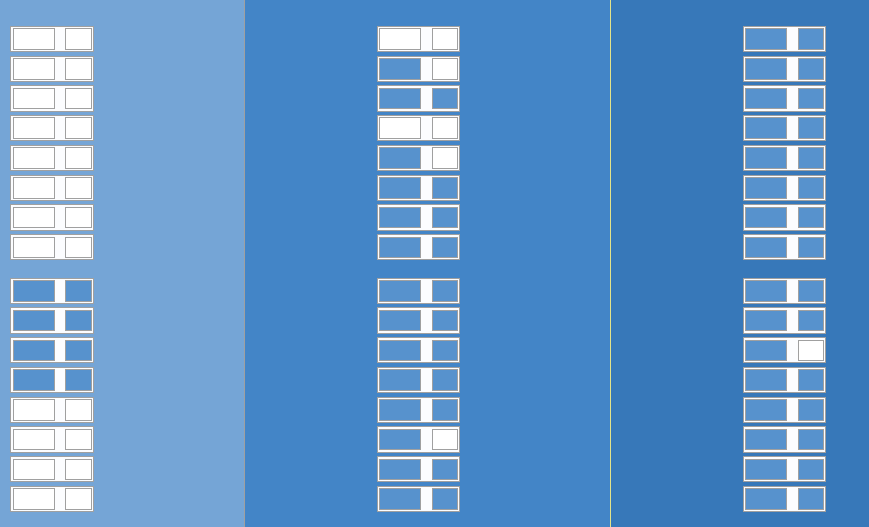
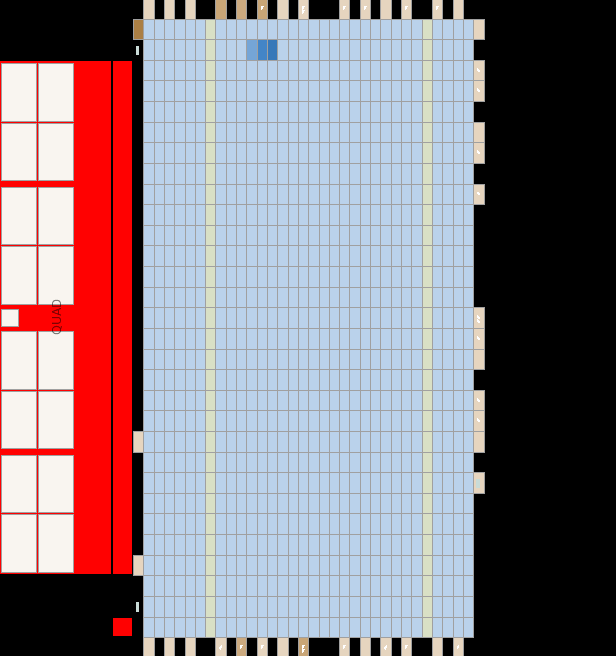
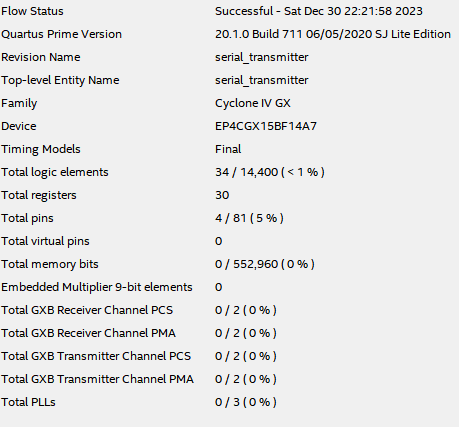
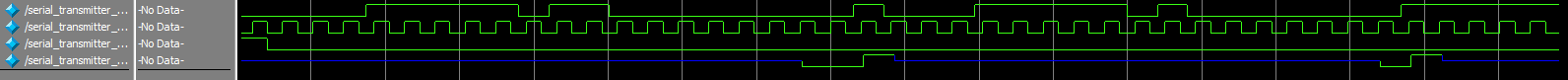
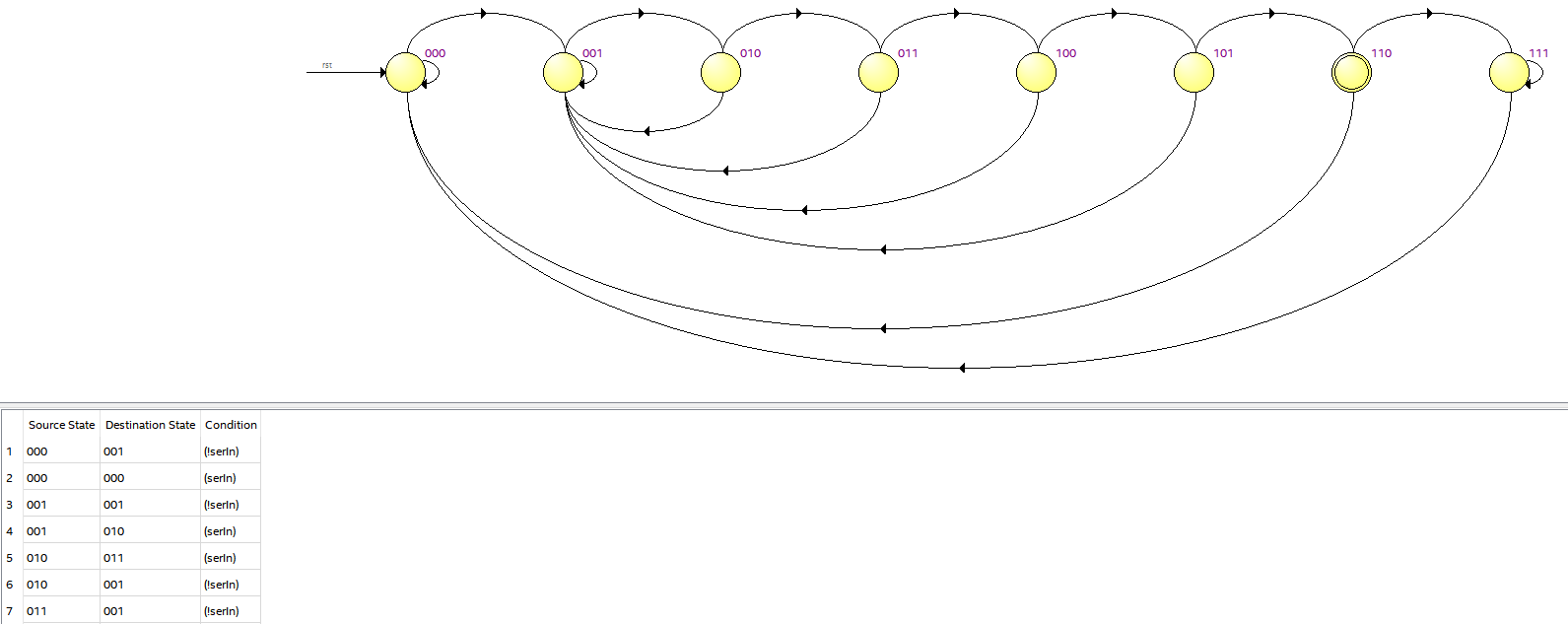
Second attempt for the Transmitter (with controller and lpm):





Third attempt for the Transmitter (with controller and my code): 

First attempt for the Serial Transmitter (without controller):



Second attempt for the Transmitter (with controller):

