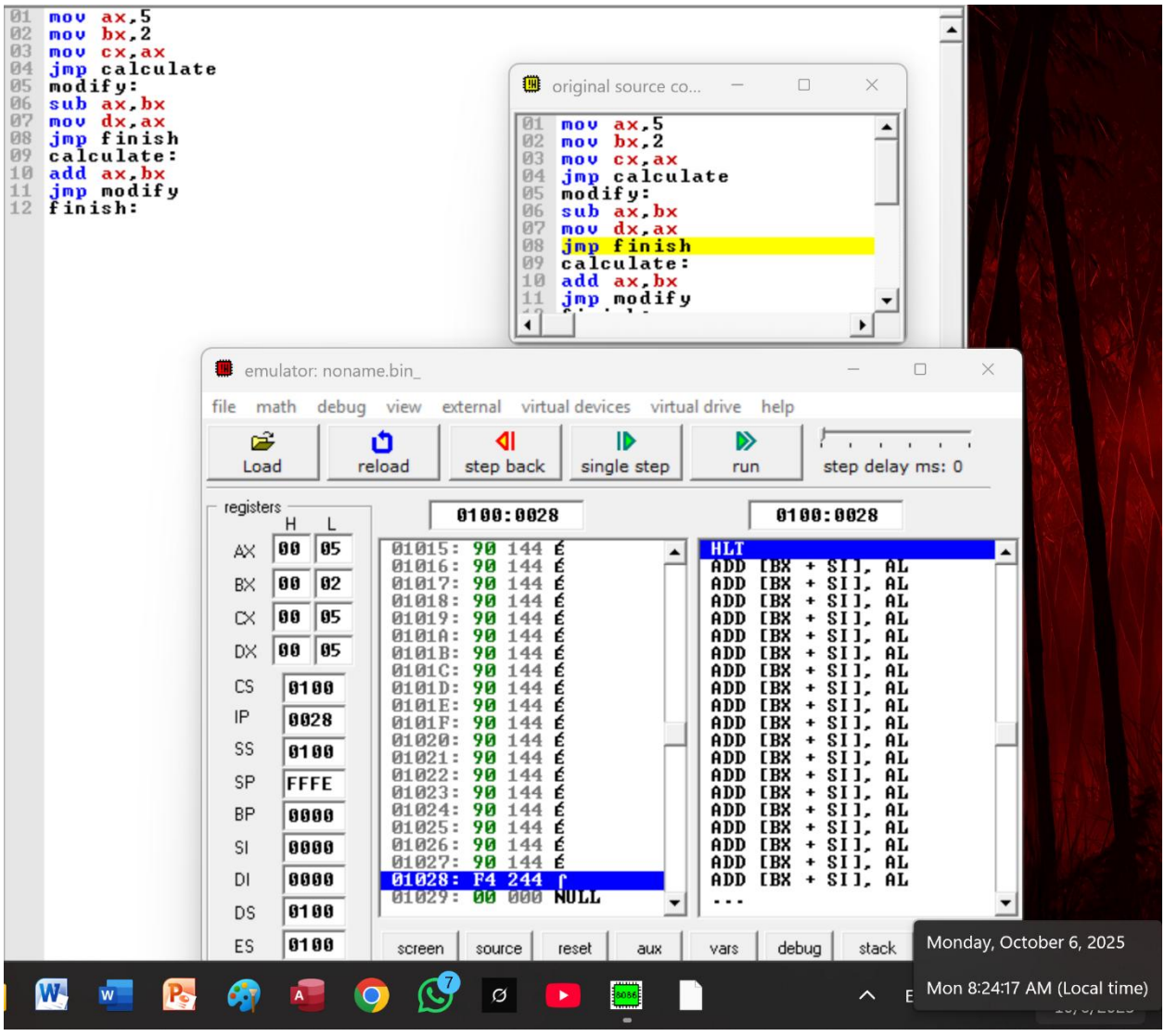
NAME: Babar Ali

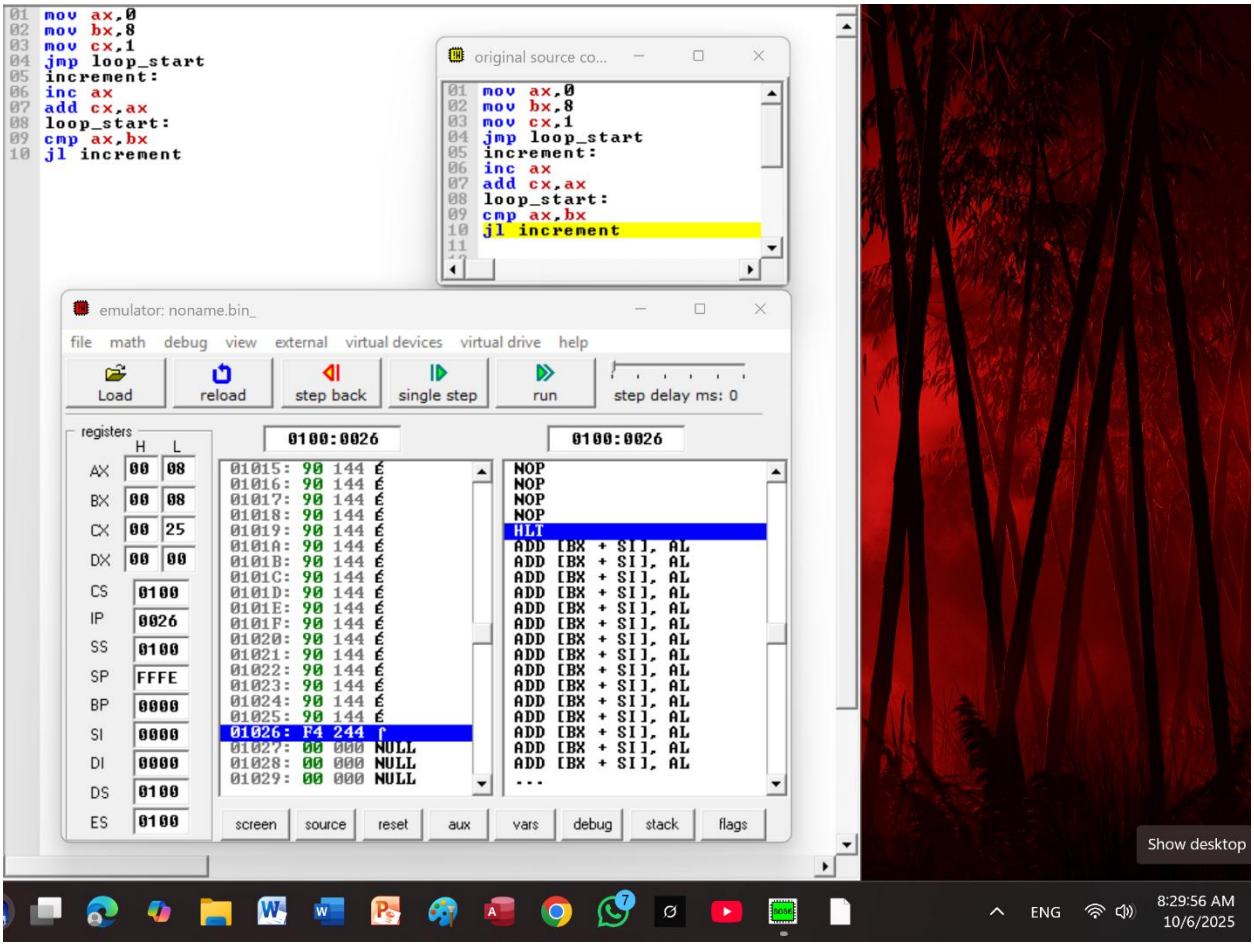
SAP ID: 65731

SUBJECT: COAL

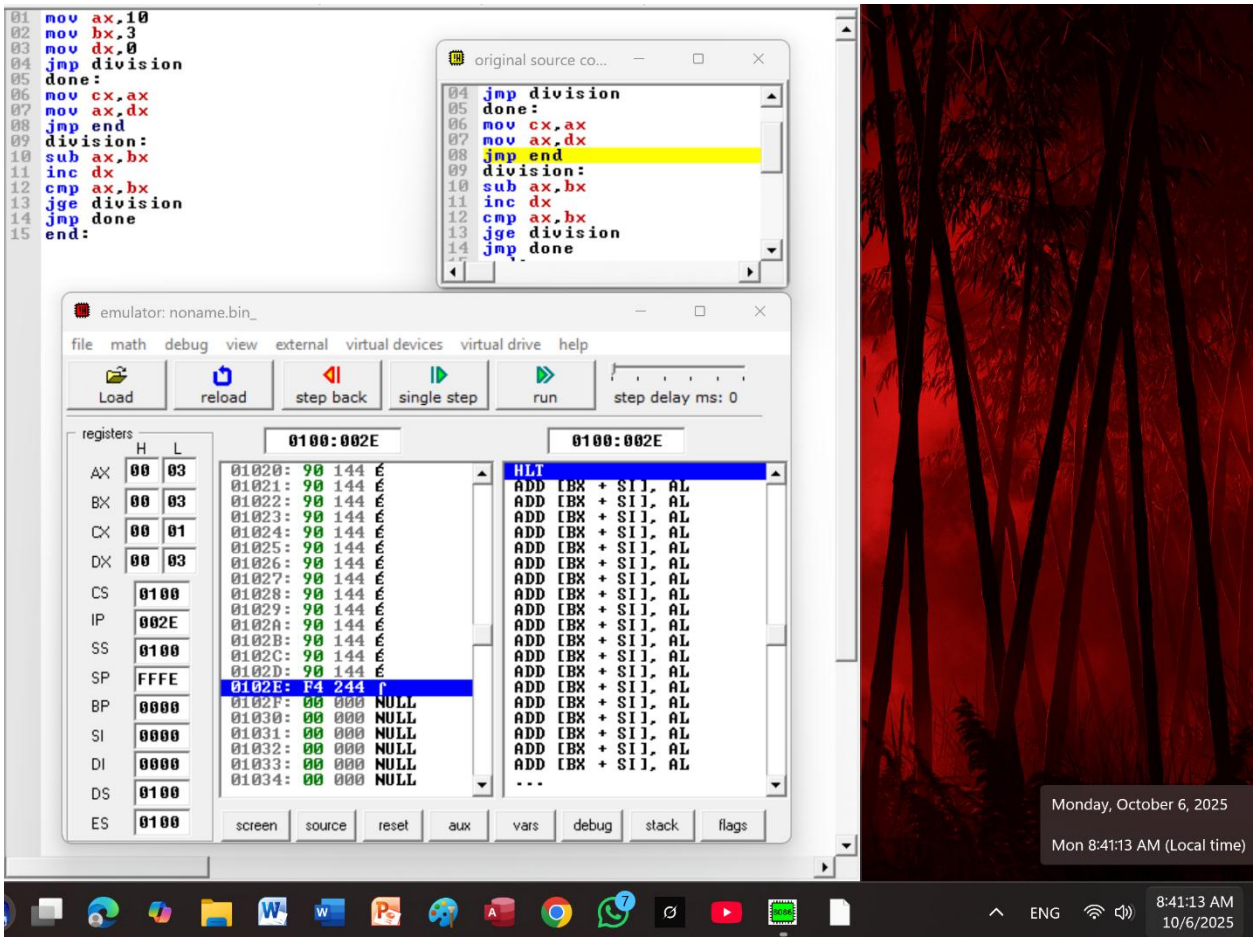
LAB7-TASK1:



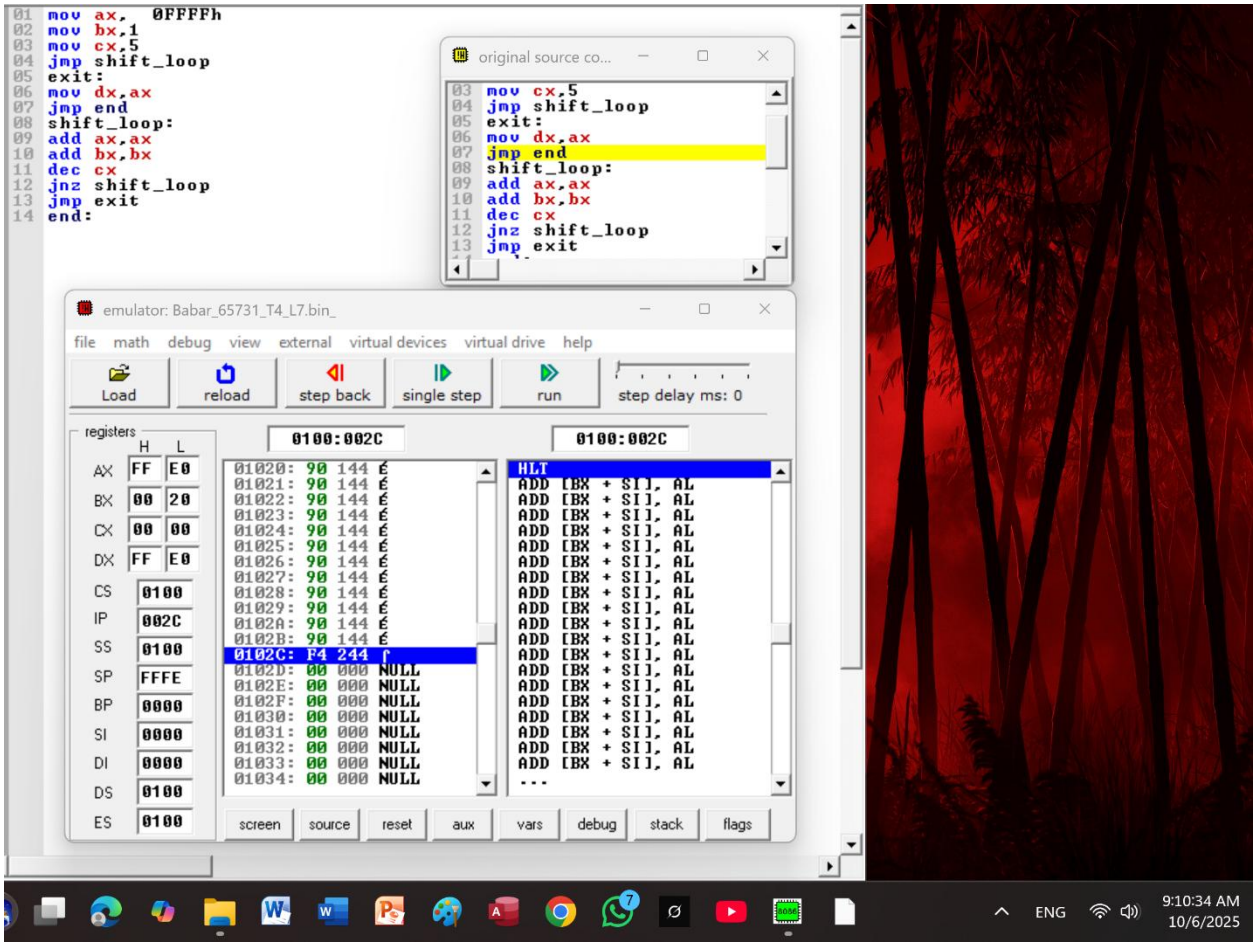
TASK2:


mov ax,0
mov bx,8
mov cx,1
jmp loop_start
increment:
inc ax
add cx,ax
loop_start:
cmp ax,bx
jl increment
original source co...
emulator: noname.bin_
file math debug view external virtual devices virtual drive help
Load
reload
step back
single step
run
step delay ms: 0
registers
0100:0026
0100:0026
screen source reset aux vars debug stack flags
Show desktop
ENG
8:29:56 AM
10/6/2025

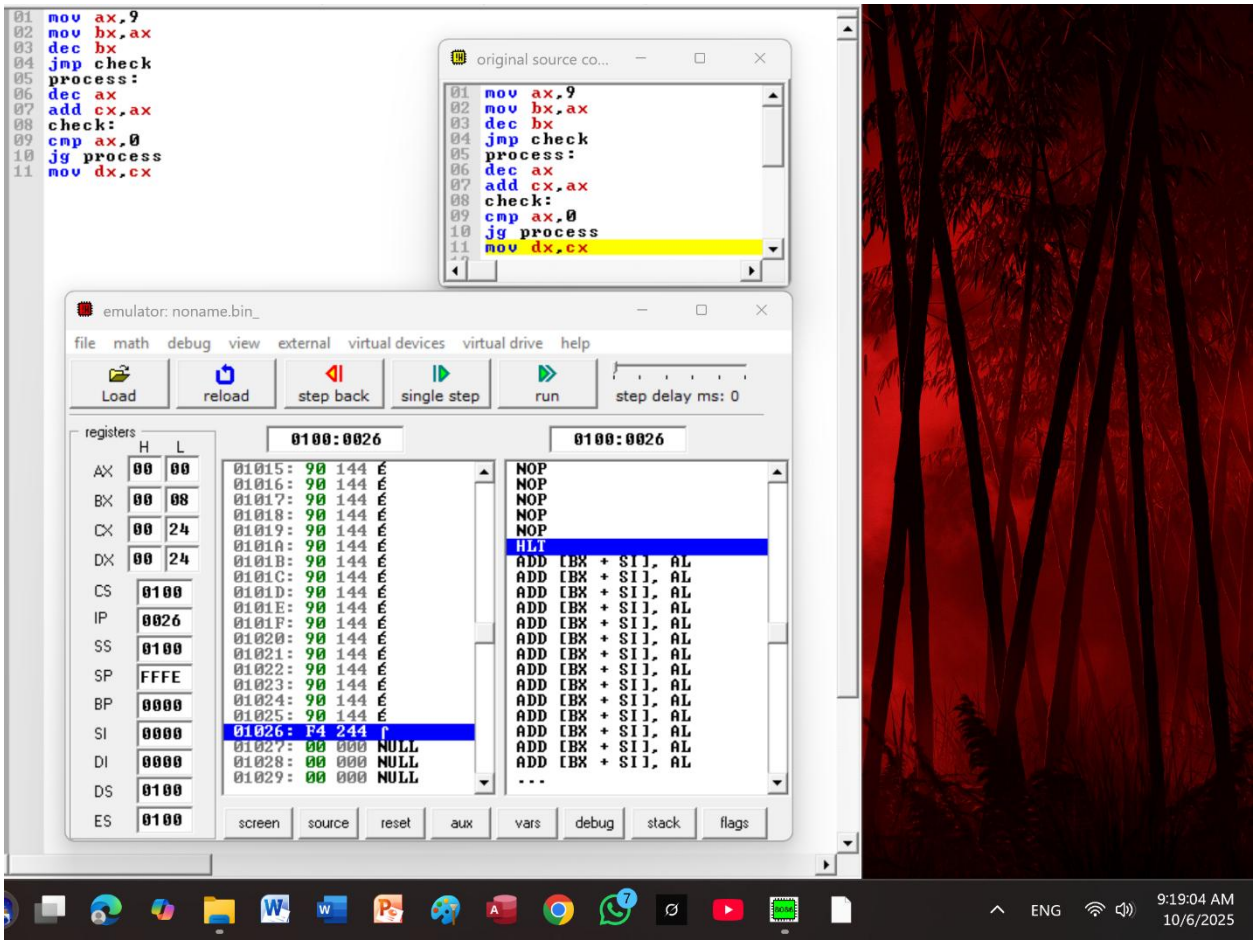
TASK3:


mov ax,10
mov bx,3
mov dx,0
jmp division
done:
mov cx,ax
mov ax,dx
jmp end
division:
sub ax,bx
inc dx
cmp ax,bx
jge division
jmp done
end:
original source co...
emulator: noname.bin_
file math debug view external virtual devices virtual drive help
Load
reload
step back
single step
run
step delay ms: 0
registers
0100:002E
0100:002E
screen source reset aux vars debug stack flags
Monday, October 6, 2025
Mon 8:41:13 AM (Local time)
ENG
8:41:13 AM
10/6/2025

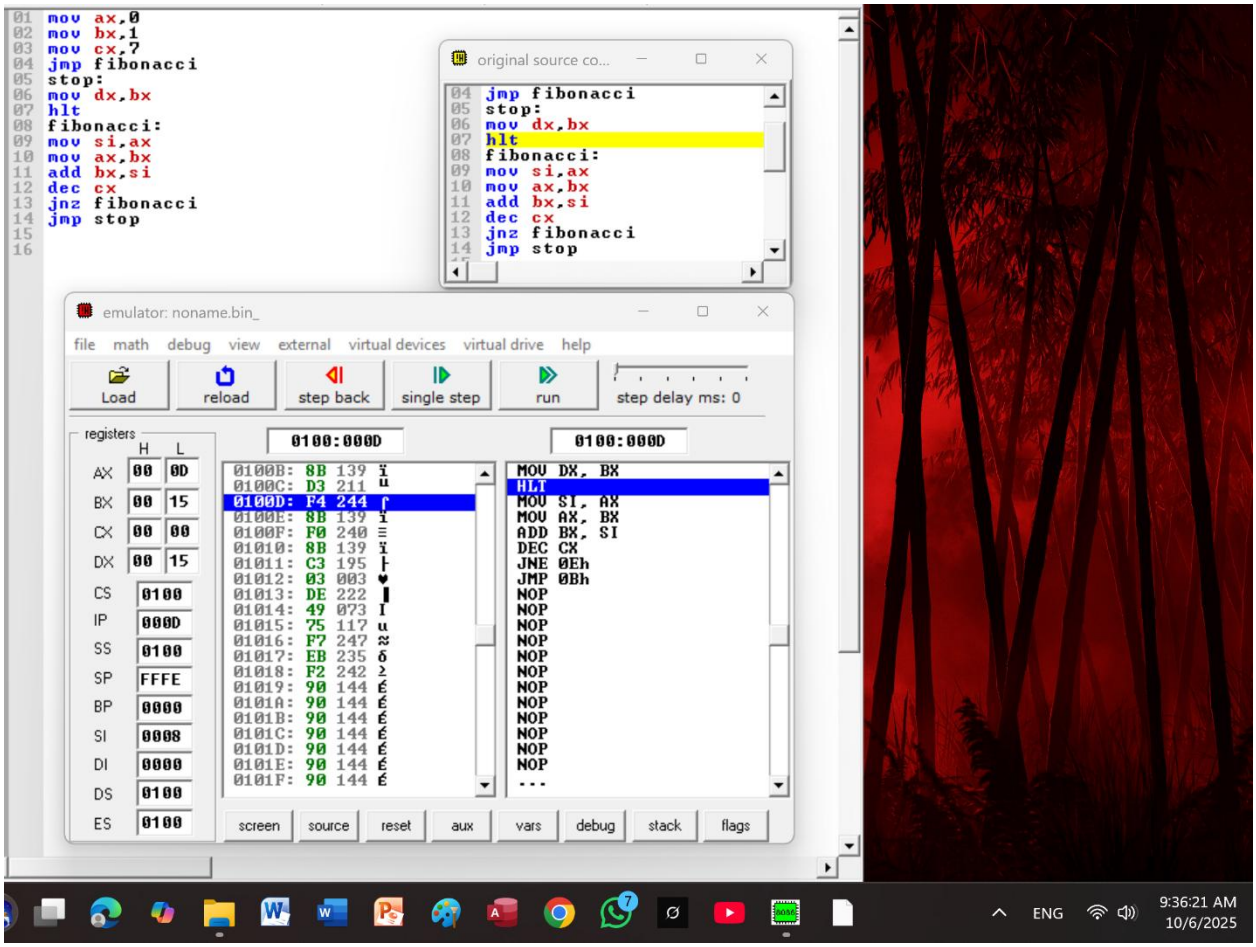
TASK4:


mov ax, 0FFFFh
mov bx,1
mov cx,5
jmp shift_loop
exit:
mov dx,ax
jmp end
shift_loop:
add ax,ax
add bx,bx
dec cx
jnz shift_loop
jmp exit
end:
original source co...
emulator: Babar_65731_T4_L7.bin_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
H L
AX FF E0
BX 00 20
CX 00 00
DX FF E0
CS 0100
IP 002C
SS 0100
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0100
ES 0100
0100:002C
0100:002C
HLT
ADD [BX + SI], AL
screen source reset aux vars debug stack flags
ENG
9:10:34 AM
10/6/2025

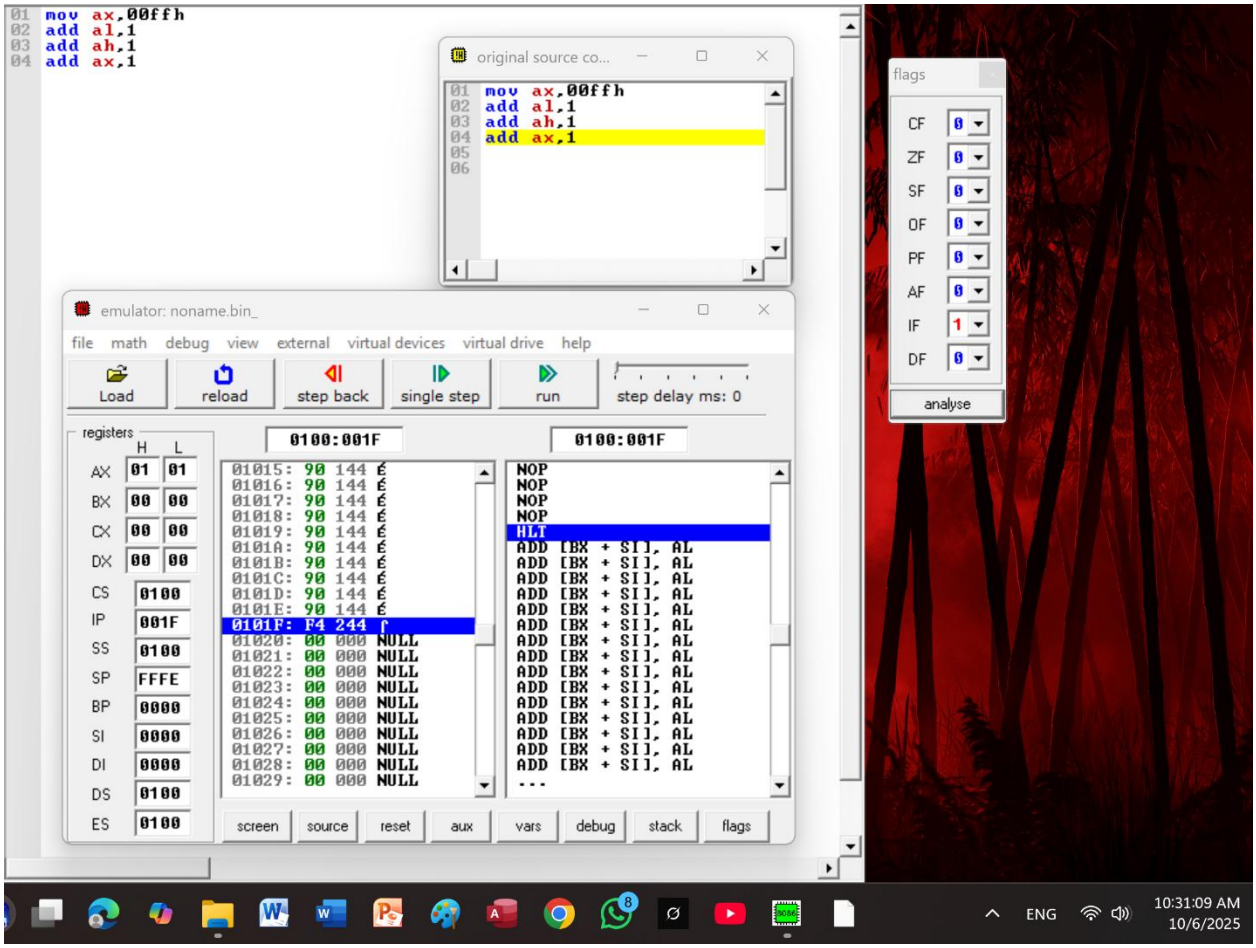
TASK5:


mov ax,9
mov bx,ax
dec bx
jmp check
process:
dec ax
add cx,ax
check:
cmp ax,0
jg process
mov dx,cx
original source co...
emulator: noname.bin_
file math debug view external virtual devices virtual drive help
Load
reload
step back
single step
run
step delay ms: 0
registers
H L
AX 00 00
BX 00 08
CX 00 24
DX 00 24
CS 0100
IP 0026
SS 0100
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0100
ES 0100
0100:0026
0100:0026
01026: F4 244 ô
01027: 00 000 NULL
01028: 00 000 NULL
01029: 00 000 NULL
NOP
NOP
NOP
NOP
NOP
HLT
ADD [BX + SI], AL
screen source reset aux vars debug stack flags
ENG
9:19:04 AM
10/6/2025

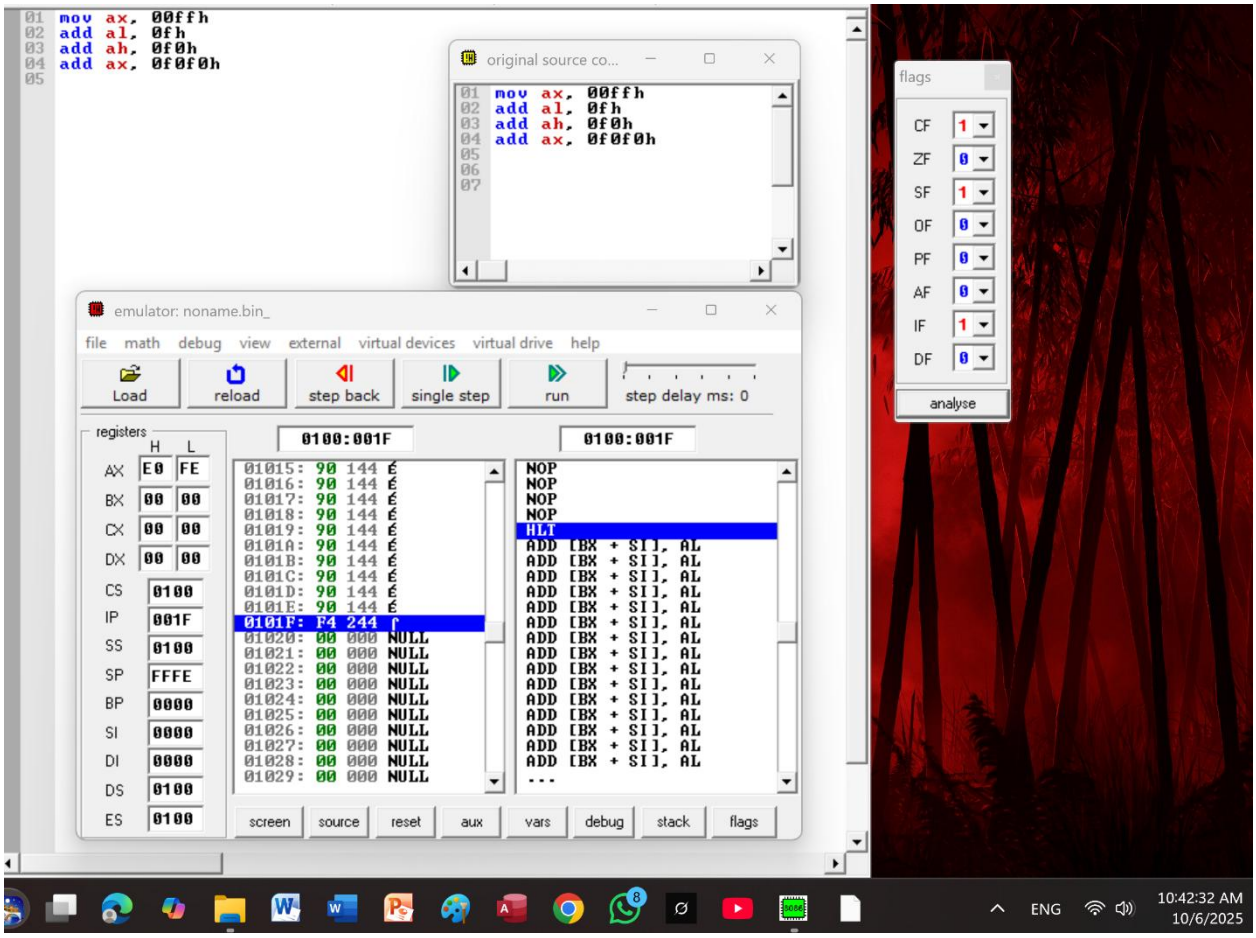
TASK6:


mov ax,0
mov bx,1
mov cx,7
jmp fibonacci
stop:
mov dx,bx
hlt
fibonacci:
mov si,ax
mov ax,bx
add bx,si
dec cx
jnz fibonacci
jmp stop
original source co...
emulator: noname.bin_
file math debug view external virtual devices virtual drive help
Load
reload
step back
single step
run
step delay ms: 0
registers
H L
AX 00 0D
BX 00 15
CX 00 00
DX 00 15
CS 0100
IP 000D
SS 0100
SP FFFE
BP 0000
SI 0008
DI 0000
DS 0100
ES 0100
0100:000D
0100:000D
0100B: 8B 139 ï
0100C: D3 211 ╙
0100D: F4 244 ⌠
0100E: 8B 139 ï
0100F: F0 240 ≡
01010: 8B 139 ï
01011: C3 195 ├
01012: 03 003 ♥
01013: DE 222 ▌
01014: 49 073 I
01015: 75 117 u
01016: F7 247 ≈
01017: EB 235 δ
01018: F2 242 ≥
01019: 90 144 É
0101A: 90 144 É
0101B: 90 144 É
0101C: 90 144 É
0101D: 90 144 É
0101E: 90 144 É
0101F: 90 144 É
MOV DX, BX
HLT
MOV SI, AX
MOV AX, BX
ADD BX, SI
DEC CX
JNE 0Eh
JMP 0Bh
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
NOP
---
screen source reset aux vars debug stack flags
ENG
9:36:21 AM
10/6/2025

TASK7:


mov ax,00ffh
add al,1
add ah,1
add ax,1
original source co...
emulator: noname.bin_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
H L
AX 01 01
BX 00 00
CX 00 00
DX 00 00
CS 0100
IP 001F
SS 0100
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0100
ES 0100
0100:001F
0100:001F
01015: 90 144 É
01016: 90 144 É
01017: 90 144 É
01018: 90 144 É
01019: 90 144 É
0101A: 90 144 É
0101B: 90 144 É
0101C: 90 144 É
0101D: 90 144 É
0101E: 90 144 É
0101F: F4 244 ⌠
01020: 00 000 NULL
01021: 00 000 NULL
01022: 00 000 NULL
01023: 00 000 NULL
01024: 00 000 NULL
01025: 00 000 NULL
01026: 00 000 NULL
01027: 00 000 NULL
01028: 00 000 NULL
01029: 00 000 NULL
NOP
NOP
NOP
NOP
HLT
ADD [BX + SI], AL
screen source reset aux vars debug stack flags
flags
CF 0
ZF 0
SF 0
OF 0
PF 0
AF 0
IF 1
DF 0
analyse
ENG
10:31:09 AM
10/6/2025

TASK8:


mov ax, 00ffh
add al, 0fh
add ah, 0f0h
add ax, 0f0f0h
original source co...
emulator: noname.bin_
file math debug view external virtual devices virtual drive help
Load reload step back single step run step delay ms: 0
registers
H L
AX E0 FE
BX 00 00
CX 00 00
DX 00 00
CS 0100
IP 001F
SS 0100
SP FFFE
BP 0000
SI 0000
DI 0000
DS 0100
ES 0100
0100:001F
0100:001F
01015: 90 144 É
01016: 90 144 É
01017: 90 144 É
01018: 90 144 É
01019: 90 144 É
0101A: 90 144 É
0101B: 90 144 É
0101C: 90 144 É
0101D: 90 144 É
0101E: 90 144 É
0101F: F4 244 ⌠
01020: 00 000 NULL
01021: 00 000 NULL
01022: 00 000 NULL
01023: 00 000 NULL
01024: 00 000 NULL
01025: 00 000 NULL
01026: 00 000 NULL
01027: 00 000 NULL
01028: 00 000 NULL
01029: 00 000 NULL
NOP
NOP
NOP
NOP
HLT
ADD [BX + SI], AL
screen source reset aux vars debug stack flags
flags
CF 1
ZF 0
SF 1
OF 0
PF 0
AF 0
IF 1
DF 0
analyse
ENG
10:42:32 AM
10/6/2025